



Contents lists available at ScienceDirect

Annual Reviews in Control

journal homepage: www.elsevier.com/locate/arcontrol

Review

Tutorial on dynamic analysis of the Costas loop[☆]R.E. Best^a, N.V. Kuznetsov^{b,c,*}, G.A. Leonov^{b,d}, M.V. Yuldashev^b, R.V. Yuldashev^b^a Best Engineering, Oberwil, Switzerland^b Saint-Petersburg State University, Russia^c University of Jyväskylä, Finland^d Institute of Problems of Mechanical Engineering RAS, Russia

ARTICLE INFO

Article history:

Received 29 August 2016

Accepted 29 August 2016

Available online xxx

Keywords:

Costas loop

Nonlinear analysis

PLL-based circuits

Simulation

Pull-in range

Hold-in range

Lock-in range

ABSTRACT

Costas loop is a classical phase-locked loop (PLL) based circuit for carrier recovery and signal demodulation. The PLL is an automatic control system that adjusts the phase of a local signal to match the phase of the input reference signal. This tutorial is devoted to the dynamic analysis of the Costas loop. In particular the acquisition process is analyzed. Acquisition is most conveniently described by a number of frequency and time parameters such as lock-in range, lock-in time, pull-in range, pull-in time, and hold-in range. While for the classical PLL equations all these parameters have been derived (many of them are approximations, some even crude approximations), this has not yet been carried out for the Costas loop. It is the aim of this analysis to close this gap. The paper starts with an overview on mathematical and physical models (exact and simplified) of the different variants of the Costas loop. Then equations for the above mentioned key parameters are derived. Finally, the lock-in range of the Costas loop for the case where a lead-lag filter is used for the loop filter is analyzed.

© 2016 International Federation of Automatic Control. Published by Elsevier Ltd. All rights reserved.

1. Introduction

Costas loop is a classical phase-locked loop (PLL) based circuit for carrier recovery and signal demodulation (Costas, 1962; Waters, 1982). The PLL is an automatic control system, which is designed to generate an electrical signal (voltage), the frequency of which is automatically tuned to the frequency of the input (reference) signal. Various PLL based circuits are widely used in modern telecommunications, computer architectures, electromechanical systems (see, e.g. Best, 2007; Kobayashi, Hara, & Tanaka, 1990; Lazzari, Parma, De Marco, & Bittanti, 2015). Nowadays among the applications of Costas loop there are Global Positioning Systems (see, e.g., Kaplan & Hegarty, 2006), wireless communication (see, e.g., Rohde, Whitaker, & Bateman, 2000) and others (Bakshi & Godse, 2010; Couch, 2007; Proakis & Salehi, 2008; Sidorkina, Sizykh, Shakhtarin, & Shevtsev, 2016; Stephens, 2002).

Dynamic behavior of the PLL and the Costas loop has been described extensively in the literature (Best, 2007; Best, Kuznetsov, Leonov, Yuldashev, & Yuldashev, 2014; Bianchi, 2005;

Bizzarri, Brambilla, & Gajani, 2012; Cahn, 1977; Gardner, 1979; Kaplan & Hegarty, 2006; Kuznetsov et al., 2014a; 2012; Kuznetsov, Leonov, Yuldashev, & Yuldashev, 2014d; Leonov, Kuznetsov, Yuldashev, & Yuldashev, 2015b; Rantzer, 2001; Rohde et al., 2000; Simon & Lindsey, 1977), and a number of key parameters has been defined that describe its lock-in and lock-out characteristics. When the PLL is initially out of lock, two different types of acquisition processes can occur, either the so-called lock-in process or the so-called pull-in process. The first of those is a fast process, i.e. the acquisition takes place within at most one beat note of the difference between reference frequency ω_1 and initial VCO (Voltage Controlled Oscillator) frequency ω_2 , cf. Fig. 1 for signal denotations¹. The frequency difference for which such a fast acquisition process takes place corresponds to the lock-in range $\Delta\omega_L$, and the duration of the locking process is called lock time T_L . When the difference between reference and VCO frequency is larger than the lock-in range but less than the pull-in range $\Delta\omega_p$, a slow acquisition process occurs. The time required to get acquisition is called pull-in time T_p . In case of the PLL all these acquisition parameters can be approximated by characteristic parameters of the PLL, i.e. from natural frequency ω_n and damping factor ζ .

[☆] This work was supported by Russian Science Foundation (project 14-21-00041) and Saint-Petersburg State University

* Corresponding author.

E-mail addresses: rolandbest@aol.com (R.E. Best), nkuznetsov239@gmail.com (N.V. Kuznetsov), g.leonov@spbu.ru (G.A. Leonov), maratyv@gmail.com (M.V. Yuldashev), renatyv@gmail.com (R.V. Yuldashev).

¹ Non-sinusoidal signals in PLL-based circuits are considered in Kuznetsov, Leonov, Yuldashev, and Yuldashev (2011); Leonov, Kuznetsov, Yuldashev, and Yuldashev (2012)

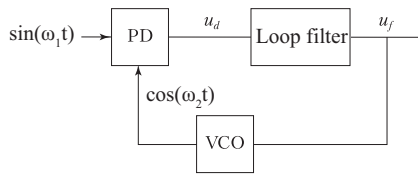


Fig. 1. Block diagram of a PLL.

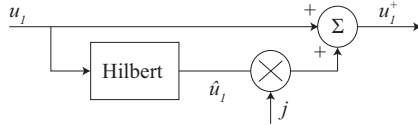


Fig. 2. Generation of the pre-envelope signal using Hilbert transformer.

To the authors knowledge such acquisition parameters have not been analytically derived for the different types of Costas loops. It seems that most authors only described the static properties of the Costas loop such as the derivation of the phase error in the locked state and the like. Based on methods developed earlier for the PLL, the authors could now derive similar expressions for all relevant acquisition parameters of the Costas loop. This enables the designer to determine the lock-in and pull-in ranges, and to estimate the duration of the corresponding processes.

Because the systems considered are highly nonlinear, exact computation of such parameters is very difficult or even impossible. Therefore it is necessary to introduce a number of simplifications. This implies that the obtained results are only approximations, in some cases rather crude approximations.

As will be shown in the following sections there are different types of Costas loops. The first of these loops has been described by J. Costas in 1956 (Costas, 1956) and was primarily used to demodulate amplitude-modulated signals with suppressed carrier (DSB-AM). The same circuit was used later for the demodulation of BPSK signals (binary phase shift keying) (Proakis & Salehi, 2008). With the advent of QPSK (quadrature phase shift keying) this Costas loop was extended to demodulate QPSK signals as well. These two types of Costas loop operated with real signals. In case of BPSK, the input signal $u_1(t)$ is a sine carrier that was phase modulated by a binary signal, i.e.

$$u_1(t) = m_1(t) \sin(\omega_1 t), \quad (1)$$

where ω_1 is the (radian) carrier frequency, and $m_1(t)$ can have two different values, either $+1$ or -1 , or two arbitrary equal and opposite values $+c$ and $-c$, where c can be any value. In the case of QPSK, two quadrature carriers are modulated by two modulating signals, i.e.

$$u_1(t) = m_1(t) \cos(\omega_1 t) + m_2(t) \sin(\omega_1 t), \quad (2)$$

where m_1 and m_2 can both have two equal and opposite values $+c$ and $-c$. It is obvious that in both cases the input signal is a real quantity. In the following these two types of Costas loop will be referred to as “conventional Costas loops”.

Much later, Costas loops have been developed that operate not on real signals, but on pre-envelope signals (Tretter, 2007). These types of Costas loops will be referred to as “modified Costas loop” in the following sections. The block diagram shown in Fig. 2 explains how the pre-envelope signal is obtained. The real input signal $u_1(t)$ is applied to the input of a Hilbert transformer [2], [5]. The output of the Hilbert transformer $\hat{u}_1(t)$ is considered as the imaginary part of the pre-envelope signal, i.e the pre-envelope signal is obtained from

$$u_1^+(t) = u_1(t) + j\hat{u}_1(t).$$

The Costas loops operating with pre-envelope signals will be referred to as “modified Costas loops”, cf. Sections 4 and 5.

Because there are different types of Costas loops the acquisition parameters must be derived separately for each of these types. This will be performed in the following sections. In order to see how good or bad the obtained approximations are, we will develop Simulink models for different types of Costas loops and compare the results of the simulation with those predicted by theory.

1.1. Classical mathematical models of the Costas loops

1.1.1. BPSK Costas loop

The operation of the Costas loop is considered first in the locked state with zero phase difference (see Fig. 3), hence the frequency of the carrier is identical with the frequency of the VCO.

By (1) the input signal $u_1(t)$ is the product of a transferred binary data and the harmonic carrier $\sin(\omega t)$ with a high frequency ω . Since the Costas loop is considered to be locked, the VCO orthogonal output signals are synchronized with the carrier (i.e. there is no phase difference between these signals). The input signal is multiplied (multiplier block \otimes) by the corresponding VCO signal on the upper branch and by the VCO signal, shifted by 90° , on the lower branch. Therefore on the multipliers’ outputs one has $I_1(t) = m_1(t) - m_1(t) \cos(2\omega t)$, $Q_1(t) = m_1(t) \sin(2\omega t)$.

Consider the low-pass filters (LPF) operation.

Assumption 1. Signals components, whose frequency is about twice the carrier frequency, do not affect the synchronization of the loop (since they are suppressed by the low-pass filters).

Assumption 2. Initial states of the low-pass filters do not affect the synchronization of the loop (since for the properly designed filters, the impact of filter’s initial state on its output decays exponentially with time).

Assumption 3. The data signal $m_1(t)$ does not affect the synchronization of the loop.

Assumptions 1, 2, and 3 together lead to the concept of so-called *ideal low-pass filter*. Ideal low-pass filter completely eliminates all frequencies above the cutoff frequency (Assumption 1) and passes all frequencies below cut-off frequency unchanged (Assumptions 2,3). In the classic engineering theory of the Costas loop it is assumed that the low-pass filters LPF are ideal low-pass filters.²

Since in Fig. 3 the loop is in lock, i.e. the transient process is over and the synchronization is achieved, by Assumptions 1,2, and 3 for the outputs $I_2(t)$ and $Q_2(t)$ of the low-pass filters LPF one has $I_2(t) = m_1(t)$, $Q_2(t) = 0$. Thus, the upper branch works as a demodulator and the lower branch works as a phase-locked loop.

Since after a transient process there is no phase difference, a control signal at the input of VCO, which is used for VCO frequency adjustment to the frequency of input carrier signal, has to be zero: $u_d(t) = 0$. In the general case when the carrier frequency ω and a free-running frequency ω_{free} of the VCO are different, after a transient processes the control signal at the input of VCO has to be nonzero constant: $u_d(t) = const$, and a constant phase difference θ_e may remain.

Consider the Costas loop before synchronization (see Fig. 4). Here the phase difference with $\theta_e(t) = \theta_1(t) - \theta_2(t)$ varies over time, because the loop has not yet acquired lock (frequencies or phases of the carrier and VCO are different).

² Note that Assmptions 1–3 may not be valid and require rigorous justification (Best et al., 2015; Kuznetsov et al., 2015a)

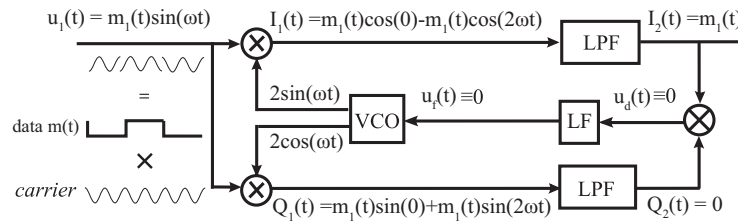


Fig. 3. Costas loop is locked (the case of equal phases of input carrier and free running VCO output): there is no phase difference.

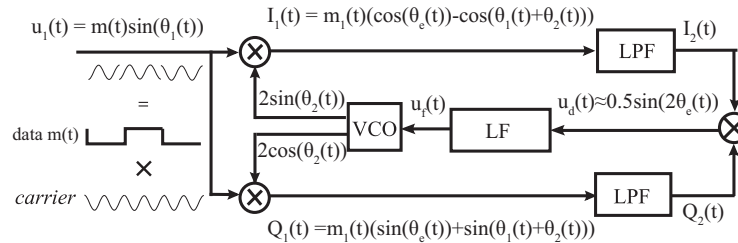


Fig. 4. Costas loop is out of lock: there is time-varying phase difference.

In this case, using Assumption 1, the signals $I_1(t)$ and $Q_1(t)$ can be approximated as

$$I_1(t) \approx m_1(t) \cos(\theta_e(t)), \quad Q_1(t) \approx m_1(t) \sin(\theta_e(t)). \quad (3)$$

Approximations (3) depend on the phase difference of signals, i.e. two multiplier blocks (\otimes) on the upper and lower branches operate as phase detectors. The obtained expressions (3) with $m_1(t) \equiv 1$ coincide with well-known (see, e.g., Best, 2007; Viterbi, 1966) phase detector characteristic of the classic PLL with multiplier/mixer phase-detector for sinusoidal signals.

By Assumptions 2 and 3 the low-pass filters outputs can be approximated as

$$I_2(t) \approx m_1(t) \cos(\theta_e(t)), \quad Q_2(t) \approx m_1(t) \sin(\theta_e(t)). \quad (4)$$

Since $m_1^2(t) \equiv 1$, the input of the loop filter (LF) is as follows

$$u_d(t) = I_2(t)Q_2(t) \approx \varphi(\theta_e(t)) = \frac{m_1(t)^2}{2} \sin(2\theta_e(t)). \quad (5)$$

Such an approximation is called a *phase detector characteristic of the Costas loop*.

Since an ideal low-pass filter is hardly realized, its use in the mathematical analysis requires additional justification. Thus, the impact of the low-pass filters on the lock acquisition process must be studied rigorously.

The relation between the input $u_d(t)$ and the output $u_f(t)$ of the loop filter has the form

$$\dot{x} = Ax + bu_d(t), \quad u_f(t) = c^*x + hu_d(t), \quad (6)$$

where A is a constant matrix, the vector $x(t)$ is the loop filter state, b, c are constant vectors, h is a number. The filter transfer function has the form:

$$H(s) = -c^*(A - sI)^{-1}b + h. \quad (7)$$

The control signal $u_f(t)$ is used to adjust the VCO frequency to the frequency of the input carrier signal

$$\dot{\theta}_2(t) = \omega_2(t) = \omega_{free} + K_0 u_f(t). \quad (8)$$

Here ω_{free} is the free-running frequency of the VCO and K_0 is the VCO gain. The solution of (6) with initial data $x(0)$ (the loop filter output for the initial state $x(0)$) is as follows

$$u_f(t, x(0)) = \alpha_0(t, x(0)) + \int_0^t \gamma(t - \tau) \varphi(\theta_e(\tau)) d\tau + hu_d(t). \quad (9)$$

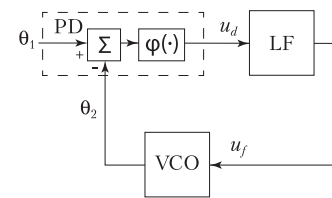


Fig. 5. Phase model of Costas loop.

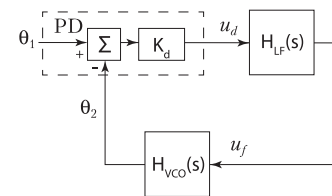


Fig. 6. Linear model of Costas loop.

where $\gamma(t - \tau) = c^*e^{A(t-\tau)}b + h$ is the impulse response of the loop filter and $\alpha_0(t, x(0)) = c^*e^{At}x(0)$ is the zero input response of the loop filter, i.e. when the input of the loop filter is zero.

Assumption 4. (Analog of Assumption 2). Zero input response of loop filter $\alpha_0(t, x(0))$ does not affect the synchronization of the loop (one of the reasons is that $\alpha_0(t, x(0))$ is an exponentially damped function for a stable matrix A).

Consider a constant frequency of the input carrier:

$$\dot{\theta}_1(t) = \omega_1(t) \equiv \omega_1, \quad (10)$$

and introduce notation

$$\Delta\omega_0 = \omega_1 - \omega_{free}. \quad (11)$$

Then Assumption 4 allows one to obtain the classic mathematical model of PLL-based circuit in signal's phase space (see Fig. 5):

$$\dot{\theta}_e = \Delta\omega_0 - K_0 \int_0^t \gamma(t - \tau) \varphi(\theta_e(\tau)) d\tau - K_0 h \varphi(\theta_e(t)). \quad (12)$$

For the locked state a linear PLL model can be derived, which is shown in Fig. 6. This model is useful for approximation of hold-in range.

In the locked state both reference and VCO frequencies are approximately the same, hence the input of the lowpass filter is a

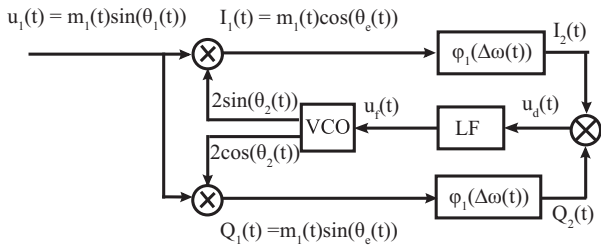


Fig. 7. Model of Costas loop with delays.

very low frequency signal. Therefore the lowpass filter can be ignored when setting up the linear model of the Costas loop. The linear model is made up of three blocks, the phase detector PD, the loop filter LF and the VCO. In digital Costas loops the VCO is replaced by a DCO (digital controlled oscillator). This will be discussed in later sections. For these building blocks the transfer functions are now defined as follows.

Phase detector (PD). In the locked state, the phase error θ_e is very small so by (5) we can write

$$u_d(t) \approx m_1^2(t)\theta_e = K_d\theta_e \quad (13)$$

with K_d called phase detector gain.

$$H_{PD}(s) = \frac{U_d(s)}{\Theta_e(s)} = K_d. \quad (14)$$

Note that the uppercase symbols are Laplace transforms of the corresponding lower case signals.

Loop filter (LF). For the loop filter we choose a PI (proportional + integral) filter whose transfer function has the form

$$H_{LF}(s) = \frac{U_f(s)}{U_d(s)} = \frac{1 + s\tau_2}{s\tau_1}. \quad (15)$$

This filter type is the preferred one because it offers superior performance compared with lead-lag or lag filters.

VCO. The transfer function of the VCO is given by

$$H_{VCO}(s) = \frac{\Theta_2(s)}{U_f(s)} = \frac{K_0}{s} \quad (16)$$

where K_0 is called VCO gain.

Consider another nonlinear model of Costas loop in Fig. 7 (delay model).

Here we use Assumptions 1–3 (initial states of filters are omitted, double-frequency terms are completely filtered by LPFs, and $m_1(t)$ doesn't affect synchronization) and filters LPFs are replaced by the corresponding phase-delay blocks $\varphi_1(\theta_e(t)) = \varphi_1(\Delta\omega(t))$. Outputs of low-pass filters are

$$\begin{aligned} I_2(t) &= \cos(\theta_e(t) + \varphi_1(\dot{\theta}_e(t))), \\ Q_2(t) &= \sin(\theta_e(t) + \varphi_1(\dot{\theta}_e(t))), \end{aligned} \quad (17)$$

where

$$\varphi_1(\omega) = \arg(H_{LPF}(j\omega)). \quad (18)$$

Then after multiplication of $I_2(t)$ and $Q_2(t)$ we have

$$u_d(t) = I_2(t)Q_2(t) = \frac{1}{2} \sin(2\theta_e(t) + 2\varphi_1(\dot{\theta}_e(t))) \quad (19)$$

and the output $u_f(t)$ of the loop filter (15) satisfies the following equations

$$\begin{aligned} \dot{x} &= \frac{1}{2} \sin(2\theta_e(t) + 2\varphi_1(\dot{\theta}_e(t))), \\ u_f(t) &= \frac{1}{\tau_1}x + \frac{\tau_2}{2\tau_1} \sin(2\theta_e(t) + 2\varphi_1(\dot{\theta}_e(t))). \end{aligned} \quad (20)$$

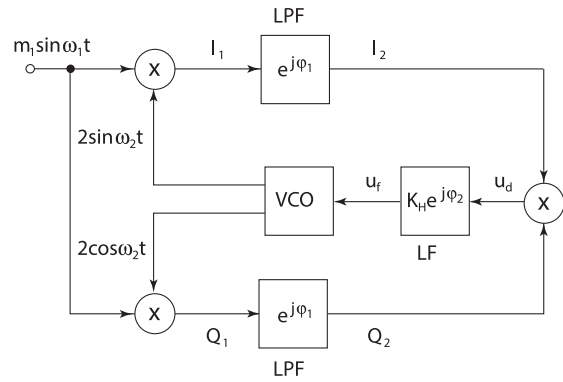


Fig. 8. Model of the Costas loop with delays in complex exponent form.

Equations of Costas loop in this case take the form:

$$\begin{aligned} \dot{x} &= \frac{1}{2} \sin(2\theta_e + 2\varphi_1(\dot{\theta}_e)), \\ \dot{\theta}_e &= \Delta\omega_0 - K_0 \left(\frac{1}{\tau_1}x + \frac{\tau_2}{2\tau_1} \sin(2\theta_e + 2\varphi_1(\dot{\theta}_e)) \right). \end{aligned} \quad (21)$$

For LPF transfer functions

$$H_{LPF}(s) = \frac{1}{1 + s/\omega_3} \quad (22)$$

phase shift is equal to $\varphi_1(\dot{\theta}_e) = -\arctan(\dot{\theta}_e/\omega_3)$. Therefore (21) is equal to the following system

$$\begin{aligned} \dot{x} &= \frac{1}{2} \sin(2\theta_e - 2\arctan(\dot{\theta}_e/\omega_3)), \\ \dot{\theta}_e &= \Delta\omega_0 - \frac{K_0}{\tau_1}x - \frac{K_0\tau_2}{2\tau_1} \sin(2\theta_e - 2\arctan(\dot{\theta}_e/\omega_3)), \end{aligned} \quad (23)$$

where

$$\arctan(\dot{\theta}_e/\omega_3) \in \left(-\frac{\pi}{2}, \frac{\pi}{2}\right)$$

Eq. (23) is hard to analyze both numerically and analytically, however this model is still useful. In the following discussion it is used to approximate pull-in range and pull-in time. For this purpose we need to simplify delay model shown in Fig. 7. Consider block diagram in Fig. 8.

The lowpass filters (LPF) used in both I and Q branches are assumed to be first order filters having transfer function (15). As will be demonstrated later the corner frequency of these filters must be chosen such that the data signal I is recovered with sufficient accuracy, i.e. the corner frequency ω_3 must be larger than the symbol rate. Typically it is chosen twice the symbol rate, i.e. $f_3 = 2f_s$ with $f_s =$ symbol rate and $f_3 = \omega_3/2\pi$. The output signal I_1 of the multiplier in the I branch consists of two terms, one having the sum frequency $\omega_1 + \omega_2$ and one having the difference frequency $\omega_1 - \omega_2$. Because the sum frequency term will be suppressed by the lowpass filter, only the difference term is considered. The same holds true for signal Q_1 in the Q branch. It will show up that the range of difference frequencies is markedly below the corner frequency ω_3 of the lowpass filter. Hence the filter gain will be nearly 1 for the frequencies of interest. As will also be shown later the phase at frequency $\Delta\omega = \omega_1 - \omega_2$ cannot be neglected. The lowpass filter is therefore represented as a delay block whose transfer function has the value $\exp(j\varphi_1)$, where φ_1 is the phase at frequency $\Delta\omega$. The delayed signals I_2 and Q_2 are now multiplied by the product block at the right in the block diagram. Consequently the output signal $u_d(t)$ of this block will have a frequency of $2\Delta\omega$. This signal is now applied to the input of the loop filter LF. Its transfer function has been defined in (15). The corner frequency

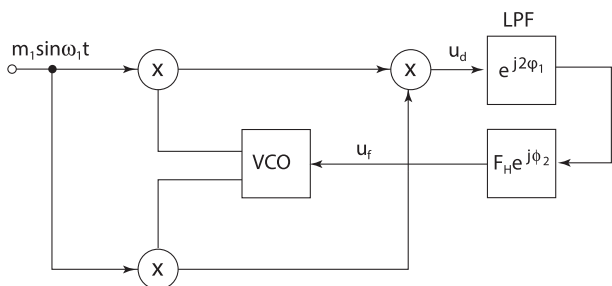


Fig. 9. Modified model of Costas loop, reversed order of blocks.

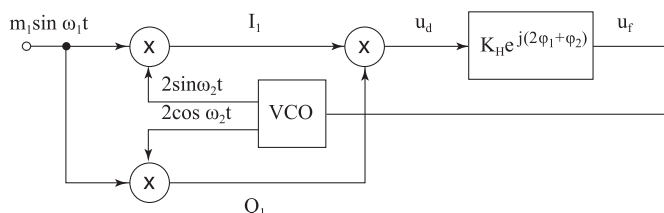


Fig. 10. Modified model of Costas loop, concatenated blocks.

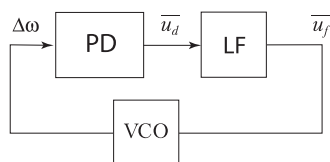


Fig. 11. Nonlinear model of Costas loop for computation of pull-in time.

of this filter is $\omega_c = 1/\tau_2$. Because the phase of the loop filter cannot be neglected, it is represented as a delay block characterized by

$$H_{LF}(2\Delta\omega) = K_H \exp(j\varphi_2), \quad (24)$$

where φ_2 is the phase of the loop filter at frequency $2\Delta\omega$.

The analysis of dynamic behavior becomes easier when the order of some blocks in Fig. 8 is reversed (see Fig. 9), i.e. when we put the multiplying block before the lowpass filter.

Because the frequency of signal $u_d(t)$ in Fig. 8 is twice the frequency of the signals I_2 and Q_2 , the phase shift created by the lowpass filter at frequency $2\Delta\omega$ is now twice the phase shift at frequency $\Delta\omega$. The LPF is therefore represented here by a delay block having transfer function $\exp(2j\varphi_1)$.

We can simplify the block diagram even more by concatenating the lowpass filter and loop filter blocks. The resulting block delays the phase by $\varphi_{tot} = 2\varphi_1 + \varphi_2$. This is shown in Fig. 10. The output signal $u_f(t)$ of this delay block now modulates the frequency generated by the VCO.

To compute pull-in time we need to consider Costas loop model in Fig. 5 with averaged signals of phase detector output u_d and filter output u_f (see Fig. 11).

The model is built from three blocks. The first of these is labeled “phase-frequency detector”. We have seen that in the locked state the output of the phase detector depends on the phase error θ_e . In the unlocked state, however, the average phase detector output signal \bar{u}_d is a function of frequency difference as will be shown in next section (Eq. (68)), hence it is justified to call that block “phase-frequency detector”. As we will recognize the pull-in process is a slow one, i.e. its frequency spectrum contains low frequencies only that are below the corner frequency ω_c of the loop filter, cf. Eq. (15). The loop filter can therefore be modeled as a

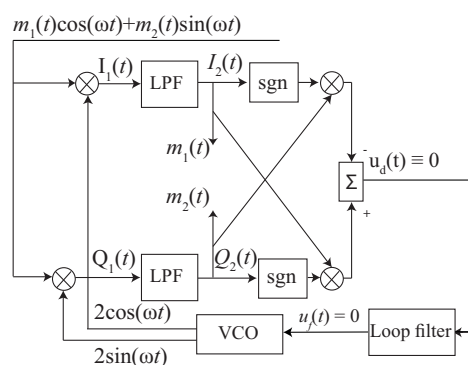


Fig. 12. QPSK Costas loop after transient process.

simple integrator with transfer function

$$H_{LF}(s) \approx \frac{1}{s\tau_1}. \quad (25)$$

Therefore

$$\bar{u}_f(t) = \frac{1}{\tau_1} \int_0^t \bar{u}_d(\tau) d\tau, \quad (26)$$

The frequency ω_2 of the VCO output signal is defined as

$$\omega_2 = \omega_{free} + K_0 \bar{u}_f, \quad (27)$$

where ω_{free} is the free running frequency and K_0 is the VCO gain. Now we define the instantaneous frequency difference $\Delta\omega$ as

$$\Delta\omega = \omega_1 - \omega_2. \quad (28)$$

Substituting (11) and (28) into (27) finally yields

$$\Delta\omega = \Delta\omega_0 - K_0 \bar{u}_f. \quad (29)$$

1.1.2. QPSK Costas loop

Consider QPSK Costas loop operation (see Fig. 12) for the sinusoidal carrier and VCO in lock state for the same initial frequencies $\omega_1 = \omega_2 = \omega$.

By (2), the input QPSK signal has the form

$$m_1(t) \cos(\omega t) + m_2(t) \sin(\omega t),$$

where $m_{1,2}(t) = \pm 1$ is the transmitted data, $\sin(\omega t)$ and $\cos(\omega t)$ are sinusoidal carriers, $\theta_1(t) = \omega t$ is phase of input signal. The outputs of the VCO are $2\cos(\omega t)$ and $2\sin(\omega t)$.

After multiplication of VCO signals and the input signal by multiplier blocks (\otimes) on the upper I branch one has

$$I_1(t) = 2 \cos(\omega t) (m_1(t) \cos(\omega t) + m_2(t) \sin(\omega t)).$$

On the lower branch the output signal of VCO is multiplied by the input signal:

$$Q_1(t) = 2 \sin(\omega t) (m_1(t) \cos(\omega t) + m_2(t) \sin(\omega t)).$$

Here from an engineering point of view, the high-frequency terms $\cos(2\omega t)$ and $\sin(2\omega t)$ are removed by ideal low-pass filters LPFs (see Assumption 1 in previews section). In this case, the signals $I_2(t)$ and $Q_2(t)$ on the upper and lower branches can be approximated as

$$\begin{aligned} I_2(t) &\approx m_1(t) \cos(0) + m_2(t) \sin(0) = m_1(t), \\ Q_2(t) &\approx -m_1(t) \sin(0) + m_2(t) \cos(0) = m_2(t). \end{aligned} \quad (30)$$

Apart from considered case there are two possible cases: 1) the frequencies are different or 2) the frequencies are the same but there is a constant phase difference. Consider Costas loop before

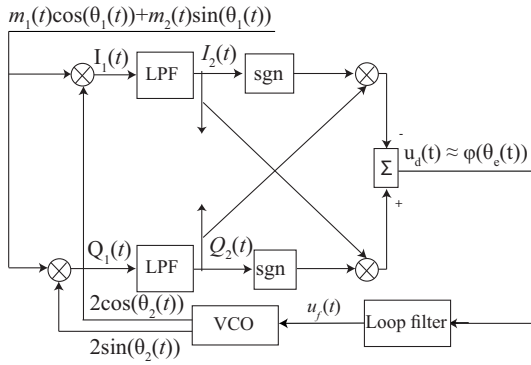


Fig. 13. QPSK Costas loop is out of lock, there is nonzero phase difference.

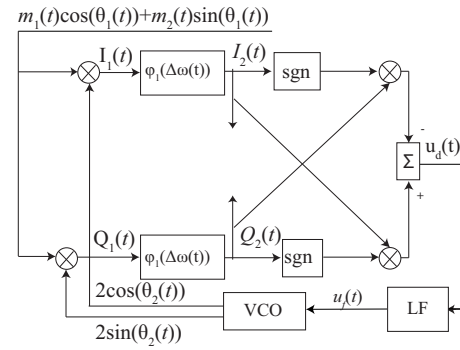


Fig. 15. Model of QPSK Costas loop with delays.

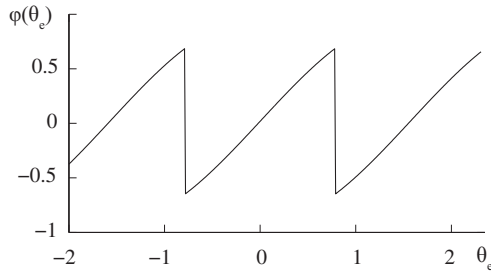


Fig. 14. $\varphi(\theta_e)$.

synchronization (see Fig. 13) in the case when the phase of the input carrier $\theta_1(t)$ and the phase of VCO $\theta_2(t)$ are different:

$$\theta_e(t) = \theta_1(t) - \theta_2(t) \neq \text{const.} \tag{31}$$

In this case, using Assumption 1, the signals $I_2(t)$ and $Q_2(t)$ on the upper and lower branches can be approximated as

$$\begin{aligned} I_2(t) &\approx m_1(t) \cos(\theta_e(t)) + m_2(t) \sin(\theta_e(t)), \\ Q_2(t) &\approx -m_1(t) \sin(\theta_e(t)) + m_2(t) \cos(\theta_e(t)). \end{aligned} \tag{32}$$

After the filtration, both signals, $I_1(t)$ and $Q_1(t)$, pass through the limiters (sgn blocks). Then the outputs of the limiters $\text{sign}(I_2(t))$ and $\text{sign}(Q_2(t))$ are multiplied with $Q_2(t)$ and $I_2(t)$, respectively. By Assumption 2 and corresponding formula (32), the difference of these signals

$$u_d(t) = -Q_2(t) \text{sign}(I_2(t)) + I_2(t) \text{sign}(Q_2(t)) \tag{33}$$

can be approximated as

$$u_d(t) \approx \varphi(\theta_e(t)) = \begin{cases} 2m \sin(\theta_e(t)), & -\frac{\pi}{4} < \theta_e(t) < \frac{\pi}{4}, \\ -2m \cos(\theta_e(t)), & \frac{\pi}{4} < \theta_e(t) < \frac{3\pi}{4}, \\ -2m \sin(\theta_e(t)), & \frac{3\pi}{4} < \theta_e(t) < \frac{5\pi}{4}, \\ 2m \cos(\theta_e(t)), & \frac{5\pi}{4} < \theta_e(t) < -\frac{\pi}{4}, \end{cases} \tag{34}$$

with $m = |m_1| = |m_2|$. Here $\varphi(\theta_e(t))$ is a piecewise-smooth function³ shown in Fig. 14.

The resulting signal $\varphi(t)$, after the filtration by the loop filter, forms the control signal $u_j(t)$ for the VCO.

To derive mathematical model in the signal space describing physical model of QPSK Costas loop one takes into account (6) and

³ It should be noted, that function $\varphi(\theta_e(t))$ depends on $m_{1,2}$ at the points $\theta_e = \pm \frac{\pi}{4}, \pm \frac{3\pi}{4}$.

(8):

$$\begin{aligned} \dot{x}_1 &= A_1 x_1 + 2b_1 \cos(\omega_1 t - \theta_e) (m_1(t) \cos(\omega_1 t) + m_2(t) \sin(\omega_1 t)), \\ \dot{x}_2 &= A_2 x_2 + 2b_2 \sin(\omega_1 t - \theta_e) (m_1(t) \cos(\omega_1 t) + m_2(t) \sin(\omega_1 t)), \\ \dot{x} &= Ax + b(\text{sign}(c_2^* x_2)(c_1^* x_1) - \text{sign}(c_1^* x_1)(c_2^* x_2)), \\ \dot{\theta}_e &= \Delta\omega_0 - K_0(c^* x) - K_0 h(\text{sign}(c_2^* x_2)(c_1^* x_1) - \text{sign}(c_1^* x_1)(c_2^* x_2)). \end{aligned} \tag{35}$$

However Eq. (35) are nonlinear and nonautonomous with discontinuous right-hand side, which are extremely hard to investigate. Therefore, the study of (35) is outside of the scope of this work.

To derive linear model, we consider (34) and the corresponding Fig. 14. The curve looks like a “chopped” sine wave. The Costas loop can get locked at four different values of θ_e , i.e. with $\theta_e = 0, \pi/2, \pi$, or $3\pi/2$. To simplify the following analysis, we can define the phase error to be zero wherever the loop gets locked. Moreover, in the locked state the phase error is small, so we can write

$$u_d \approx 2m\theta_e = K_d \theta_e, \tag{36}$$

i.e. the output signal of the sum block at the right of Fig. 13 is considered to be the phase detector output signal u_d . The phase detector gain is then as follows

$$K_d = 2m. \tag{37}$$

It is easily seen that the linear model for the locked state is identical with that of the Costas loop for BPSK, cf. Fig. 6. Because only small frequency differences are considered here, the lowpass filters can be discarded. The transfer functions of the loop filter and the VCO are assumed to be the same as in the case of the Costas loop for BPSK, hence these are given by Eqs. (15) and (16).

Similar to BPSK Costas loop, it is reasonable to consider delay model of QPSK Costas loop (see Fig. 15).

Filters LPFs are replaced by the corresponding phase-delay blocks $\varphi_1(\Delta\omega) = \arg(H_{LPF}(j\omega))$. The outputs of low-pass filters take the form

$$\begin{aligned} I_2(t) &\approx \cos(\theta_e(t) + \varphi_1(\Delta\omega(t))) + \sin(\theta_e(t) + \varphi_1(\Delta\omega(t))), \\ Q_2(t) &\approx -\sin(\theta_e(t) + \varphi_1(\Delta\omega(t))) + \cos(\theta_e(t) + \varphi_1(\Delta\omega(t))). \end{aligned} \tag{38}$$

Then $u_d(t)$ can be approximated as

$$u_d(t) \approx \varphi(\theta_e(t) + \varphi_1(\Delta\omega(t))) = \begin{cases} 2 \sin(\theta_e(t) + \varphi_1(\Delta\omega(t))), & -\frac{\pi}{4} < \theta_e(t) + \varphi_1(\Delta\omega(t)) < \frac{\pi}{4}, \\ -2 \cos(\theta_e(t) + \varphi_1(\Delta\omega(t))), & \frac{\pi}{4} < \theta_e(t) + \varphi_1(\Delta\omega(t)) < \frac{3\pi}{4}, \\ -2 \sin(\theta_e(t) + \varphi_1(\Delta\omega(t))), & \frac{3\pi}{4} < \theta_e(t) + \varphi_1(\Delta\omega(t)) < \frac{5\pi}{4}, \\ 2 \cos(\theta_e(t) + \varphi_1(\Delta\omega(t))), & \frac{5\pi}{4} < \theta_e(t) + \varphi_1(\Delta\omega(t)) < -\frac{\pi}{4}. \end{cases} \tag{39}$$

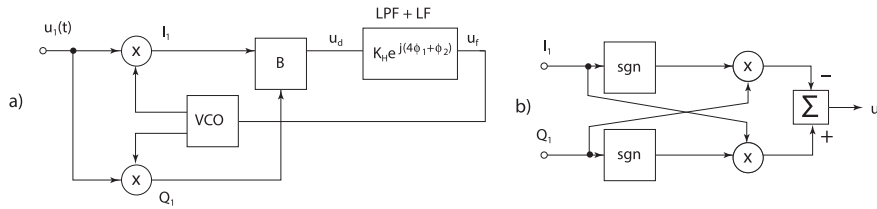


Fig. 16. Nonlinear model of the Costas loop for QPSK with delays in complex exponent form.

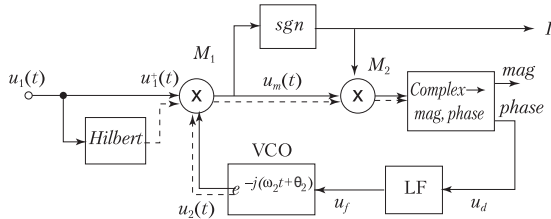


Fig. 17. Block diagram of modified Costas loop for BPSK.

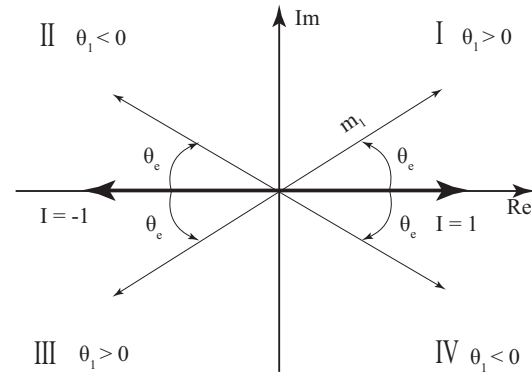


Fig. 18. Representation of phasor $u_m(t)$ in the complex plane.

Consider the loop filter transfer function (15). Equations of delay model of QPSK Costas loop in this case are the following

$$\begin{aligned} \dot{x} &= \varphi(\theta_e(t) + \varphi_1(\dot{\theta}_e)), \\ \dot{\theta}_e &= \Delta\omega_0 - K_0 \left(\frac{1}{\tau_1} x + \frac{\tau_2}{\tau_1} \varphi(\theta_e(t) + \varphi_1(\dot{\theta}_e)) \right). \end{aligned} \quad (40)$$

The nonlinear model of the Costas loop for QPSK is developed on the basis of the nonlinear model which derived for the Costas loop for BPSK, cf. Fig. 10. Here again the order of lowpass filters and the blocks shown at the right of Fig. 12 is reversed. This results in the model shown in Fig. 16a.

In the block labeled “B” the function blocks at the right of Fig. 16a have been integrated, cf. Fig. 16b. The output signal u_d of block B is the “chopped” sine wave as shown in Fig. 3.1. Its fundamental frequency is 4 times the frequency difference $\omega_1 - \omega_2$. The lowpass filters and the loop filter have been concatenated in the block labeled “LPF + LF” at the right of Fig. 16a. Referring to Fig. 12 signals I_1 and Q_1 are passed through lowpass filters. As in the case of the Costas loop for BPSK we assume here again that the difference frequency $\Delta\omega$ is well below the corner frequency ω_3 of the lowpass filters, hence the gain of the lowpass filters is nearly 1 at $\omega = \Delta\omega$. Because the phase shift must not be neglected, we represent the lowpass filter by a delay, i.e. its frequency response at $\omega = \Delta\omega$ is as follows

$$H_{LPF}(\Delta\omega) = \exp(j\varphi_1),$$

where φ_1 is the phase of the lowpass filter. Due to the arithmetic operations in block “B” (cf. Fig. 16) the frequency of the u_d is quadrupled, which implies that the phase shift at frequency $4\Delta\omega$ becomes $4\varphi_1$. The frequency response of the loop filter at $\omega = 4\Delta\omega$ is given by

$$H_{LF}(4\Delta\omega) = \exp(j\varphi_2),$$

where φ_2 is the phase of the loop filter at frequency $\omega = 4\Delta\omega$. Hence the cascade of lowpass filter and loop filter can be modeled by the transfer function $\exp(j[4\varphi_1 + \varphi_2])$ as shown in Fig. 16a.

1.2. Mathematical models of modified Costas loops

1.2.1. Modified Costas loop for BPSK

The block diagram of the modified Costas loop for BPSK is shown in Fig. 17. The input signal is given by

$$u_1(t) = m_1(t) \cos(\omega_1 t + \theta_1),$$

where θ_1 is initial phase. The input signal is first converted into a pre-envelope signal, as explained in Section 1. The output signal of the Hilbert transformer is as follows

$$\hat{u}_1(t) = H[m_1(t) \cos(\omega_1 t + \theta_1)] = m_1(t) \sin(\omega_1 t + \theta_1).$$

Note that because the largest frequency of the spectrum of the data signal $m_1(t)$ is much lower than the carrier frequency ω_1 , the Hilbert transform of the product $H[m_1(t) \cos(\omega_1 t + \theta_1)]$ equals $m_1(t)H[\cos(\omega_1 t + \theta_1)]$ [5]. The pre-envelope signal is obtained now from

$$u_1^+(t) = u_1(t) + j\hat{u}_1(t) = m_1(t) \exp(j[\omega_1 t + \theta_1]). \quad (41)$$

The exponential in Eq. (41) is referred to as a “complex carrier”. In Fig. 17 complex signals are shown as double lines. The solid line represents the real part, the dotted line represents the imaginary part. To demodulate the BPSK signal, the pre-envelope signal is now multiplied with the output signal of the VCO, which is here a complex carrier as well. The complex output signal of the VCO is defined as

$$u_2(t) = \exp(-j[\omega_2 t + \theta_2]). \quad (42)$$

In the locked state of the Costas loop both frequencies ω_1 and ω_2 are equal, and we also have $\theta_1 \approx \theta_2$. Hence the output signal of the multiplier M_1 is as follows

$$u_m(t) = m_1(t) \exp(j[(\omega_1 - \omega_2)t + \theta_1 - \theta_2]) \approx m_1(t), \quad (43)$$

i.e. the output of the multiplier is the demodulated data signal $m_1(t)$. To derive the linear model of this Costas loop, it is assumed that $\omega_1 = \omega_2$ and $\theta_1 \neq \theta_2$. The output signal of multiplier M_1 then becomes

$$u_m(t) = m_1(t) \exp(j[\theta_1 - \theta_2]). \quad (44)$$

This is a phasor having magnitude $|m_1(t)|$ and phase $\theta_1 - \theta_2$, as shown in Fig. 18. Two quantities are determined from the phase of phasor $u_m(t)$, i.e. the demodulated data signal I and the phase error θ_e . The data signal is defined as

$$I = \text{sgn}(\text{Re}[u_m(t)]), \quad (45)$$

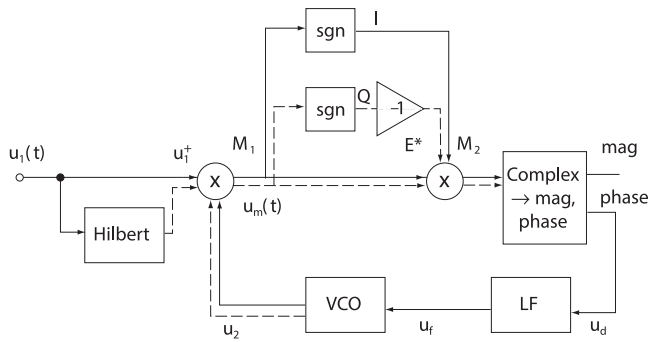


Fig. 19. Block diagram of modified Costas loop for QPSK.

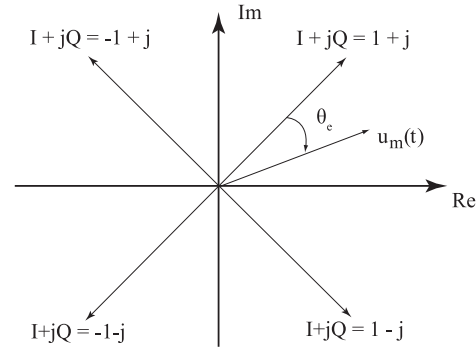


Fig. 20. Representation of phasor $u_m(t)$ in the complex plane.

i.e. when the phasor lies in quadrants I or IV, the data signal is considered to be +1, and when the phasor is in quadrants II or III, the data signal is considered to be -1. This means that I can be either a phasor with phase 0 or a phasor with phase π .

These two phasors are plotted as thick lines in Fig. 18.

The phase error θ_e is now given by the difference of the phases of phasor $u_m(t)$ and phasor I , as shown in Fig. 18, i.e. θ_e is determined from

$$\theta_e = \text{phase}(u_m(t)I) \quad (46)$$

The product $u_m(t)I$ is computed by multiplier M_2 in Fig. 18. The block labeled “Complex \rightarrow mag, phase” is used to convert the complex signal delivered by M_2 into magnitude and phase. The magnitude is not used in this case, but only the phase. It follows from Eq. (46) that the phase output of this block is the phase error θ_e , hence the blocks M_1 , M_2 , sgn, and Complex \rightarrow mag, phase represent a phase detector with gain $K_d = 1$. The phase output of block Complex \rightarrow mag, phase is therefore labeled u_d . Fig. 6 shows the complete linear model of the modified Costas loop for BPSK. The transfer functions of the loop filter and VCO have been defined in Eqs. (15) and (16). Note that with this type of Costas loop there is no additional lowpass filter, because the multiplication of the two complex carriers (cf. Eq. (43)) does not create the unwanted double frequency component as found with the conventional Costas loops.

1.2.2. Modified Costas loop for QPSK

Fig. 19 shows the block diagram of the modified Costas loop for QPSK.

The reference signal $u_1(t)$ is defined by

$$u_1(t) = m_1(t) \cos(\omega_1 t + \theta_1) - m_2(t) \sin(\omega_1 t + \theta_1), \quad (47)$$

The Hilbert transformed signal is then given by

$$\hat{u}_1(t) = m_1(t) \sin(\omega_1 + \theta_1) + m_2(t) \cos(\omega_1 + \theta_1) \quad (48)$$

and the pre-envelope signal then becomes

$$u_1^+(t) = m_1(t) \cos(\omega_1 t + \theta_1) - m_2(t) \sin(\omega_1 t + \theta_1) + jm_1(t) \sin(\omega_1 t + \theta_1) + jm_2(t) \cos(\omega_1 t + \theta_1). \quad (49)$$

This can be rewritten as

$$u_1^+(t) = (m_1(t) + jm_2(t))(\cos[\omega_1 t + \theta_1] + j \sin[\omega_1 t + \theta_1]) = (m_1(t) + jm_2(t)) \exp(j[\omega_1 t + \theta_1]). \quad (50)$$

Herein the term $(m_1(t) + jm_2(t))$ is complex envelope, and the term $\exp(j[\omega_1 t + \theta_1])$ is complex carrier. The VCO generates another complex carrier given by (42). The multiplier M_1 creates signal $u_m(t)$ that is given by

$$u_m(t) = (m_1(t) + jm_2(t)) \exp(j[(\omega_1 - \omega_2)t + (\theta_1 - \theta_2)]). \quad (51)$$

When the loop has acquired lock, $\omega_1 = \omega_2$, and $\theta_1 \approx \theta_2$, so we have

$$u_m(t) \approx (m_1(t) + jm_2(t)), \quad (52)$$

hence the output of M_1 is the complex envelope. In the locked state, the complex envelope can take four positions, as shown in Fig. 20. When there is a phase error, $u_m(t)$ deviates from the ideal position, as demonstrated in the figure. The phase error θ_e then is the angle between $u_m(t)$ and the closest of the four possible positions. When $u_m(t)$ is in quadrant I, e.g., phasor $1 + j$ is considered as the estimate of the complex envelope. When $u_m(t)$ is in quadrant II, the estimate of the complex envelope is $-1 + j$ etc. The estimates I and Q are taken from the output of sgn blocks, cf. Fig. 19. The phase error is obtained from

$$\theta_e = \text{phase}[u_m(t)(I - jQ)] \quad (53)$$

where $I - jQ$ is the conjugate of the complex envelope. Multiplier M_2 delivers the product $u_m(t)(I - jQ)$, and the block “Complex \rightarrow mag, phase” is used to compute the phase of this complex quantity. Note that the magnitude is not required. The blocks M_1 , sgn, Inverter, M_2 , and Complex \rightarrow mag, phase form a phase detector having gain $K_d = 1$. The phase output of block Complex \rightarrow mag, phase is therefore labeled u_d .

Fig. 6 shows the completed linear model of the modified Costas loop for QPSK, which is the same as for BPSK. The transfer functions of the loop filter and VCO have been defined in Eqs. (15) and (16).

1.3. Definitions of hold-in range, lock-in range, pull-in range.

In the classic books on phase-locked loops (Gardner, 1966; Shakhgil'dyan & Lyakhovkin, 1966; Viterbi, 1966) such concepts as hold-in, pull-in, lock-in, and other frequency ranges for which PLL can achieve lock were introduced. Usually in engineering literature nonrigorous definitions are given for these concepts. In the following we introduce definitions, based on rigorous discussion in Kuznetsov, Leonov, Yuldashev, and Yuldashev (2015b); Leonov, Kuznetsov, Yuldashev, and Yuldashev (2015a).

Definition of hold-in range. The largest interval $[0, \Delta\omega_h)$ of frequency deviations $|\Delta\omega_0|$, such that the loop re-achieves locked state after small perturbations of the filters' state, the phases and frequencies of VCO, and the input signals, is called a hold-in range (in general the stable equilibria can be considered as a multiple-valued function, in which case the existence of its continuous single-valued branch is required). This effect is also called steady-state stability. In addition, for a frequency deviation within the hold-in range, the loop in a locked state tracks small changes in input frequency, i.e. achieves a new locked state (tracking process) (Kuznetsov et al., 2015b; Leonov et al., 2015a).

Assume that the loop power supply is initially switched off and then at $t = 0$ the power is switched on, and assume that the initial

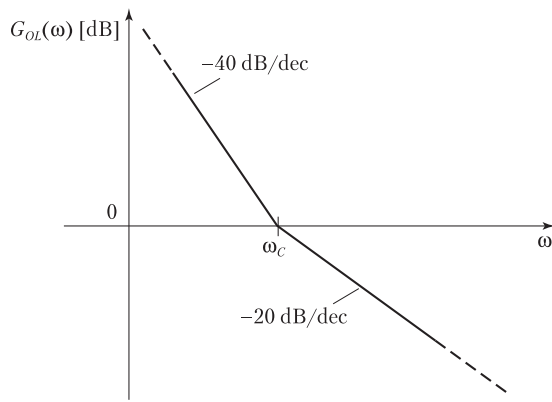


Fig. 2.1. Bode plot of magnitude of open loop gain $G_{OL}(\omega)$.

frequency difference is sufficiently large. The loop may not lock within one beat note, but the VCO frequency will be slowly tuned toward the reference frequency (acquisition process). This effect is also called a transient stability. The pull-in range is used to name such frequency deviations that make the acquisition process possible.

Definition of pull-in range. The largest interval $[0, \Delta\omega_p]$ of frequency deviations $|\Delta\omega_0|$, such that the loop achieves locked state for any initial states (filters and initial phase of VCO), is called a pull-in range (Kuznetsov et al., 2015b; Leonov et al., 2015a). The largest frequency deviation $\Delta\omega_p$ is called a pull-in frequency (Kuznetsov et al., 2015b; Leonov et al., 2015a).

Definition of lock-in range. Lock-in range is a largest interval of frequency deviations $|\Delta\omega_0| \in [0, \Delta\omega_L]$ inside pull-in range, such that after an abrupt change of ω_1 within a lock-in range the PLL reacquires lock without cycle slipping, if it is not interrupted. Here $\Delta\omega_L$ is called a lock-in frequency (Kuznetsov et al., 2015b; Leonov et al., 2015a).⁴

Finally, our definitions give $\Omega_{\text{lock-in}} \subset \Omega_{\text{pull-in}} \subset \Omega_{\text{hold-in}}$,

$$[0, \Delta\omega_L] \subset [0, \Delta\omega_p] \subset [0, \Delta\omega_H],$$

which is in agreement with the classical consideration (see, e.g. Best, 1984, p.34, Hsieh and Hung, 1996, p.612, Best, 2007, p.61, Egan, 2007, p.138, Kroupa, 2012, p.258).

2. BPSK Costas loop

2.1. Lock-in range $\Delta\omega_L$ and lock time T_L

Recall linear model of Costas loop in phase space (see Fig. 6). By (13)–(15) we can derive the open loop transfer function of the Costas loop, which is defined by the ratio $\Theta_2(s)/\Theta_1(s)$:

$$G_{OL}(s) = K_d \frac{K_0}{s} \frac{1 + s\tau_2}{s\tau_1} \quad (54)$$

Fig. 2.1 shows a Bode plot of the magnitude of G_{OL} . The plot is characterized by the corner frequency ω_c , which is defined by $\omega_c = 1/\tau_2$, and gain parameters K_d and K_0 . At lower frequencies

the magnitude rolls off with a slope of -40 dB/decade. At frequency ω_c the zero of the loop filter causes the magnitude to change its slope to -20 dB/decade. To get a stable system, the magnitude curve should cut the 0 dB line with a slope that is markedly less than -40 dB/decade. Setting the parameters such that the gain is just 0 dB at frequency ω_c provides a phase margin of 45 degrees, which assures stability [2]. From the open loop transfer function we now can calculate the closed loop transfer function defined by

$$G_{CL}(s) = \frac{\Theta_2(s)}{\Theta_1(s)}. \quad (55)$$

After some mathematical manipulations we get

$$G_{CL}(s) = \frac{K_0 K_d \frac{1+s\tau_2}{s\tau_1}}{s^2 + s \frac{K_0 K_d \tau_2}{\tau_1} + \frac{K_0 K_d}{\tau_1}}. \quad (56)$$

It is natural to represent this transfer function in normalized form, i.e.

$$G_{CS}(s) = \frac{2s\zeta\omega_n + \omega_n^2}{s^2 + 2s\zeta\omega_n + \omega_n^2} \quad (57)$$

with the substitutions

$$\omega_n = \sqrt{\frac{K_0 K_d}{\tau_1}}, \quad \zeta = \frac{\omega_n \tau_2}{2}, \quad (58)$$

where ω_n is called natural frequency and ζ is called damping factor. The linear model enables us to derive simple approximations for lock-in range $\Delta\omega_L$ and lock time T_L .

For the following analysis we assume that the loop is initially out of lock. The frequency of the input signal (Fig. 4) is ω_1 , and the frequency of the VCO is ω_2 . The multiplier in the I branch therefore generates an output signal consisting of a sum frequency term $\omega_1 + \omega_2$ and a difference frequency term $\omega_1 - \omega_2$. The sum frequency term is removed by the lowpass filter, and the frequency of the difference term is assumed to be much below the corner frequency ω_3 of the lowpass filter, hence the action of this filter can be neglected for this case. Under this condition the phase detector output signal $u_d(t)$ will have the form (cf. Eqs. (19) and (13))

$$u_d(t) = \frac{K_d}{2} \sin(2\Delta\omega t) \quad (59)$$

with $\Delta\omega = \omega_1 - \omega_2$. $u_d(t)$ is plotted in Fig. 2.2, left trace. This signal passes through the loop filter. In most cases the corner frequency $\omega_c = 1/\tau_2$ is much lower than the lock-in range, hence we can approximate its transfer function by

$$H_{LF}(\omega) \approx \frac{\tau_2}{\tau_1} = K_H. \quad (60)$$

Let us define the gain of this filter at higher frequencies by constant K_H . Now the output signal of the loop filter is a sine wave having amplitude $K_d K_H/2$ as shown by the middle trace in Fig. 2.2. Consequently the frequency of the VCO will be modulated as shown in the right trace. The modulation amplitude is given by $K_d K_0 K_H/2$. In this figure the reference frequency and the initial frequency ω_{free} of the VCO are plotted as horizontal lines. When ω_1 and ω_{free} are such that the top of the sine wave just touches the ω_1 line, the loop acquires lock suddenly, i.e. the lock-in range $\Delta\omega_L$ is nothing more than the modulation amplitude $K_d K_0 K_H/2$. Making use of the substitutions (58) we finally get

$$\Delta\omega_L = \zeta \omega_n \quad (61)$$

Now the lock process is a damped oscillation whose frequency is the natural frequency. Because the loop is assumed to lock within at most one cycle of that frequency, the lock time can be approximated by the period of the natural frequency, i.e. we have

$$T_L \approx \frac{2\pi}{\omega_n} \quad (62)$$

⁴ The concept of the lock-in range was suggested by F. Gardner in 1966 (Gardner, 1966, p.40) and it is widely used nowadays (see, e.g. (Best, 1984, p.34–35), (Wolaver, 1991, p.161), (Hsieh and Hung, 1996, p.612), (Irwin, 1997, p.532), (Craninckx and Steyaert, 1998, p.25), (Kihara, Ono, and Eskelinen, 2002, p.49), (Abramovitch, 2002, p.4), (De Muer and Steyaert, 2003, p.24), (Dyer, 2004, p.749), (Shu and Sanchez-Sinencio, 2005, p.56), (Goldman, 2007, p.112), (Best, 2007, p.61), (Egan, 2007, p.138), (Baker, 2011, p.576), (Kroupa, 2012, p.258)). However later Gardner noticed that the lock-in range definition lacks rigor and requires clarification (Gardner, 1979, p.70), (Gardner, 2005, p.187–188). Recently a rigorous definition was suggested in Kuznetsov et al. (2015b); Leonov et al. (2015a).

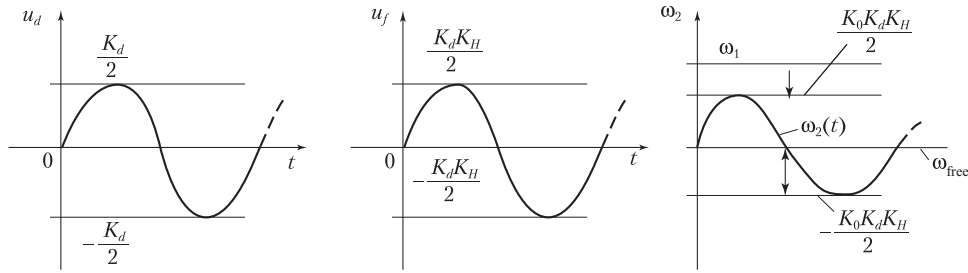


Fig. 2.2. Lock-in range of Costas loop.

2.2. Pull-in range $\Delta\omega_p$ and pull-in time T_p

We have seen that all signals found in this block diagram are sine functions, i.e. all of them seem to have zero average, hence do not show any *dc* component. This would lead to the (erroneous) conclusion that a pull-in process would not be possible. In reality it will be recognized that some of the signals become asymmetrical, i.e. the duration of the positive half wave is different from the duration of the negative one. This creates a nonzero *dc* component, and under suitable conditions acquisition can be obtained. We are therefore going to analyze the characteristics of the signals in Fig. 10.

All considered signals are plotted in Fig. 2.3. For signals I_1 and Q_1 we obtain

$$I_1(t) = m_1(t) \cos(\Delta\omega t)$$

$$Q_1(t) = m_1(t) \sin(\Delta\omega t)$$

The sum frequency terms are discarded because they are removed by the lowpass filter. The signal $u_d(t)$ is the product of I_1 and Q_1 and is given by (59). For small arguments $2\Delta\omega t$ this can be written as

$$u_d(t) = m_1^2(t)\Delta\omega t = m_1^2(t)\theta_e(t),$$

where $\theta_e = \Delta\omega t$. Because the phase detector gain is defined by

$$u_d(t) = K_d\theta_e(t),$$

we have $K_d = m_1^2$.

Next the loop filter output signal $u_f(t)$ is plotted. Its amplitude is $K_H m_1^2/2$, and its phase is delayed by $\varphi_{tot} = 2\varphi_1 + \varphi_2$. This signal modulates the frequency of the VCO as shown in the bottom trace of Fig. 2.3. The modulation amplitude is given by $\frac{m_1^2 K_H K_0}{2}$. In order to get an estimate for the nonzero *dc* component of $u_d(t)$ we will have to analyze the asymmetry of the signal waveforms. It will be shown that $\overline{u_d}$ (the average of $u_d(t)$) is a function of frequency difference $\Delta\omega$ and phase φ_{tot} . The analysis becomes easier when we first calculate $\overline{u_d}$ for some special values for φ_{tot} , i.e. for $\varphi_{tot} = 0; -\pi/2$; and $-\pi$. Let us start with $\varphi_{tot} = 0$, cf. Fig. 2.4.

In Fig. 2.4 the waveforms for $u_d(t)$ and $\omega_2(t)$ are shown. The asymmetry of the signals is exaggerated in this plot. During the positive half cycle (duration T_1) the average value of VCO output frequency $\omega_2(t)$ is increased, which means that the average difference frequency $\Delta\omega(t)$ is lowered. Consequently the duration of the positive half wave becomes larger than half of a full cycle. During the negative half cycle (duration T_2), however, the average value of VCO output frequency $\omega_2(t)$ is decreased, which means that the average difference frequency $\Delta\omega(t)$ is increased. Consequently the duration of the negative half wave becomes less than half of a full cycle. Next we are going to calculate the average frequency difference in both half cycles. The average frequency difference during half cycle T_1 is denoted $\overline{\Delta\omega_{d+}}$, the average frequency difference

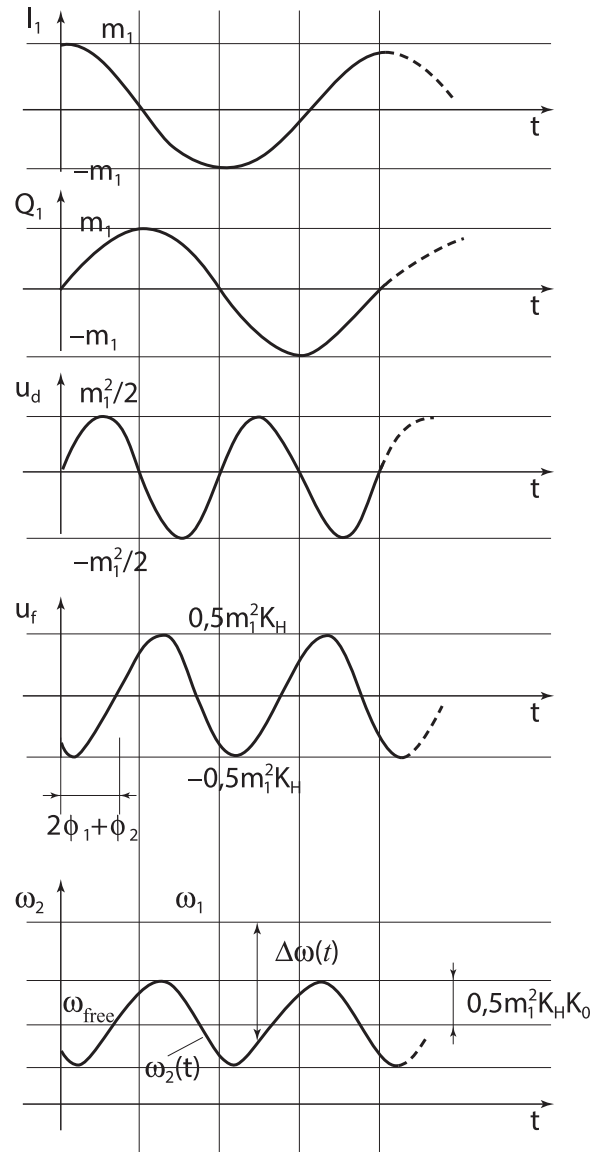


Fig. 2.3. Signals of the model in Fig. 10.

during half cycle T_2 is denoted $\overline{\Delta\omega_{d-}}$. We get

$$\overline{\Delta\omega_{d+}} = \Delta\omega - \frac{2}{\pi} \frac{K_0 K_d K_H}{2}, \tag{63}$$

$$\overline{\Delta\omega_{d-}} = \Delta\omega + \frac{2}{\pi} \frac{K_0 K_d K_H}{2}. \tag{64}$$

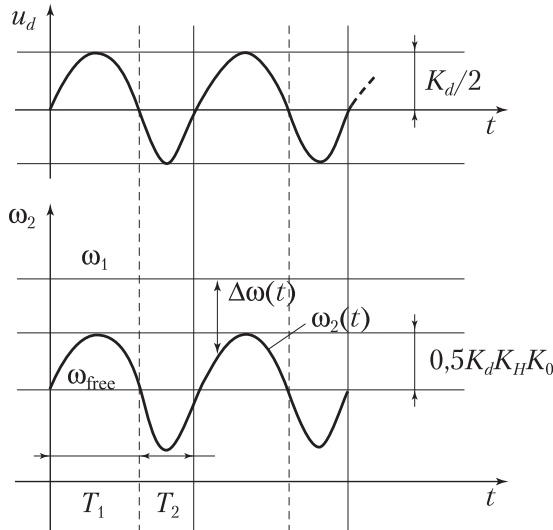


Fig. 2.4. Signals of the model in Fig. 10 for $\varphi_{tot} = 0$.

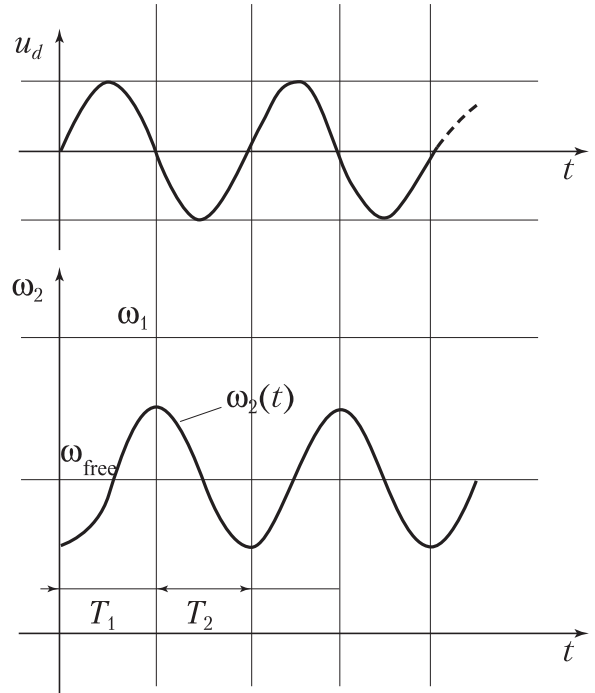


Fig. 2.6. Signals of the model in Fig. 10 for $\varphi_{tot} = -\pi/2$.

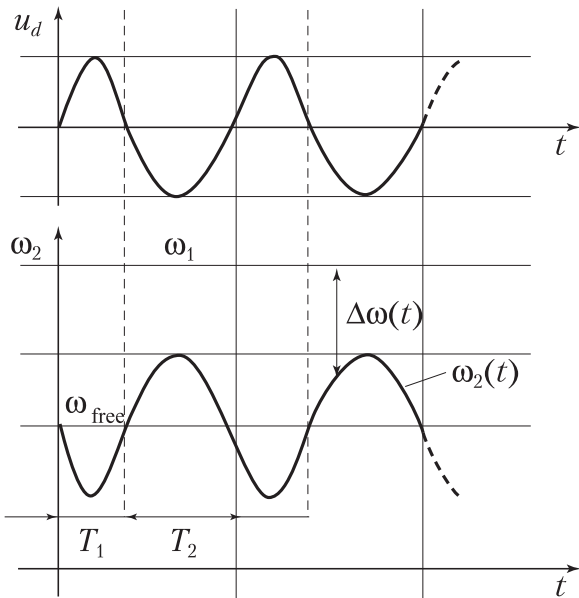


Fig. 2.5. Signals of the model in Fig. 10 for $\varphi_{tot} = -\pi$.

For the durations T_1 and T_2 we obtain after some manipulations the following

$$T_1 \approx \frac{\pi}{2\Delta\omega} \left(1 + \frac{K_0K_dK_H}{\pi\Delta\omega} \right), \quad (65)$$

$$T_2 \approx \frac{\pi}{2\Delta\omega} \left(1 - \frac{K_0K_dK_H}{\pi\Delta\omega} \right). \quad (66)$$

Now the average value $\overline{u_d}$ can be calculated from

$$\overline{u_d(t)} = \frac{K_0K_d^2K_H}{\pi^2\Delta\omega}. \quad (67)$$

The average signal $\overline{u_d}$ is seen to be inversely proportional to the frequency difference $\Delta\omega$. Because $\overline{u_d}$ is positive, the instantaneous frequency $\omega_2(t)$ is pulled in positive direction, i.e. versus ω_1 , which means that a pull-in process will take place.

Next we are going to analyze the dependence of $\overline{u_d}$ on phase φ_{tot} . Let us consider now the case for $\varphi_{tot} = -\pi$, cf. Fig. 2.5. We observe that in interval T_1 the instantaneous frequency $\omega_2(t)$ is pulled in negative direction, hence the average difference frequency $\overline{\Delta\omega_{d+}}$ becomes larger. Consequently interval T_1 becomes

shorter. In interval T_2 , however, the reverse is true. Here the instantaneous frequency T_1 is pulled in positive direction, hence the average $\overline{\Delta\omega_{d-}}$ is reduced, and interval T_2 becomes longer. The average $\overline{u_d}$ is now equal and opposite to the value of $\overline{u_d}$ for $\varphi_{tot} = 0$. Because it is negative under this condition, a pull-in process cannot take place, because the frequency of the VCO is “pulled away” in the wrong direction.

Last we consider the case $\varphi_{tot} = -\pi/2$, cf. Fig. 2.6. In the first half of interval T_1 the instantaneous frequency $\omega_2(t)$ is decreased, but in the second half it is increased. Consequently the average difference frequency $\overline{\Delta\omega_{d+}}$ does not change its value during T_1 . The same happens in interval T_2 . $\overline{\Delta\omega_{d-}}$ does not change either, and $\overline{u_d}$ remains 0.

It is easy to demonstrate that $\overline{u_d}$ varies with $\cos(\varphi_{tot})$, hence we have

$$\overline{u_d(t)} = \frac{K_0K_d^2K_H}{\pi^2\Delta\omega} \cos(\varphi_{tot}), \quad \varphi_{tot} = 2\varphi_1 + \varphi_2. \quad (68)$$

Eq. (68) tells us that the pull-in range is finite. The pull-in range can be found as the frequency difference for which phase $\varphi_{tot} = -\pi/2$. An equation for the pull-in range will be derived in Section 2.2. We also will have to find an equation for the pull-in time. The model shown in Fig. 11 will enable us to obtain a differential equation for the average frequency difference $\overline{\Delta\omega}$ as a function of time.

Recall equations of filter output (26)

$$\overline{u_f(t)} = \frac{1}{\tau_1} \int_0^t \overline{u_d(\tau)} d\tau$$

and frequency deviation (29)

$$\Delta\omega = \Delta\omega_0 - K_0\overline{u_f}.$$

Eqs. (68), (26), and (29) enable us to compute the three variables $\overline{u_d}$, $\overline{u_f}$, and $\Delta\omega$ as a function of time. This will be demonstrated in Section 2.2.

The pull-in range can be computed using Eq. (68). Lock can only be obtained when the total phase shift φ_{tot} is not more negative

than $-\pi/2$. This leads to an equation of the form

$$2\varphi_1(\Delta\omega_p) + \varphi_2(2\Delta\omega_p) = -\pi/2. \tag{69}$$

According to Eqs. (15) and (22) φ_1 and φ_2 are given by

$$\varphi_1(\omega) = -\text{arctg}(\omega/\omega_3),$$

$$\varphi_2(\omega) = -\pi/2 + \text{arctg}(\omega/\omega_c)$$

with $\omega_c = 1/\tau_2$. Hence the pull-in range $\Delta\omega_p$ can be computed from the transcendental equation

$$2\text{arctg}(\Delta\omega_p/\omega_3) = \text{arctg}(2\Delta\omega_p/\omega_c). \tag{70}$$

To solve this equation for $\Delta\omega_p$ we use the addition formula for the tangent function

$$\text{tg}(2\alpha) = \frac{2\text{tg}\alpha}{1 - \text{tg}^2\alpha}$$

and can replace $2\text{arctg}(\Delta\omega_p/\omega_3)$ by $\text{arctg} \frac{2 \frac{\Delta\omega_p}{\omega_3}}{1 - \frac{\Delta\omega_p^2}{\omega_3^2}}$. Eq. (70) can now

$$\text{be rewritten as } \text{arctg} \frac{2 \frac{\Delta\omega_p}{\omega_3}}{1 - \frac{\Delta\omega_p^2}{\omega_3^2}} = \text{arctg} 2 \frac{\Delta\omega_p}{\omega_c}.$$

When the arctg expressions on both sides of the equation are equal, their arguments must also be identical, which leads to

$$\frac{2 \frac{\Delta\omega_p}{\omega_3}}{1 - \frac{\Delta\omega_p^2}{\omega_3^2}} = 2 \frac{\Delta\omega_p}{\omega_c}.$$

Hence we get for the pull-in range

$$\Delta\omega_p = \omega_3 \sqrt{\frac{\omega_3}{\omega_c} - 1}. \tag{71}$$

Last, equation for the pull-in time T_p will be derived. Eqs. (68), (26), and (29) describe the behavior of three building blocks in Fig. 11 and enable us to compute three variables \bar{u}_d , \bar{u}_f , and $\Delta\omega$. We only need to know the instantaneous $\Delta\omega$ vs. time, hence we eliminate \bar{u}_d and \bar{u}_f from Eqs. (26) and (29) and obtain the differential equation

$$\frac{d}{dt} \Delta\omega \tau_1 + \frac{1}{\Delta\omega} \frac{K_0^2 K_d^2 K_H}{\pi^2} \cos(\varphi_{tot}) = 0. \tag{72}$$

This differential equation is nonlinear, but the variables $\Delta\omega$ and t can be separated, which leads to an explicit solution. Putting all terms containing $\Delta\omega$ to the left side and performing an integration, we get

$$\frac{\tau_1 \pi^2}{K_0^2 K_d^2 K_H} \int_{\Delta\omega_0}^{\Delta\omega_L} \frac{\Delta\omega}{\cos(\varphi_{tot})} d\Delta\omega = - \int_0^{T_p} dt. \tag{73}$$

The limits of integration are $\Delta\omega_0$ and $\Delta\omega_L$ on the left side, because the pull-in process starts with an initial frequency offset $\Delta\omega = \Delta\omega_0$ and ends when $\Delta\omega$ reaches the value $\Delta\omega_L$, which is the lock-in range. Following that instant a lock-in process will start. The integration limits on the right side are 0 and T_p , respectively, which means that the pull-in process has duration T_p , and after that interval (fast) lock-in process starts.

Performing the integration on the left imposes some considerable problems, when we remember that $\cos(\varphi_{tot})$ is given by

$$\cos(\varphi_{tot}) = \cos\left(-2 \text{arctg} \frac{\Delta\omega}{\omega_3} - \frac{\pi}{2} + \text{arctg} \frac{2\Delta\omega}{\omega_c}\right).$$

Finding an explicit solution for the integral seems difficult if not impossible, but the cos term can be drastically simplified. When we plot $\cos(\varphi_{tot})$ vs. $\Delta\omega$ we observe that within the range $\Delta\omega_L$

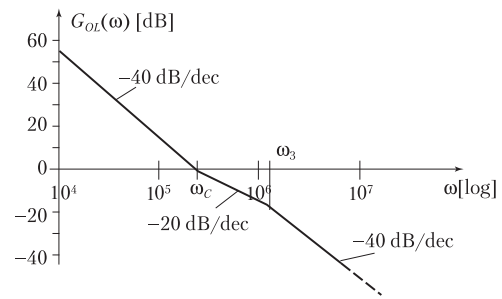


Fig. 2.7. Bode plot of open loop transfer function of Costas loop.

$< \Delta\omega < \Delta\omega_0$ the term $\cos(\varphi_{tot})$ is an almost perfect straight line. Hence we can replace $\cos(\varphi_{tot})$ by

$$\cos(\varphi_{tot}) \approx 1 - \frac{\Delta\omega}{\Delta\omega_p}.$$

Inserting that substitution into Eq. (73) yields a rational function of $\Delta\omega$ on the left side, which is easily integrated. After some mathematical procedures we obtain for the pull-in time T_p the following

$$T_p = \frac{\Delta\omega_p \pi^2 \tau_1}{2K_0^2 K_d^2 K_H} \left[\Delta\omega_p \ln \frac{\Delta\omega_p - \Delta\omega_L}{\Delta\omega_p - \Delta\omega_0} - \Delta\omega_0 + \Delta\omega_L \right]. \tag{74}$$

Making use of Eqs. (58) and (60) we have

$$K_H = \frac{\tau_2}{\tau_1}, \quad \omega_n^2 = \frac{K_0 K_d}{\tau_1}, \quad \zeta = \frac{\omega_n \tau_2}{2}.$$

Using these substitutions Eq. (74) can be rewritten as

$$T_p = \frac{\Delta\omega_p \pi^2}{2\zeta \omega_n^3} \left[\Delta\omega_p \ln \frac{\Delta\omega_p - \Delta\omega_L}{\Delta\omega_p - \Delta\omega_0} - \Delta\omega_0 + \Delta\omega_L \right]. \tag{75}$$

This equation is valid for initial frequency offsets in the range $\Delta\omega_L < \Delta\omega_0 < \Delta\omega_p$. For lower frequency offsets, a fast pull-in process will occur, and Eq. (62) should be used.

2.3. Numerical example 1: Designing an analog Costas loop for BPSK

An analog Costas loop for BPSK shall be designed in this section. It is assumed that a binary signal is modulated with a carrier. The carrier frequency is set to 400 kHz, i.e. the Costas loop will operate at a center frequency $\omega_0 = 2\pi \cdot 400,000 = 2,512,000 \text{ rad s}^{-1}$. The symbol rate is assumed to be $f_s = 100,000 \text{ symbols/s}$. Now the parameters of the loop (such as time constants τ_1 and τ_2 , corner frequencies ω_c and ω_3 , and gain parameters such as K_0 , K_d) must be determined. (Note that these parameters have been defined in Eqs. (14)–(16) and (62)).

The modulation amplitude is set $m_1 = 1$. According to Eq. (13) the phase detector gain is then $K_d = 1$. It has proven advantageous to determine the remaining parameters by using the open loop transfer function $G_{OL}(s)$ of the loop [2]. This is given by

$$G_{OL}(s) = \frac{K_0 K_d}{s} \frac{1 + s/\omega_c}{s\tau_1} \frac{1}{1 + s/\omega_3} \tag{76}$$

The magnitude $|G_{OL}(\omega)|$ (Bode diagram) is plotted in Fig. 2.7. The magnitude curve crosses the 0 dB line at the so called transit frequency ω_T . It is common practice to choose ω_T to be about $(0.05\omega_0 \dots 0.1\omega_0)$. Here we set $\omega_T = 0.1\omega_0$, i.e. $\omega_T = 251,200 \text{ rad s}^{-1}$. Furthermore we set corner frequency $\omega_c = \omega_T$. When doing so, the slope of the asymptotic magnitude curve changes from -40 dB/decade to -20 dB/decade at $\omega = \omega_c$. Under this condition the phase of $G_{OL}(\omega)$ is -135° at ω_c . Consequently the phase margin of the loop becomes 45° , which provides sufficient stability. According to Eq. (15) τ_2 becomes $4\mu\text{s}$. Next corner frequency ω_3 will be determined. The corner frequency of the lowpass filter

must be chosen such that the demodulated data signal (i. e. the output of the lowpass filter in the I branch) is recovered with high fidelity. To fulfill this requirement, ω_3 should be chosen as large as possible. On the other hand, the lowpass filter should suppress the double frequency component (here at about 800 kHz) sufficiently, which means that ω_3 should be markedly less than $2\omega_0$. It is a good compromise to set corner frequency to twice the symbol rate, i.e. $\omega_3 = 2 \cdot 2\pi \cdot 100,000 = 1,256,000 \text{ rads}^{-1}$. Last, remaining parameters τ_1 and K_0 must be chosen. They have to be specified such that the open loop gain becomes 1 at frequency $\omega = \omega_c$. According to Eq. (76) we can set

$$G_{OL}(\omega_c) = 1 \approx \frac{K_0 K_d}{\omega_c^2 \tau_1} \tag{77}$$

Because 2 parameters are still undetermined, one of those can be chosen arbitrarily, hence we set $\tau_1 = 20 \text{ }\mu\text{s}$. Finally from (77) we get $K_0 = 1,262,000 \text{ s}^{-1}$.

The design of the Costas loop is completed now, and we can compute the most important loop parameters. For the natural frequency and damping factor we get from (58)

$$n = 251,000 \text{ rads}^{-1} \quad (f_n = 40 \text{ kHz}),$$

$$\zeta = 0.5.$$

From (61) the lock-in range becomes

$$\Delta\omega_L = 125,000 \text{ rads} \quad (\Delta f_L = 20 \text{ kHz})$$

and from (62) the lock time becomes

$$T_L = 25\mu\text{s}.$$

Next we want to compute the pull-in range. Eq. (71) yields

$$\Delta\omega_p = 1,086,440 \text{ rad s}^{-1} \quad (\Delta f_p = 173 \text{ kHz}).$$

2.4. Numerical example 2: Designing a digital Costas loop for BPSK

To convert the analog loop into a digital one, we first must define a suitable sampling frequency f_{samp} (or sampling interval $T = 1/f_{\text{samp}}$). To satisfy the Nyquist theorem, the sampling frequency must be higher than twice the highest frequency that exists in the loop. In our case the highest frequency is found at the output of the multipliers in the I and Q branches (cf. Fig. 4). The sum frequency term is about twice the center frequency, hence f_{samp} must be greater than 4 times the center frequency. A suitable choice would be $f_{\text{samp}} = 8 f_0 = 3.2 \text{ MHz}$.

Next the transfer functions of the building block have to be converted into discrete transfer functions, i.e. $H(s) \rightarrow H(z)$. For best results it is preferable to use the bilinear z transform. Given an analog transfer function $H(s)$, it can be converted into a discrete transfer function $H(z)$ by replacing s by

$$s = \frac{2}{T} \frac{1-z^{-1}}{1+z^{-1}} \tag{78}$$

Now the bilinear z transform has the property that the analog frequency range from $0 \dots \infty$ is compressed to the digital frequency range from $0 \dots f_{\text{samp}}/2$. To avoid undesired “shrinking” of the corner frequencies (ω_c and ω_3), they must be “prewarped” accordingly, i.e. we must set

$$\omega_{c,p} = \frac{2}{T} \text{tg} \frac{\omega_c T}{2}, \tag{79}$$

$$\omega_{3,p} = \frac{2}{T} \text{tg} \frac{\omega_3 T}{2}, \tag{80}$$

where $\omega_{c,p}$ and $\omega_{3,p}$ are the prewarped corner frequencies. Now we can apply the bilinear z transform to the transfer functions of

Table 2.1
Comparison of predicted and simulated results for the pull-in range.

Δf_0 (Hz)	$\Delta\omega_0$ (rad s ⁻¹)	T_p (theory) (μs)	T_p (simulation) (μs)
50 kHz	314,000	33	30
70 kHz	439,000	78	85
100 kHz	628,000	204	200

the lowpass filters (cf. Eq. (22)) and of the loop filter (cf. Eq. (15)) and get

$$H_{LPF}(z) = \frac{\left[1 + \frac{2}{\omega_{3,p}T}\right] + \left[1 - \frac{2}{\omega_{3,p}T}\right]z^{-1}}{1 + z^{-1}}, \tag{81}$$

$$H_{LF}(z) = \frac{\left[1 + \frac{2}{\omega_{c,p}T}\right] + \left[1 - \frac{2}{\omega_{c,p}T}\right]z^{-1}}{\frac{2\tau_1}{T} - \frac{2\tau_1}{T}z^{-1}}. \tag{82}$$

Because the VCO is a simple integrator, we can apply the discrete z transform of an integrator, i.e.

$$H_{VCO}(z) = \frac{K_0 T}{1 - z^{-1}}. \tag{83}$$

The digital Costas loop is ready now for implementation. A Simulink model will be presented in Section 2.5.

2.5. Simulating the digital Costas loop for BPSK

A Simulink model of a Costas loop for BPSK is shown in Fig. 2.8.

A data signal is created by a random number generator at the left in the block diagram. The other blocks are self explanatory. The model is used now to check the validity of the approximations found for pull-in range and pull-in time.

Eq. (71) predicts a pull-in range $\Delta f_p = 173 \text{ kHz}$. The simulation revealed a pull-in range of $\Delta f_p = 133 \text{ kHz}$, which shows that the theoretical result is a rather crude approximation. A series of other simulation delivered results for the pull-in time ΔT_p . The results are listed in Table 2.1.

We note that the predicted and simulated parameters are in good agreement.

2.6. Remarks on simulation of BPSK Costas loop

Note that a numerical simulation of various models of the same circuit can lead to essentially different results if the corresponding mathematical assumptions, used for the models construction, are not satisfied. Also the errors caused by numerical integration (e.g. in MATLAB and SPICE) can lead to unreliable results (Best et al., 2015; Bianchi, Kuznetsov, Leonov, Yuldashev, & Yuldashev, 2016b; Kuznetsov et al., 2015a; 2014b). The following examples demonstrate some limitations of numerical approach on simple models.

Next the following parameters are used in simulation: lowpass filters transfer functions $H_{lpf}(s) = \frac{2}{s/\omega_3+1}$, $\omega_3 = 1.2566 \cdot 10^6$ and the corresponding parameters in system (6) are $A_{1,2} = -\omega_3$, $b_{1,2} = 1$, $c_{1,2} = \omega_3$; loop filter transfer function $H_{lf}(s) = \frac{\tau_2 s+1}{\tau_1 s}$, $\tau_2 = 3.9789 \cdot 10^{-6}$, $\tau_1 = 2 \cdot 10^{-5}$, and the corresponding parameters in system (6) are $A = 0$, $b = 1$, $c = \frac{1}{\tau_1}$, $h = \frac{\tau_2}{\tau_1}$; carrier frequency $\omega_1 = 2 \cdot \pi \cdot 400000$; VCO input gain $L = 4.8 \cdot 10^6$; and carrier initial phase $\theta_2(0) = \theta_1(0) = 0$.

Example 1 (double frequency and averaging). In Fig. 2.9 it is shown that Assumption 1 may not be valid: mathematical model in signal's phase space (see Fig. 1 – black color) and physical model (see Fig. 4 and system (9) – red color) after transient processes have different phases in the locked states.

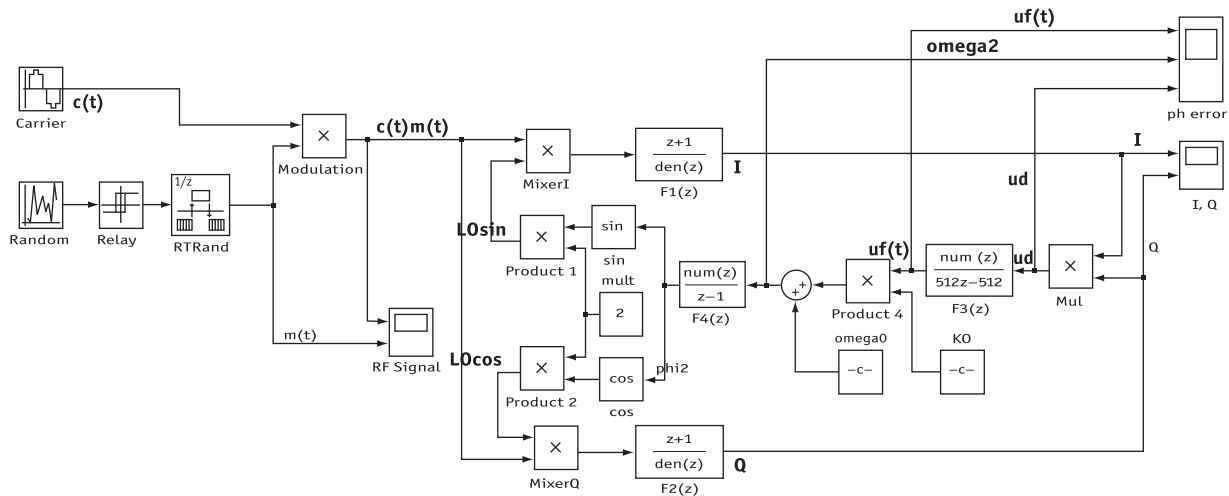


Fig. 2.8. Simulink model of the digital Costas loop for BPSK.

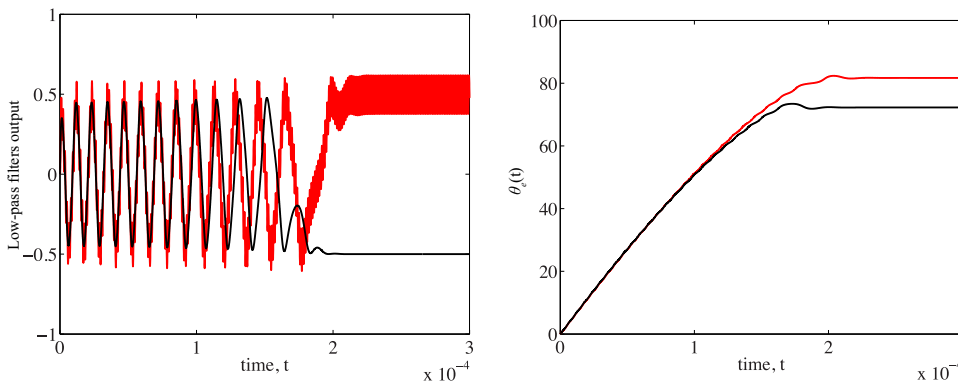


Fig. 2.9. Low-pass filter outputs and phase difference for averaged model (black) and physical model (red) in Fig. 4. (For interpretation of the references to colour in this figure legend, the reader is referred to the web version of this article.)

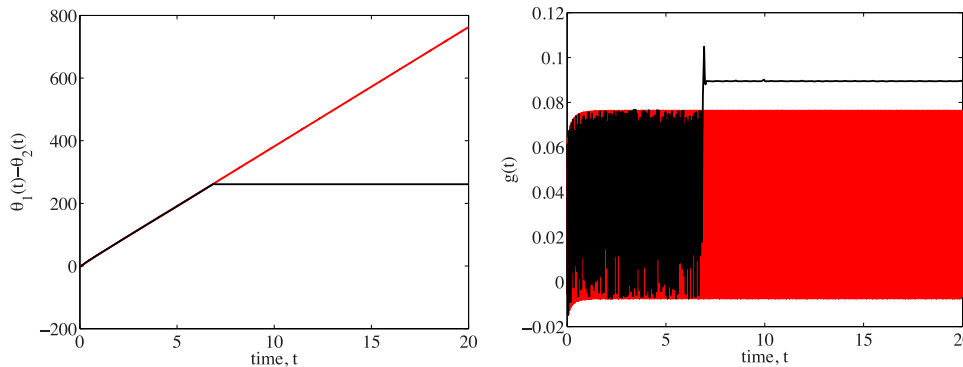


Fig. 2.10. Filter outputs: default integration parameters in Simulink 'max step size' set to 'auto' (black curve); Parameters configured manually 'max step size' set to '1e-3' (red curve). (For interpretation of the references to colour in this figure legend, the reader is referred to the web version of this article.)

Here VCO free-running frequency $\omega_{free} = 2 \cdot \pi \cdot 400000 - 600000$; initial states of filters are all zero; $x(0) = x_1(0) \equiv x_2(0) = 0$.

Example 2 (numerical integration parameters). In Fig. 30 it is shown that standard simulation of the loop may not be valid: while the classic mathematical model in signal's phase space (Fig. 1), simulated in Simulink with predefined integration parameters: 'max step size' set to '1e-3', is out of lock (black), the same model simulated in Simulink with default integration parameters: 'max step size' set to 'auto', acquires lock (red). Here Matlab

chooses step from $5 \cdot 10^{-3}$ to $9 \cdot 10^{-2}$; for the fixed step $2 \cdot 10^{-2}$ the model acquires lock, for the fixed step $1 \cdot 10^{-2}$ the model doesn't acquire lock.

Here the initial loop filter state output is $x(0) = 0.0125$; VCO free-running frequency $\omega_{free} = 10000 - 89.45$; VCO input gain $L = 1000$; initial phase shift $\theta_e(0) = -3.4035$.

Consider now the corresponding phase portrait (see Fig. 2.11). Here the red trajectory tends to a stable equilibrium (red dot). Lower and higher black trajectories are stable and unstable limit cycles, respectively. The blue trajectory tends to a stable periodic trajectory (lower black periodic curve) and in this case the model

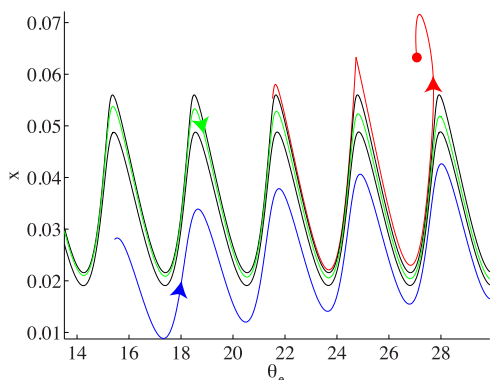


Fig. 2.11. Phase portrait: coexistence of stable and unstable periodic solutions. (For interpretation of the references to colour in this figure legend, the reader is referred to the web version of this article.)

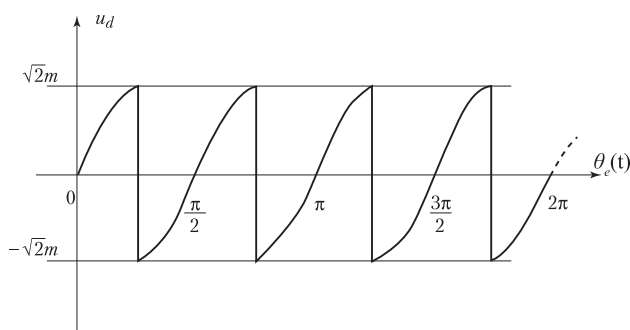


Fig. 3.1. Phase detector output signal u_d as a function of phase error θ_e .

does not acquire lock. All trajectories between black trajectories (see green trajectory) tend to the stable lower black trajectory.

If the gap between stable and unstable trajectories (black lines) is smaller than the discretization step, the numerical procedure may slip through the stable trajectory (blue trajectory may step over the black and green lines and begins to be attracted to the red dot). In other words, the simulation may show that the Costas loop acquires lock although in reality it does not happen. The considered case corresponds to the coexisting attractors (one of which is a hidden oscillation) and the bifurcation of birth of a semistable trajectory (Leonov & Kuznetsov, 2013).

Note, that only trajectories (red) above the unstable limit cycle is attracted to the equilibrium. Hence $\Delta\omega = 89.45$ does not belong to the pull-in range.

Corresponding limitations, caused by hidden oscillations, appear in simulation of various phase-locked loop (PLL) based systems (Best et al., 2015; Bianchi et al., 2016a; Bianchi et al., 2016b; Kudryashova et al., 2014; Kuznetsov et al., 2014a; 2015a; 2014b; Kuznetsov, Leonov, Yuldashev, & Yuldashev, 2014c; Leonov & Kuznetsov, 2013; Leonov et al., 2015a).

3. QPSK Costas loop

3.1. Lock-in range $\Delta\omega_L$ and lock time T_L

The open loop transfer function is identical with that of the Costas loop for BPSK, cf. Eq. (54) and Fig. 2.1. This holds true for the closed loop transfer function, too, cf. Eqs. (56)–(58). To determine the lock-in range, we assume that the loop is out of lock. Let the reference frequency be ω_1 , and the initial VCO frequency ω_{free} . The difference frequency $\omega_1 - \omega_2$ is called $\Delta\omega$. When the loop has not acquired lock, the phase error θ_e is a continuously rising function that increases towards infinity. The phase detector output sig-

nal u_d is then a chopped sine wave as depicted in Fig. 3.1. The fundamental frequency of this signal is four times the difference frequency, i.e. $4\Delta\omega$. This signal is plotted once again in the left trace of Fig. 3.2. The amplitude of this signal is $K_d/\sqrt{2}$. Because for the Costas loop for QPSK the phase detector gain is $K_d = 2m$, it is equal to $\sqrt{2}m$. The fundamental frequency of u_d is assumed to be much higher than the corner frequency ω_C of the loop filter, hence the transfer function of the loop filter can be approximated by

$$H_{LF}(s) \approx \frac{\tau_2}{\tau_1} = K_H. \quad (84)$$

Hence the output signal of the loop filter u_f has an amplitude of $K_d K_H/\sqrt{2}$, cf. middle trace of Fig. 3.2. This signal modulates the output frequency of the VCO, and the modulation amplitude is given by $K_d K_H K_0/\sqrt{2}$, cf. right trace in Fig. 3.2. It is easily seen that the loop spontaneously locks when the peak of the $\omega_2(t)$ waveform touches the ω_1 line, hence we have

$$\Delta\omega_L = \frac{K_0 K_d K_H}{\sqrt{2}}. \quad (85)$$

Making use of Eqs. (58) and (84) this can be rewritten as

$$\Delta\omega_L = \sqrt{2}\zeta\omega_n. \quad (86)$$

Because the transient response of the loop is a damped oscillation whose frequency is ω_n , the loop will lock in at most one cycle of ω_n , and we get for the lock time

$$T_L \approx \frac{2\pi}{\omega_n}. \quad (87)$$

3.2. Pull-in range and pull-in time for QPSK

Consider the simplified nonlinear model of QPSK Costas loop, cf. Section 1.1.2. Let us define the total phase by $\varphi_{tot} = 4\varphi_1 + \varphi_2$. Next we are computing the average phase detector output signal \bar{u}_d as a function of frequency difference and phase. First we calculate \bar{u}_d for the special case $\varphi_{tot} = 0$. As shown in the right trace in Fig. 3.3 during interval T_1 the average frequency ω_2 is increased, hence the average difference $\Delta\omega$ becomes smaller. During next half cycle T_2 the reverse is true: the average difference $\Delta\omega$ becomes greater, hence for $\varphi_{tot} = 0$ T_1 is longer than T_2 . The modulating signal is therefore asymmetric, and because also $u_d(t)$ (left trace) is asymmetric its average \bar{u}_d becomes nonzero and positive. This asymmetry has been shown exaggerated in Fig. 3.3.

Using the same mathematical procedure as for BPSK Costas loop, the average u_d signal is given by

$$\bar{u}_d = \frac{0.373^2 K_d^2 K_H}{\Delta\omega} \cos(4\varphi_1[\Delta\omega] + \varphi_2[4\Delta\omega]). \quad (88)$$

As in case of the Costas loop for BPSK, here again Eq. (88) shows us that the pull-in range is finite. The pull-in range is the frequency difference for which phase $\varphi_{tot} = -\pi/2$. An equation for the pull-in range will be derived here. We also have to find an equation for the pull-in time. To derive the pull-in process, we will use the same nonlinear model as used for the Costas loop for BPSK, cf. Fig. 11. The transfer functions for the loop filter and for the VCO have been given in Eqs. (26) and (29), respectively.

The pull-in range can be computed using Eq. (88). Lock can only be obtained when the total phase shift is not more negative than $-\pi/2$. This leads to an equation of the form

$$4\varphi_1(\Delta\omega_p) + \varphi_2(4\Delta\omega_p) = -\pi/2. \quad (89)$$

According to Eqs. (15) and (22) φ_1 and φ_2 are given by

$$\varphi_1(\omega) = -\arctg(\omega/\omega_3),$$

$$\varphi_2(\omega) = -\pi/2 + \arctg(\omega/\omega_C)$$

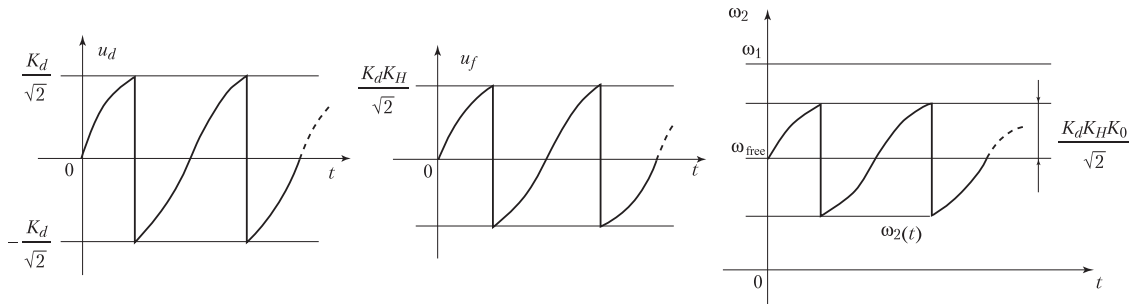


Fig. 3.2. Signals $u_d(t)$, $u_f(t)$, and $\omega_2(t)$ during the pull-in process.

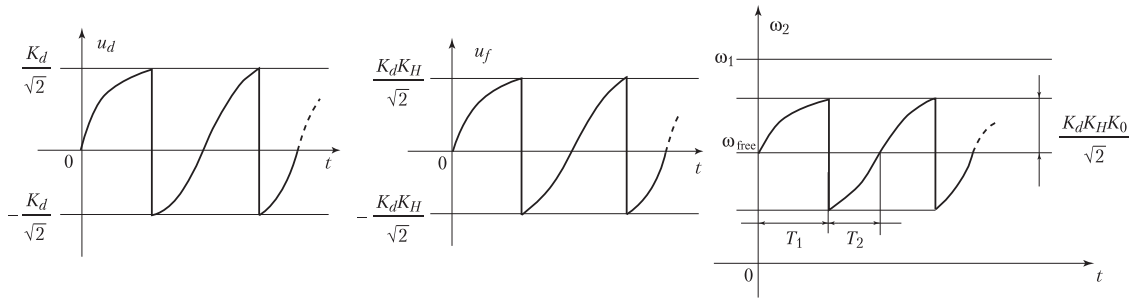


Fig. 3.3. Signals of the Costas loop for QPSK in the unlocked state.

with $\omega_c = 1/\tau_2$. Hence the pull-in range $\Delta\omega_p$ can be computed from the transcendental equation

$$4\text{arctg}(\Delta\omega_p/\omega_3) = \text{arctg}(4\Delta\omega_p/\omega_c). \tag{90}$$

Using the addition theorem of the tangent function

$$\text{tg}(4\alpha) = \frac{(1 - \text{tg}^2\alpha)4\text{tg}\alpha}{1 - 6\text{tg}^2\alpha + \text{tg}^4\alpha},$$

the term $4\text{arctg}(\Delta\omega_p/\omega_3)$ can be replaced by

$$\text{arctg} \frac{[1 - (\frac{\Delta\omega_p}{\omega_3})^2] 4 \frac{\omega_p}{\omega_3}}{1 - 6(\frac{\Delta\omega_p}{\omega_3})^2 + (\frac{\Delta\omega_p}{\omega_3})^4}.$$

Eq. (90) then takes form

$$\text{arctg} \frac{[1 - (\frac{\Delta\omega_p}{\omega_3})^2] 4 \frac{\omega_p}{\omega_3}}{1 - 6(\frac{\Delta\omega_p}{\omega_3})^2 + (\frac{\Delta\omega_p}{\omega_3})^4} = \text{arctg} \frac{4\Delta\omega_p}{\omega_c}.$$

When the arctg expressions on both sides are equal, the arguments must be identical as well, hence we get

$$\frac{[1 - (\frac{\Delta\omega_p}{\omega_3})^2] 4 \frac{\omega_p}{\omega_3}}{1 - 6(\frac{\Delta\omega_p}{\omega_3})^2 + (\frac{\Delta\omega_p}{\omega_3})^4} = \frac{4\Delta\omega_p}{\omega_c}.$$

Solving for $\Delta\omega_p$ yields

$$\Delta\omega_p = \omega_3 \sqrt{\frac{6 - \frac{\omega_c}{\omega_3} - \sqrt{[6 - \frac{\omega_c}{\omega_3}]^2 - 4(1 - \frac{\omega_c}{\omega_3})}}{2}}. \tag{91}$$

Last, equation for the pull-in time T_p will be derived. Based on the nonlinear model shown in Fig. 11 and in Eqs. (26), (29), and (88) we can create a differential equation for the instantaneous

difference frequency $\Delta\omega$ as a function of time. For this type of Costas loop the differential equation has the form

$$\frac{d}{dt} \Delta\omega \tau_1 + \frac{\cos \varphi_{tot}}{\Delta\omega} 0.373^2 K_0^2 K_d^2 K_H = 0$$

with

$$\cos \varphi_{tot} = -4 \text{arctg} \frac{\Delta\omega}{\omega_3} - \frac{\pi}{2} + \text{arctg} \Delta\omega \omega_c.$$

Also here the cos term can be replaced by

$$\cos \varphi_{tot} \approx 1 - \frac{\Delta\omega}{\Delta\omega_p}$$

and, using similar procedures as in previews section, we get for the pull-in time

$$T_p \approx \frac{\Delta\omega_p}{0.278\zeta\omega_3^2} \left[\Delta\omega_p \ln \frac{\Delta\omega_p - \Delta\omega_L}{\Delta\omega_p - \Delta\omega_0} - \Delta\omega_0 + \Delta\omega_L \right], \tag{92}$$

which again is valid for initial frequency offsets in the range $\Delta\omega_L < \Delta\omega_0 < \Delta\omega_p$. For lower frequency offsets, a fast pull-in process will occur, and Eq. (87) should be used.

3.3. Numerical example: Designing a digital Costas loop for QPSK

A digital Costas loop for QPSK shall be designed in this section. It is assumed that two binary signals (I and Q) are modulated with a quadrature carrier (cosine and sine carrier). The carrier frequency is set to 400 kHz, i.e. the Costas loop will operate at a center frequency $\omega_0 = 2\pi \cdot 400,000 = 2,512,000 \text{ rad s}^{-1}$. The symbol rate is assumed to be $f_s = 100,000$ symbols/s. Now the parameters of the loop (such as time constants τ_1 and τ_2 , corner frequencies ω_c and ω_3 , and gain parameters such as K_0 , K_d) must be determined. (Note that these parameters have been defined in Eqs. (14)–(16) and (62)). It is possible to use the same parameters as for digital

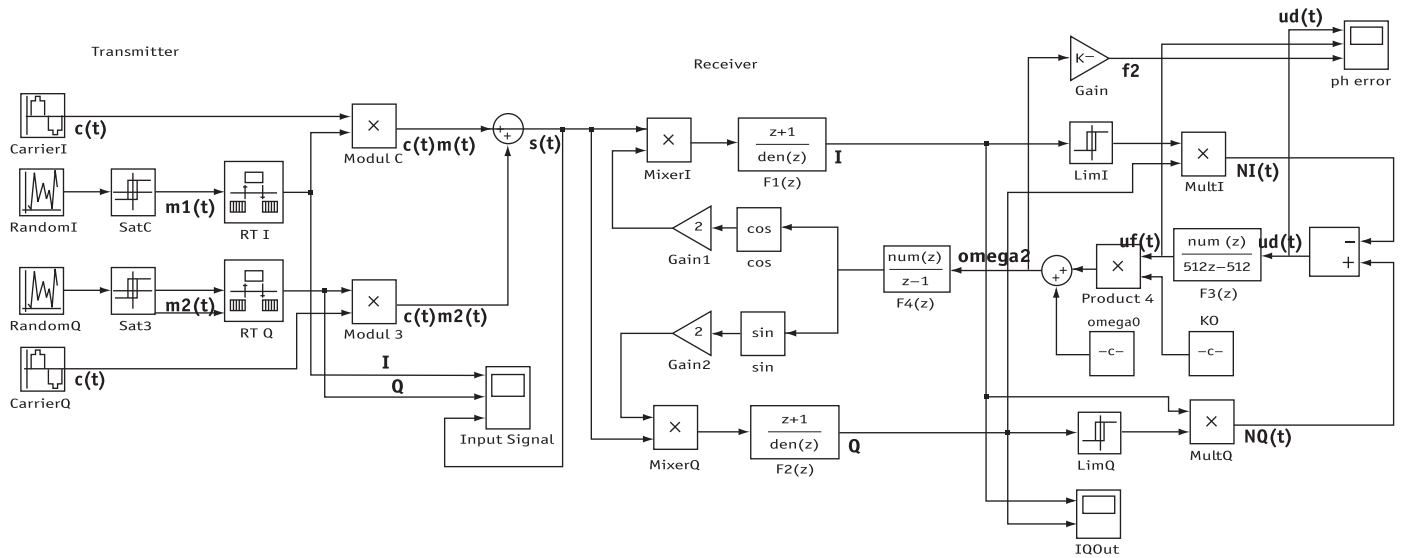


Fig. 3.4. Simulink model of the digital Costas loop for QPSK.

BPSK, i.e.

$$m_1 \equiv m_2 \equiv 1,$$

$$K_d = 2,$$

$$G_{OL}(s) = \frac{K_0 K_d}{s} \frac{1 + s/\omega_c}{s\tau_1} \frac{1}{1 + s/\omega_3},$$

$$\omega_T = 251,200,$$

$$\tau_2 = 4 \mu s,$$

$$\omega_3 = 2 * 2\pi * 100,000 = 1,256,000,$$

$$\tau_1 = 20 \mu s,$$

$$K_0 = 631,000 s^{-1},$$

$$\omega_n = 251,000 \text{ rad/s} \quad (f_n = 40 \text{ kHz})$$

$$\zeta = 0.5,$$

$$\Delta\omega_L = 177,483 \text{ rads} \quad (\Delta f_L = 20 \text{ kHz}).$$

From (86) the lock-in range becomes

$$\Delta\omega_L = 177,483 \text{ rads} \quad (\Delta f_L = 20 \text{ kHz})$$

and from (87) the lock time becomes

$$T_L = 25 \mu s.$$

Next we want to compute the pull-in range. Eq. (91) yields $\Delta f_p = 73 \text{ kHz}$. In Section 3.4 we will simulate this Costas loop and compare the results of the simulation with the predicted ones.

In digital domain $f_{samp} = 8$ and $f_0 = 3.2 \text{ MHz}$. Transfer functions $H_{LPF}(z)$, H_{VCO} and $H_{LF}(z)$ are defined in (81)–(83). A Simulink model will be presented in Section 3.4.

3.4. Simulating the digital Costas loop for QPSK

A Simulink model of a Costas loop for QPSK is shown in Fig. 3.4. Two data signals (I and Q) is created by random number generators at the left of the block diagram. The other blocks are self explanatory. The model is used now to check the validity of the approximations found for pull-in range and pull-in time.

Eq. (91) predicts a pull-in range $\Delta f_p = 73 \text{ kHz}$. The simulations reveals a value of 62 kHz . A series of other simulations delivered results for the pull-in time ΔT_p . The results are listed in Table 3.1.

At higher frequency offsets the results of the simulation are in good agreement with the predicted ones. The pull-in time for an initial frequency offset of 40 kHz is too low, however, but it should

Table 3.1

Comparison of predicted and simulated results for the pull-in range.

Δf_0 (Hz)	$\Delta\omega_0$ (rad s^{-1})	T_p (theory) (μs)	T_p (simulation) (μs)
40 kHz	251,200	14	35
50 kHz	314,000	37	40
60 kHz	376,800	86	70

be noted that the lock time T_L is about $25 \mu s$, and the total pull-in time cannot be less than the lock time.

3.5. Remarks on simulation of QPSK Costas loop

Similar problems to BPSK Costas loop simulation also exist for QPSK. Different mathematical models can give qualitatively different results, which shows the importance of analytical methods in studying QPSK Costas loops.

4. Modified Costas loop for BPSK

4.1. Lock-in range $\Delta\omega_L$ and lock time T_L

From the model of Fig. 6 with $K_d = 1$ the open loop transfer function is determined as

$$G_{OL}(s) = \frac{K_0}{s} \frac{1 + s\tau_2}{s\tau_1}. \quad (94)$$

Since open loop transfer function of Modified Costas loop is effectively the same as (54), linear analysis is the same as for BPSK Costas loop. Therefore transfer function in normalized form is equal to

$$G_{CS}(s) = \frac{2s\zeta\omega_n + \omega_n^2}{s^2 + 2C\zeta\omega_n + \omega_n^2},$$

where

$$\omega_n = \sqrt{\frac{K_0}{\tau_1}}, \quad \zeta = \frac{\omega_n\tau_2}{2}. \quad (95)$$

Here ω_n is natural frequency and ζ is damping factor.

For the following analysis we assume that the loop is initially out of lock. The frequency of the reference signal (Fig. 17) is ω_1 , and the frequency of the VCO is ω_2 . The output signal of multiplier M_1 is then a phasor rotating with angular velocity $\Delta\omega = \omega_1 - \omega_2$.

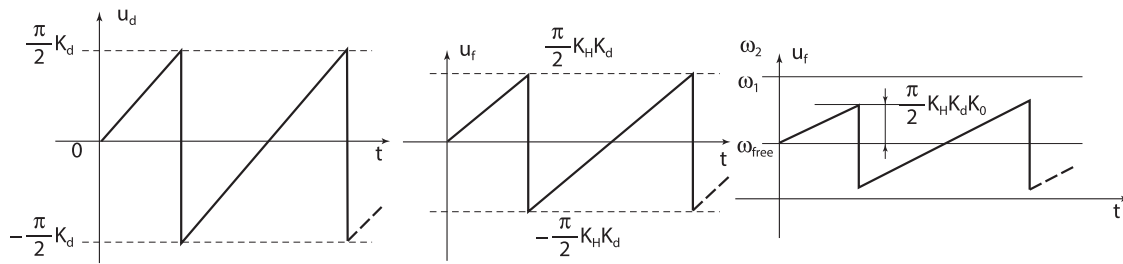


Fig. 4.1. Signals u_d , u_f , and ω_2 during the lock process.

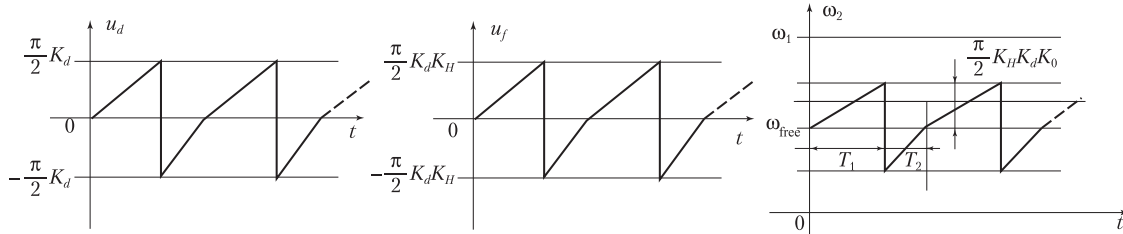


Fig. 4.2. Pull-in process of the modified Costas loop for BPSK.

Consequently the phase output of block “Complex \rightarrow mag, phase” is a sawtooth signal having amplitude $(\pi/2) K_d$ and fundamental frequency $2\Delta\omega$, as shown in the left trace of Fig. 4.1. Because $2\Delta\omega$ is usually much higher than the corner frequency ω_c of the loop filter, the transfer function of the loop filter at higher frequencies can be approximated again by

$$H_{LF}(\omega) \approx \frac{\tau_2}{\tau_1} = K_H.$$

The output signal u_f of the loop filter is a sawtooth signal and has amplitude $(\pi/2) K_d K_H$, as shown in the middle trace of the Fig. 4.1. This signal modulates the frequency ω_2 generated by the VCO. The modulation amplitude is given by $(\pi/2) K_d K_H K_0$, cf. right trace. The Costas loop spontaneously acquires lock when the peak of the ω_2 waveform touches the ω_1 line, hence we have

$$\Delta\omega_L = \frac{\pi}{2} K_d K_0 K_H = \frac{\pi}{2} K_d K_0 \frac{\tau_2}{\tau_1}.$$

Making use of the substitutions Eq. (95) this can be rewritten as

$$\Delta\omega_L = \pi \zeta \omega_n. \tag{96}$$

Because the lock process is a damped oscillation having frequency ω_n the lock time can be approximated by one cycle of this oscillation, i.e.

$$T_L \approx \frac{2\pi}{\omega_n}. \tag{97}$$

4.2. Pull-in range and pull-in time of the modified Costas loop for BPSK

Assume that the loop is not yet locked, and $\Delta\omega = \omega_1 - \omega_2$. As shown in Section 4.1 (cf. also Fig. 4.1) u_d is a sawtooth signal having frequency $2\Delta\omega$, cf. left trace in Fig. 4.2. As will be explained in short, this signal is asymmetrical, i.e. the duration of the positive wave T_1 is not identical with the duration T_2 of the negative. The middle trace shows the output signal of the loop filter, and the right trace shows the modulation of the VCO output frequency ω_2 . From this waveform it is seen that during T_1 the average frequency difference $\Delta\omega$ becomes smaller, but during interval T_2 it becomes larger. Consequently the duration of T_1 is longer than the duration of T_2 , and the average of signal u_d is nonzero and positive. Using

the same mathematical procedure as in previews sections, the average \bar{u}_d can be computed from

$$\bar{u}_d = \frac{\pi^2 K_d K_0 K_H}{8 \Delta\omega}. \tag{98}$$

Because this type of Costas loop does not require an additional lowpass filter, the u_d signal is not shifted in phase, and therefore there is no cos term in Eq. (98). This implies that there is no polarity reversal in the function $\bar{u}_d(\Delta\omega)$, hence the pull-in range becomes theoretically infinite. Of course, in a real circuit the pull-in range will be limited by the frequency range of the VCO is capable to generate. When the center frequency f_0 of the loop is 10 MHz, for example, and when the VCO can create frequencies in the range from 0...20 MHz, then the maximum pull-in range Δf_p is 10 MHz, i. e. $\Delta\omega_p = 6.28 \cdot 10^6$ rad/s.

As seen in the last section, the pull-in range of this type of Costas loop can be arbitrarily large. Using the same model as for BPSK Costas loop (see Fig. 11), we can derive an equation for the pull-in time:

$$T_p \approx \frac{2 \Delta\omega_0^2}{\pi^2 \zeta \omega_n^3}. \tag{99}$$

4.3. Designing a digital modified Costas loop for BPSK

The following design is based on the method we already used in Section 2.3. It is assumed that a binary signal l is modulated onto a carrier. The carrier frequency is set to 400 kHz, i.e. the Costas loop will operate at a center frequency $\omega_0 = 2\pi \cdot 400,000 = 2,512,000$ rad s⁻¹. The symbol rate is assumed to be $f_s = 100,000$ symbols/s. Now the parameters of the loop (such as time constants τ_1 and τ_2 , corner frequency ω_c , and gain parameters such as K_0 , K_d) must be determined. (Note that these parameters have been defined in Eqs. (14)–(16), and (62)).

It has been shown in Section 4.1 that for this type of Costas loop $K_d = 1$. The modulation amplitudes m_1 and m_2 are set to 1. It has proven advantageous to determine the remaining parameters by using the open loop transfer function $G_{OL}(s)$ of the loop, which is given here by (94). The magnitude of $G_{OL}(\omega)$ has been shown in Fig. 2.1. As already explained in Section 2.3 the magnitude curve

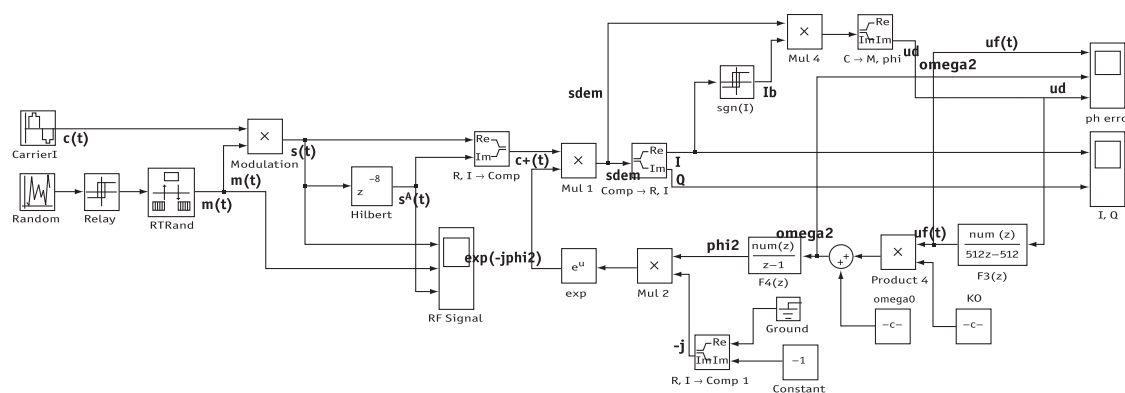


Fig. 4.3. Simulink model of the modified Costas loop for BPSK.

Table 4.1

Comparison of predicted and simulated results for the pull-in range.

Δf_0 (Hz)	$\Delta \omega_0$ (rad s ⁻¹)	T_p (theory) (μ s)	$(T_p$ (simulation) (μ s)
50 kHz	314,200	2.5	20
100 kHz	628,000	10	20
200 kHz	1,256,000	40	50

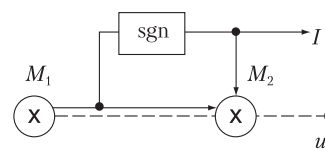


Fig. 4.4. Obtaining the phase error signal from multiplier M_2 .

crosses the 0 dB line at the transit frequency ω_T . As in the case of the conventional Costas loop for BPSK/QPSK, we again set

$$\begin{aligned}
 \omega_T &= 0.1\omega_0, \\
 \omega_T &= 251,200 \text{ rad s}^{-1}, \\
 G_{OL}(\omega) &= -135^\circ, \\
 \tau_2 &= 4\mu\text{s}, \\
 \tau_1 &= 20\mu\text{s}, \\
 K_0 &= 1,262,000 \text{ s}^{-1}.
 \end{aligned}
 \tag{100}$$

For the natural frequency and damping factor we get from Eq. (58)

$$\begin{aligned}
 \omega_n &= 251,000 \text{ rad s}^{-1} \quad (f_n = 40 \text{ kHz}) \\
 \zeta &= 0.5.
 \end{aligned}
 \tag{101}$$

From (95) lock-in range is as of

$$\Delta\omega_L = 394,000 \text{ rad s}, \quad \Delta f_L = 62.7 \text{ kHz}, \quad T_L = 25 \mu\text{s}.
 \tag{102}$$

As done in Section 2.4 a suitable sampling frequency f_{samp} must be chosen for z-domain. As shown previously f_{samp} must be greater than 4 times the center frequency of the Costas loop. Therefore $f_{samp} = 8$, $f_0 = 3.2$ MHz. The transfer functions of the loop filter and VCO are the same as (82) and (83).

The digital Costas loop is ready now for implementation. A Simulink model will be presented in Section 4.4.

4.4. Simulating the modified digital Costas loop for BPSK

Fig. 4.3 shows the Simulink model of the Costas loop. Table 4.1 lists a number of results for the pull-in time T_p .

The predictions for $\Delta f_0 = 50$ kHz and 100 kHz are too low. As already mentioned in Section 3.4 the pull-in time cannot be lower than the lock time, and the latter is estimated $\approx 25 \mu\text{s}$. The simulation results for these two difference frequencies are around $20 \mu\text{s}$, which roughly corresponds to the lock time. The simulation result

for a frequency difference of 200 kHz comes close to the predicted value.

4.5. Pull-in time for an alternative structure of the modified Costas loop for BPSK

As demonstrated in Fig. 17 the phase error signal u_d was obtained from the phase output of block “Complex \rightarrow mag, phase”. The phase of the complex input signal to this block can be obtained from the arc tg function. This imposes no problem when a processor is available. This is the case in most digital implementations of the Costas loop. As an alternative a phase error signal can also be obtained directly from the imaginary part of multiplier M_2 ; this is shown in Fig. 4.4.

It is easily seen that here u_d is given by

$$u_d = m(t) \sin(\theta_e).$$

The blocks shown in Fig. 4.4 therefore represent a phase detector having gain $K_d = m$. In the cases when $m \neq 1$ this must be taken in account when specifying the open loop transfer function, cf. Section 4.3. For this design the pull-in time of the loop is given by

$$T_p \approx \frac{\pi^2}{16} \frac{\Delta\omega_0^2}{\zeta\omega_n^3}.$$

4.6. A note on the design of Hilbert transformers

Hilbert transformers used in the system of Fig. 17 are implemented in most cases by digital filters. In this application the maximum frequency in the spectrum of the modulating signal $m_1(t)$ is much lower than the carrier frequency f_1 . Under this condition the Hilbert transformer can be replaced by a simple delay block. All we have to do is to shift the input signal $u_1(t)$ by one quarter of a period of the carrier. When the sampling frequency f_s is n times the carrier frequency f_1 , we would shift the input signal by $n/4$ samples. This implies that n must be an integer multiple of 4.

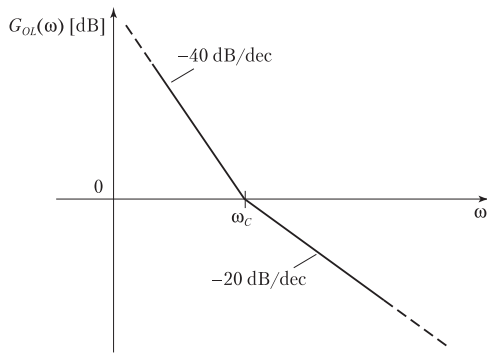


Fig. 5.1. Bode plot of magnitude of open loop gain $G_{OL}(\omega)$ for QPSK.

5. Modified Costas loop for QPSK

5.1. Lock-in range $\Delta\omega_L$ and lock time T_L

From the model of Fig. 6 the open loop transfer function is determined as

$$G_{OL}(s) = \frac{K_0}{s} \frac{1 + s\tau_2}{s\tau_1}, \tag{103}$$

as explained in Section 1.2.2.

Fig. 5.1 shows a Bode plot of the magnitude of G_{OL} . The plot is characterized by the corner frequency ω_c , which is defined by $\omega_c = 1/\tau_2$ and gain parameters K_d and K_0 . At lower frequencies the magnitude rolls off with a slope of -40 dB/decade. At frequency ω_c the zero of the loop filter causes the magnitude to change its slope to -20 dB/decade. To get a stable system, the magnitude curve should cut the 0 dB line with a slope that is markedly less than -40 dB/decade. Setting the parameters such that the gain is just 0 dB at frequency ω_c provides a phase margin of 45° , which assures stability [2]. From the open loop transfer function we now can calculate the closed loop transfer function defined by

$$G_{CL}(s) = \frac{\Theta_2(s)}{\Theta_1(s)}.$$

After some mathematical manipulations we get

$$G_{CL}(s) = \frac{K_0 K_d \frac{1+s\tau_2}{s\tau_1}}{s^2 + s \frac{K_0 K_d \tau_2}{\tau_1} + \frac{K_0 K_d}{\tau_1}}.$$

It is naturally to represent this transfer function in normalized form, i.e.

$$G_{CS}(s) = \frac{2s\zeta\omega_n + \omega_n^2}{s^2 + 2s\zeta\omega_n + \omega_n^2}$$

with the substitutions

$$\omega_n = \sqrt{\frac{K_0 K_d}{\tau_1}}, \quad \zeta = \frac{\omega_n \tau_2}{2}, \tag{104}$$

where ω_n is called natural frequency and ζ is called damping factor. The linear model enables us to derive simple expressions for lock-in range $\Delta\omega_L$ and lock time T_L .

For the following analysis we assume that the loop is initially out of lock. The frequency of the reference signal (Fig. 19) is ω_1 , and the frequency of the VCO is ω_2 . The output signal of multiplier M_1 is then a phasor rotating with angular velocity $\Delta\omega = \omega_1 - \omega_2$. Consequently the phase output of block “Complex \rightarrow mag, phase is a sawtooth signal having amplitude $(\pi/4) K_d$ and fundamental frequency $4 \Delta\omega$, as shown in the left trace of Fig. 5.2. Because $4 \Delta\omega$ is usually much higher than the corner frequency ω_c of the loop filter, the transfer function of the loop filter at higher frequencies

can be approximated again by

$$H_{LF}(\omega) \approx \frac{\tau_2}{\tau_1} = K_H.$$

The output signal u_f of the loop filter is a sawtooth signal as well and has amplitude $(\pi/4) K_d K_H$, as shown in the middle trace of the figure. This signal modulates the frequency ω_2 generated by the VCO. The modulation amplitude is given by $(\pi/4) K_d K_H K_0$, cf. right trace. The Costas loop spontaneously acquires lock when the peak of the ω_2 waveform touches the ω_1 line, hence we have

$$\Delta\omega_L = \frac{\pi}{4} K_d K_0 K_H = \frac{\pi}{4} K_d K_0 \frac{\tau_2}{\tau_1}. \tag{105}$$

Making use of the substitutions Eq. (95), this can be rewritten as

$$\Delta\omega_L = \frac{\pi}{2} \zeta \omega_n. \tag{106}$$

Because the lock process is a damped oscillation having frequency ω_n , the lock time can be approximated by one cycle of this oscillation, i.e.

$$T_L \approx \frac{2\pi}{\omega_n}. \tag{107}$$

5.2. Pull-in range and pull-in time of the modified Costas loop for QPSK

Assume that the loop is not yet locked, and that the difference between reference frequency ω_1 and VCO output frequency ω_2 is $\Delta\omega = \omega_1 - \omega_2$. As shown in Section 5.1 (cf. also Fig. 5.2) u_d is a sawtooth signal having frequency $4\Delta\omega$, cf. left trace in Fig. 5.3.

As will be explained in short, this signal is asymmetrical, i.e. the duration of the positive wave T_1 is not identical with the duration T_2 of the negative one. The middle trace shows the output signal of the loop filter, and the right trace shows the modulation of the VCO output frequency ω_2 . From this waveform it is seen that during T_1 the average frequency difference $\Delta\omega$ becomes smaller, but during interval T_2 it becomes larger. Consequently the duration of T_1 is longer than the duration of T_2 , and the average of signal u_d is nonzero and positive. Using the same mathematical procedure as in Sections 2.3 and 3.3 the average \bar{u}_d can be computed from

$$\bar{u}_d = \frac{\pi^2 K_d^2 K_0 K_H}{64 \Delta\omega}. \tag{108}$$

Because this type of Costas loop does not require an additional lowpass filter, the u_d signal is not shifted in phase, and therefore there is no \cos term in Eq. (108). This implies that there is no polarity reversal in the function $\bar{u}_d(\Delta\omega)$, hence the pull-in range becomes theoretically infinite. Of course, in a real circuit the pull-in range will be limited by the frequency range of the VCO is capable to generate. When the center frequency f_0 of the loop is 10 MHz, for example, and when the VCO can create frequencies in the range from $0 \dots 20$ MHz, then the maximum pull-in range Δf_p is 10 MHz, i. e. $\Delta\omega_p = 6.28 \cdot 10^6 \text{ rad}^{-1}$.

As seen in the last section, the pull-in range of this type of Costas loop can be arbitrarily large. Using nonlinear model (11) we can derive an equation for the pull-in range:

$$\Delta\omega_p \approx \frac{16}{\pi^2} \frac{\Delta\omega_0^2}{\zeta \omega_n^3}. \tag{109}$$

5.3. Designing a digital modified Costas loop for QPSK

The following design is based on the method we already used in Section 4.3. It is assumed that two binary signals (I and Q) are modulated onto a quadrature carrier (cosine and sine carrier). The

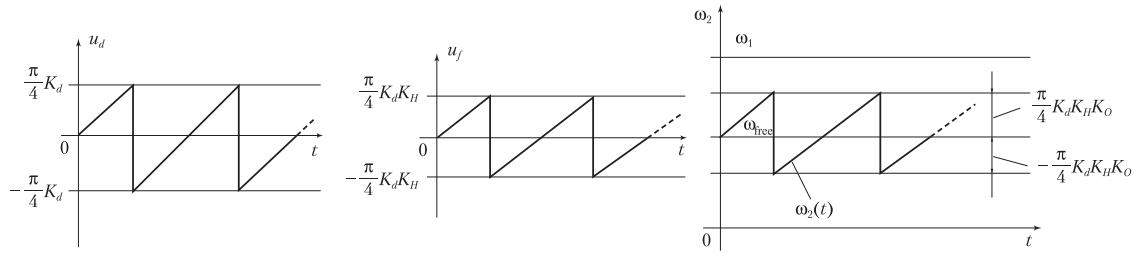


Fig. 5.2. Signals u_d , u_f , and ω_2 during the lock process.

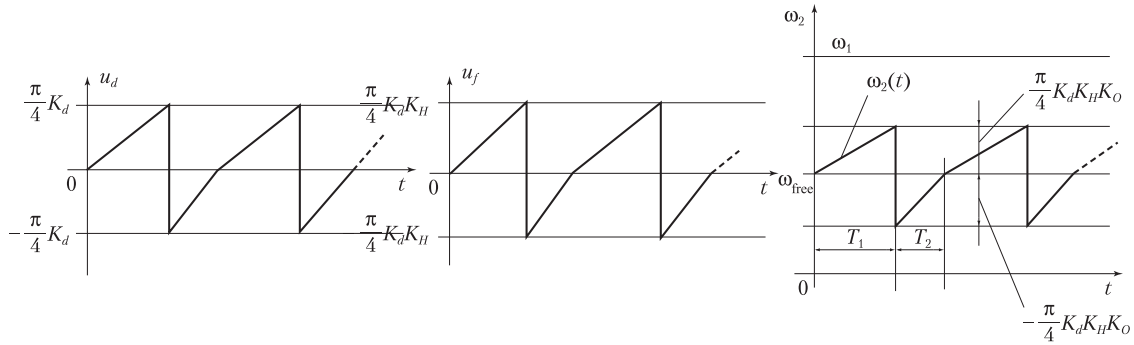


Fig. 5.3. Pull-in process of the modified Costas loop for QPSK.

Table 5.1

Comparison of predicted and simulated results for the pull-in range.

Δf_0 (Hz)	$\Delta \omega_0$ (rad s ⁻¹)	T_p (theory) (μ s)	$(T_p$ (simulation) (μ s)
50 kHz	314,200	20	20
100 kHz	628,000	81	80
200 kHz	1,256,000	327	300

carrier frequency is set to 400 kHz, i.e. the Costas loop will operate at a center frequency $\omega_0 = 2\pi 400,000 = 2,512,000 \text{ rad s}^{-1}$. The symbol rate is assumed to be $f_s = 100,000$ symbols/s. Now the parameters of the loop (such as time constants τ_1 and τ_2 , corner frequency ω_C , and gain parameters such as K_0 , K_d) must be determined. (Note that these parameters have been defined in Eqs. (14)–(16), and (62)).

It has been shown in previous sections that for this type of Costas loop $K_d = 1$. The modulation amplitudes m_1 and m_2 are set to 1. It was proven advantageous to determine the remaining parameters by using the open loop transfer function $G_{OL}(s)$ of the loop, which is given here by (103). The magnitude of $G_{OL}(\omega)$ has been shown in Fig. 5.1. As already explained in Section 2.3 the magnitude curve crosses the 0 dB line at the transit frequency ω_T . We again set parameters as in (100), (101) and (102). A Simulink model will be presented in Section 5.4.

5.4. Simulating the digital Costas loop for QPSK

Fig. 5.4 shows the Simulink model of the Costas loop. Table 5.1 lists a number of results for the pull-in time T_p .

The predictions come very close to the results obtained from the simulation.

5.5. An alternative structure of the modified Costas loop for BPSK

As demonstrated in Fig. 19 the phase error signal u_d was obtained from the phase output of block “Complex \rightarrow mag, phase”. The phase of the complex input signal to this block can be obtained from the arctg function. This imposes no problem when a

processor is available. This is the case in most digital implementations of the Costas loop. As an alternative a phase error signal can also be obtained directly from the imaginary part of multiplier M_2 ; this is shown in Fig. 5.5.

It is easily seen that here u_d is given by

$$u_d = 2m \sin(\theta_e).$$

The blocks shown in Fig. 5.5 therefore represent a phase detector having gain $K_d = 2m$. This must be taken in account when specifying the open loop transfer function, cf. Section 5.3. For this design the pull-in time of the loop is given by

$$T_p \approx \frac{1.78 \Delta \omega_0^2}{\zeta \omega_n^3}.$$

Acknowledgements

This work was supported by Russian Science Foundation (project 14-21-00041) and Saint-Petersburg State University

Appendix A

A1. Hold-in range for lead-lag filter

One needs to be cautious using model in Fig. 5 even for calculating hold-in range for BPSK Costas. Consider an example: Costas loop with lead-lag loop filter

$$F(s) = \frac{1 + s\tau_2}{1 + s\tau_1}, \quad \tau_1 > \tau_2 > 0 \tag{110}$$

and low-pass filters LPFs

$$H_{LPF}(s) = \frac{1}{1 + \frac{s}{\omega_3}}, \quad \omega_3 > 0. \tag{111}$$

In locked state phase error θ_e satisfies

$$\frac{\Delta \omega}{K_0 K_d} = \frac{\sin(2\theta_e)}{2}, \tag{112}$$

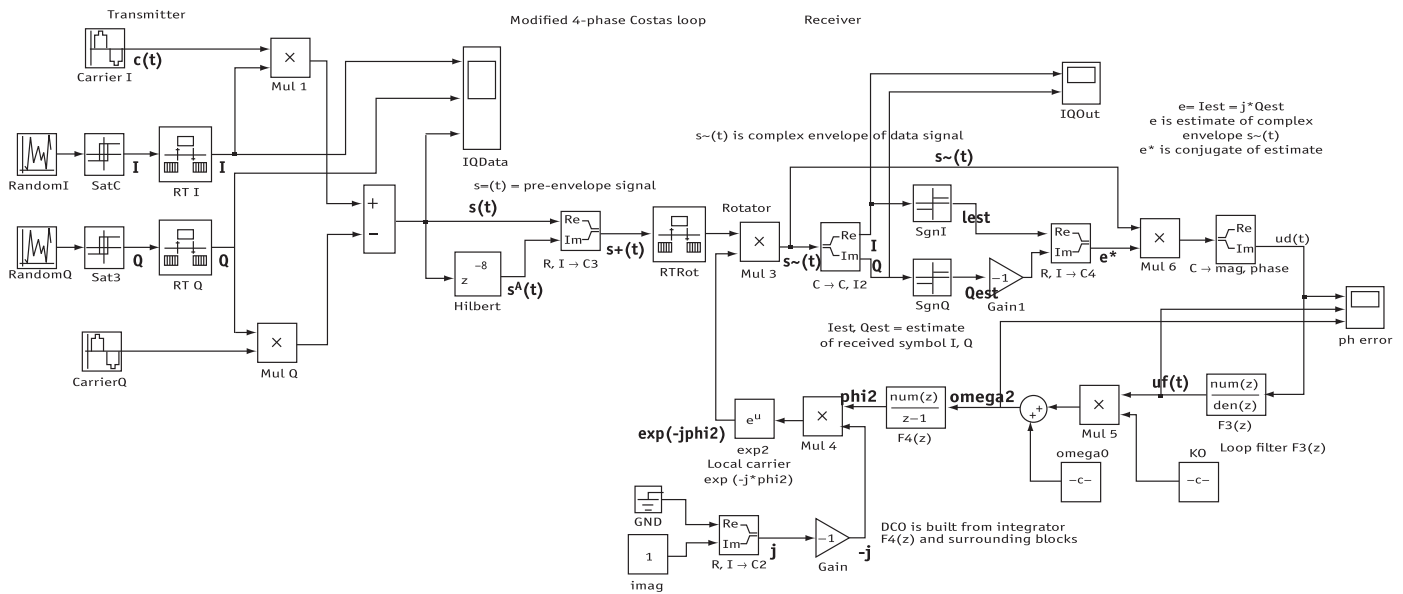


Fig. 5.4. Simulink model of the modified Costas loop for QPSK.

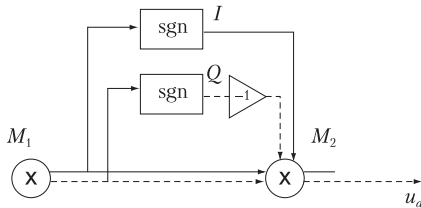


Fig. 5.5. Obtaining the phase error signal from multiplier M_2 .

therefore we get a bound for the hold-in range

$$|\Delta\omega_0| < \frac{K_0 K_d}{2}. \quad (113)$$

In order to find hold-in range we need to find poles of the closed-loop transfer function (roots of the characteristic polynomial) for the linearized model (small-signal model) of the system in Fig. 4. Open-loop transfer function is as follows

$$G_{OL} = \frac{K_0 K_d}{s} \frac{1 + s\tau_2}{1 + s\tau_1} \frac{1}{1 + \frac{s}{\omega_3}} \frac{\cos(2\theta_{eq})}{2} \quad (114)$$

$$\frac{1}{2} (1 + \tau_2 s) K_0 K_d \cos(2\theta_{eq}) + s(1 + \frac{s}{\omega_3})(1 + \tau_1 s). \quad (115)$$

Phase error θ_{eq} corresponds to hold-in range (see (112)) if all roots of the polynomial (115) have negative real parts (i.e. polynomial (115) is stable). Applying Routh-Hurwitz criterion to study stability of the polynomial, we get that for the following parameters

$$\omega_3 \geq \frac{\tau_1 - \tau_2}{\tau_1 \tau_2}, \quad (116)$$

polynomial (115) is stable for all $|\Delta\omega_0| < \frac{K_0 K_d}{8}$. However, if

$$\omega_3 < \frac{\tau_1 - \tau_2}{\tau_1 \tau_2} \quad (117)$$

the following condition is necessary for stability:

$$\cos(2\theta_{eq}) < \frac{2}{K_0 K_d} \left(\frac{-1 - \omega_3 \tau_1}{-\tau_1 + \tau_2 + \omega_3 \tau_1 \tau_2} \right). \quad (118)$$

Then, taking into account static phase error in Eq. (112), we get different hold-in ranges for different values of ω_3

$$\left[\begin{array}{l} \frac{K_0 K_d}{4} \sqrt{1 - \left(\frac{2}{K_0 K_d} \left(\frac{-1 - \omega_3 \tau_1}{-\tau_1 + \tau_2 + \omega_3 \tau_1 \tau_2} \right) \right)^2} < |\Delta\omega_0| < \frac{K_0 K_d}{4}, \\ \text{if } \omega_3 < \frac{\tau_1 - \tau_2}{\tau_1 \tau_2}, \left| \frac{2}{K_0 K_d} \left(\frac{-1 - \omega_3 \tau_1}{-\tau_1 + \tau_2 + \omega_3 \tau_1 \tau_2} \right) \right| < 1, \\ |\Delta\omega_0| < \frac{K_0 K_d}{4}, \text{ if } \omega_3 < \frac{\tau_1 - \tau_2}{\tau_1 \tau_2}, \left| \frac{2}{K_0 K_d} \left(\frac{-1 - \omega_3 \tau_1}{-\tau_1 + \tau_2 + \omega_3 \tau_1 \tau_2} \right) \right| > 1, \\ |\Delta\omega_0| < \frac{K_0 K_d}{4}, \text{ if } \omega_3 \geq \frac{\tau_1 - \tau_2}{\tau_1 \tau_2}. \end{array} \right. \quad (119)$$

References

- Abramovitch, D. (2002). Phase-locked loops: A control centric tutorial. In *American control conf. proc., vol. 1, IEEE* (pp. 1–15).
- Baker, R. (2011). CMOS: Circuit design, layout, and simulation. *IEEE press series on microelectronic systems*. Wiley-IEEE Press.
- Bakshi, U., & Godse, A. (2010). *Analog communication*. Technical Publications.
- Best, R. (1984). *Phase-locked loops: Design, simulation, and applications*. McGraw Hill.
- Best, R. (2007). *Phase-Lock loops: Design, simulation and application* (6th ed.). McGraw-Hill.
- Best, R., Kuznetsov, N., Kuznetsova, O., Leonov, G., Yuldashev, M., & Yuldashev, R. (2015). A short survey on nonlinear models of the classic costas loop: Rigorous derivation and limitations of the classic analysis. In *Proceedings of the american control conference, IEEE* (pp. 1296–1302). doi:10.1109/ACC.2015.7170912. art. num. 7170912, <http://arxiv.org/pdf/1505.04288v1.pdf>.
- Best, R., Kuznetsov, N., Leonov, G., Yuldashev, M., & Yuldashev, R. (2014). Simulation of analog costas loop circuits. *International Journal of Automation and Computing*, 11(6), 571–579. doi:10.1007/s11633-014-0846-x.
- Bianchi, G. (2005). *Phase-locked loop synthesizer simulation*. McGraw-Hill, Inc.
- Bianchi, G., Kuznetsov, N., Leonov, G., Seledzhi, S., Yuldashev, M., & Yuldashev, R. (2016a). Hidden oscillations in SPICE simulation of two-phase costas loop with non-linear VCO. *IFAC-PapersOnLine*, 49(14), 45–50. doi:10.1016/j.ifacol.2016.07.973. URL <http://www.sciencedirect.com/science/article/pii/S2405896316312617>.
- Bianchi, G., Kuznetsov, N., Leonov, G., Yuldashev, M., & Yuldashev, R. (2016b). Limitations of PLL simulation: Hidden oscillations in MATLAB and SPICE. In *International congress on ultra modern telecommunications and control systems and workshops (ICUMT 2015) 2016-january* (pp. 79–84). doi:10.1109/ICUMT.2015.7382409.
- Bizzarri, F., Brambilla, A., & Gajani, G. S. (2012). Periodic small signal analysis of a wide class of type-II phase locked loops through an exhaustive variational model. *Circuits and Systems I: Regular Papers, IEEE Transactions on*, 59(10), 2221–2231.
- Cahn, C. R. (1977). Improving frequency acquisition of a costas loop. *Communications, IEEE Transactions on*, 25(12), 1453–1459.

- Costas, J. (1956). Synchronous communications. In *Proc. IRE*, vol. 44 (pp. 1713–1718).
- Costas, J. P. (1962). *Receiver for communication system*. US Patent 3,047,659.
- Couch, L. (2007). *Digital and analog communication systems* (7th ed.). Pearson/Prentice Hall.
- Craninckx, J., & Steyaert, M. (1998). *Wireless CMOS frequency synthesizer design*. Springer.
- De Muer, B., & Steyaert, M. (2003). *CMOS Fractional-N synthesizers: Design for high spectral purity and monolithic integration*. Springer.
- Dyer, S. (2004). *Wiley survey of instrumentation and measurement*. Wiley.
- Egan, W. (2007). *Phase-Lock basics*. Wiley-IEEE Press.
- Gardner, F. (1966). *Phase-lock techniques*. New York: John Wiley & Sons.
- Gardner, F. (1979). *Phase-lock techniques* (2nd ed.). New York: John Wiley & Sons.
- Gardner, F. (2005). *Phaselock techniques* (3rd ed.). Wiley.
- Goldman, S. (2007). *Phase-Locked loops engineering handbook for integrated circuits*. Artech House.
- Hsieh, G.-C., & Hung, J. (1996). Phase-locked loop techniques. a survey, industrial electronics. *IEEE Transactions on*, 43(6), 609–615.
- Irwin, J. (1997). *The industrial electronics handbook*. Taylor & Francis.
- Kaplan, E., & Hegarty, C. (2006). *Understanding GPS: Principles and applications*. Artech House.
- Kihara, M., Ono, S., & Eskelinen, P. (2002). *Digital clocks for synchronization and communications*. Artech House.
- Kobayashi, F., Hara, S., & Tanaka, H. (1990). Reduction of motor speed fluctuation using repetitive control. In *Decision and control, 1990., proceedings of the 29th IEEE conference on* (pp. 1697–1702). IEEE.
- Kroupa, V. (2012). *Frequency stability: Introduction and applications. IEEE series on digital & mobile communication*. Wiley-IEEE Press.
- Kudryashova, E., Kuznetsova, O., Kuznetsov, N., Leonov, G., Seledzhi, S., Yuldashev, M., & Yuldashev, R. (2014). Nonlinear models of BPSK costas loop. In *ICINCO 2014 - Proceedings of the 11th International Conference on Informatics in Control, Automation and Robotics: 1* (pp. 704–710). doi:10.5220/0005050707040710.
- Kuznetsov, N., Kuznetsova, O., Leonov, G., Neittaanmaki, P., Yuldashev, M., & Yuldashev, R. (2014a). Simulation of nonlinear models of QPSK costas loop in matlab simulink. In *2014 6th international congress on ultra modern telecommunications and control systems and workshops (ICUMT), vol. 2015-january* (pp. 66–71). IEEE. doi:10.1109/ICUMT.2014.7002080.
- Kuznetsov, N., Kuznetsova, O., Leonov, G., Neittaanmaki, P., Yuldashev, M., & Yuldashev, R. (2015a). Limitations of the classical phase-locked loop analysis. In *Proceedings - IEEE international symposium on circuits and systems 2015-july* (pp. 533–536). doi:10.1109/ISCAS.2015.7168688.
- Kuznetsov, N., Kuznetsova, O., Leonov, G., Seledzhi, S., Yuldashev, M., & Yuldashev, R. (2014b). BPSK Costas loop: Simulation of nonlinear models in matlab simulink. In *2014 6th international congress on ultra modern telecommunications and control systems and workshops (ICUMT), vol. 2015-january* (pp. 83–87). IEEE. doi:10.1109/ICUMT.2014.7002083.
- Kuznetsov, N., Leonov, G., Neittaanmäki, P., Seledzhi, S., Yuldashev, M., & Yuldashev, R. (2012). Nonlinear mathematical models of costas loop for general waveform of input signal. In *IEEE 4th international conference on nonlinear science and complexity, NSC 2012 - proceedings* (pp. 109–112). doi:10.1109/NSC.2012.6304729.
- Kuznetsov, N., Leonov, G., Yuldashev, M., & Yuldashev, R. (2011). Analytical methods for computation of phase-detector characteristics and PLL design. In *ISSC 2011 - international symposium on signals, circuits and systems, proceedings* (pp. 7–10). IEEE. doi:10.1109/ISSC.2011.5978639.
- Kuznetsov, N., Leonov, G., Yuldashev, M., & Yuldashev, R. (2014c). Nonlinear analysis of classical phase-locked loops in signal's phase space. *IFAC-PapersOnline*, 47(3), 8253–8258. doi:10.3182/20140824-6-ZA-1003.02772.
- Kuznetsov, N., Leonov, G., Yuldashev, M., & Yuldashev, R. (2014d). Simulation of nonlinear models of modified BPSK costas loop for non sinusoidal waveforms in matlab simulink. In *2014 6th international congress on ultra modern telecommunications and control systems and workshops (ICUMT), vol. 2015-january* (pp. 88–94). IEEE. doi:10.1109/ICUMT.2014.7002084.
- Kuznetsov, N., Leonov, G., Yuldashev, M., & Yuldashev, R. (2015b). Rigorous mathematical definitions of the hold-in and pull-in ranges for phase-locked loops. *IFAC-PapersOnLine*, 48(11), 710–713. doi:10.1016/j.ifacol.2015.09.272.
- Lazzari, R., Parma, C., De Marco, A., & Bittanti, S. (2015). Enabling a flexible exchange of energy of a photovoltaic plant with the grid by means of a controlled storage system. *International Journal of Control*, 88(7), 1353–1365.
- Leonov, G., & Kuznetsov, N. (2013). Hidden attractors in dynamical systems. from hidden oscillations in hilbert-kolmogorov, aizerman, and kalman problems to hidden chaotic attractors in chua circuits. *International Journal of Bifurcation and Chaos*, 23(1). doi:10.1142/S0218127413300024. art. no. 1330002.
- Leonov, G., Kuznetsov, N., Yuldashev, M., & Yuldashev, R. (2012). Analytical method for computation of phase-detector characteristic. *IEEE Transactions on Circuits and Systems - II: Express Briefs*, 59(10), 633–647. doi:10.1109/TCSII.2012.2213362.
- Leonov, G., Kuznetsov, N., Yuldashev, M., & Yuldashev, R. (2015a). Hold-in, pull-in, and lock-in ranges of PLL circuits: Rigorous mathematical definitions and limitations of classical theory. *IEEE Transactions on Circuits and Systems-I: Regular Papers*, 62(10), 2454–2464. doi:10.1109/TCSI.2015.2476295.
- Leonov, G., Kuznetsov, N., Yuldashev, M., & Yuldashev, R. (2015b). Nonlinear dynamical model of costas loop and an approach to the analysis of its stability in the large. *Signal processing*, 108, 124–135. doi:10.1016/j.sigpro.2014.08.033.
- Proakis, J., & Salehi, M. (2008). *Digital communications*. McGraw-Hill Higher Education.
- Rantzer, A. (2001). Almost global stability of phase-locked loops. In *Proceedings of the IEEE conference on decision and control, vol. 1* (pp. 899–900).
- Rohde, U., Whitaker, J., & Bateman, A. (2000). *Communications receivers: DPS, software radios, and design* (3rd ed.). McGraw-Hill.
- Shakhgil'dyan, V., & Lyakhovkin, A. (1966). *Fazovaya avtopodstroika chastoty (in russian)*. Moscow: Svyaz'.
- Shu, K., & Sanchez-Sinencio, E. (2005). *CMOS PLL Synthesizers: Analysis and design*. Springer.
- Sidorkina, Y. A., Szykh, V., Shakhtarin, B., & Shevtsev, V. (2016). Costas circuit under the action of additive harmonic interferences and wideband noise. *Journal of Communications Technology and Electronics*, 61(7), 807–816.
- Simon, M. K., & Lindsey, W. C. (1977). Optimum performance of suppressed carrier receivers with costas loop tracking. *Communications, IEEE Transactions on*, 25(2), 215–227.
- Stephens, R. D. (2002). *Phase-Locked loops for wireless communications: digital, analog and optical implementations*. Springer.
- Tretter, S. A. (2007). *Communication system design using DSP algorithms with laboratory experiments for the TMS320c6713TM DSK*. Springer.
- Viterbi, A. (1966). *Principles of coherent communications*. New York: McGraw-Hill.
- Waters, G. W. (Aug. 10 1982). *Costas loop QPSK demodulator*. US Patent 4,344,178.
- Wolaver, D. (1991). *Phase-locked loop circuit design*. Prentice Hall.