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Classification of Emotion using Eeg Signals: an FPGA Based Implementation

Darshan B D, Vyshnavi Shekhar B S, Meghana M Totiger, Priyanka N, Spurthi A

***Abstract:** An electroencephalograph is a device that records all electrical energy in the human brain using wearable metal electrodes placed on the skull. Electrical impulses connect brain cells and are always mobile, even at rest. This activity appears as a squiggly line in EEG recordings. Activity gaze data is pre-processed to a frequency range of 0 to 75 Hz. This creates a new matrix with a sample rate of 200 Hz and a range of 0-75 Hz. A finite-impulse-response low-pass filter was used because the bandpass would distort his EEG data after processing. Each pre-processed EEG signal has an output, which completes feature extraction. Principal Component Analysis or PCA is passed in the feature reduction phase. PCA is an analytical process that uses singular value decomposition to transform a collection of corresponding features into mutually uncorrelated features or principal components. Principal component analysis: (a) mean normalization of features (b) covariance matrix (c) eigenvectors (d) reduced features or principal components. The above steps are passed to the SVM classifier for sentiment output. His VHDL code and testbench for 2*2 matrices were written, waveforms and RTL schemes were created in Xilinx 14.5. For the FPGA implementation, a Simulink model was designed, and the eigenvalues were pre-determined using a system generator.*

***Keywords:** Electroencephalography (EEG), Autonomous Nervous System (ANS), Principal Component Analysis (PCA), Support Vector Machine (SVM).*

I. INTRODUCTION

According to psychology, emotion recognition is the attribution of emotional states based on the observation of visual and auditory nonverbal cues. Emotions are essential in our daily life and work. Evaluating and adjusting emotions in real-time enriches and improves people's lives. emotional recognition. Another example is that when caring for patients, especially those with facial expression problems, doctors can provide better care if they are aware of their true emotional state. Emotional detection by EEG has attracted a great deal of interest in recent years. This is an important aspect of the brain-computer-computer interface system (brain-computer-computer interface), which greatly improves human-machine communication. The most common classification relates to the frequency bands of the EEG waveform, which allows EEG signals to be classified into five different frequency bands. Therefore, five separate frequency bands and their associated mental health are described below.

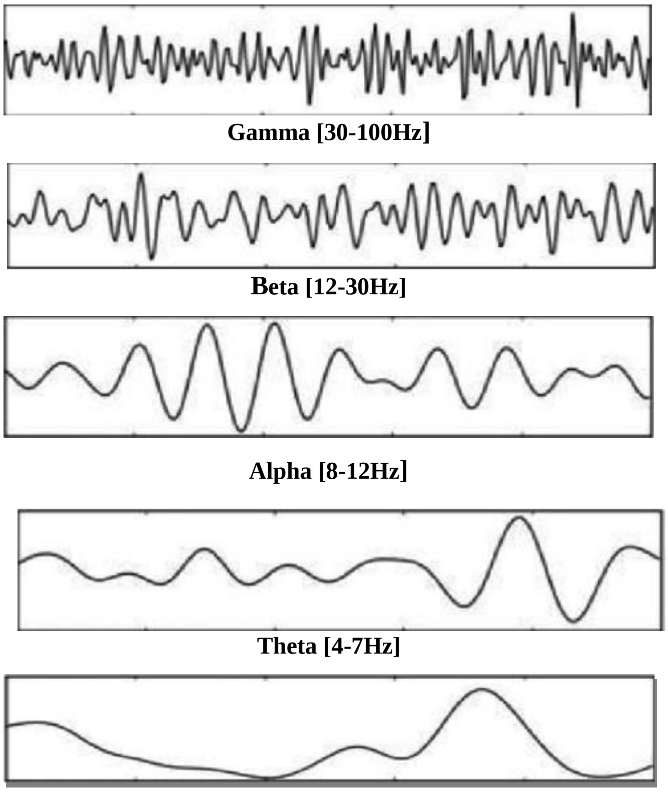

Fig 1: Different frequency bands of EEG signals

GAMMA (30-100Hz): These waves are the elevated frequency bands, and the range is by far the widest. This may be hard to measure, as the elevated frequency the small amplitude makes the signal easily contaminated by the muscles around the head.

BETA (12-30Hz): These waves are "fast" waves of activity. It is best seen from the front and is often observed bilaterally in a symmetrical distribution. Sedatives and sleeping pills, especially benzodiazepines and barbiturates, increase this value. It is widely accepted as a natural beat. This is the dominant rhythm when the patient is alert, anxious, or even has his eyes open.

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ALPHA(8-12Hz): This is the first electroencephalogram discovered. Some studies compare alpha valves to creativity. This is the main rhythm observed in typically relaxed people. It is present for most of life, especially shortly after age 13.
THETHA(4-8Hz): These waves are the "Slow" waves of activity. lesions: pear as a sign of focal specific brain lesions; it can also prevalent in diffuse diseases such as metabolism encephalopathy or hydrocephalus.

II. RELATED WORKS

It is not an easy task for organizers to observe the engagement level of a video meeting audience. This research was conducted to build an intelligent system to increase the experience of video conversations such as virtual meetings and online classrooms using convolutional neural network (CNN) and support vector machine (SVM) [1]. This study presents an electroencephalogram (EEG)-dependent hardware system architecture for instantaneous emotion recognition based on polyphasic convolutional neurons. Network Algorithm (CNN) running on 28nm technology chip and Field Programmable Gate Array (FPGA) for binary and quaternary partitions [2]. In this work, we propose an improved deep convolutional neural network model for emotion classification using a discrete training method that combines convolutional features of lower, middle, and upper layers [3]. In this paper Facial feature vectors in dual form are obtained using basic binary pattern histogram by tracing the bins in clockwise and anti-clockwise direction using SVM binary and multi class technique [4]. Digital predistortion is a baseband signal transforming approach that is veracious for impairments in RF power amplifiers. This technique is attained using cordic square root algorithm for FPGA implementation [5]. The real time SVM dependent emotion recognition algorithm has 4 different steps to achieve emotion detection. This is done using adapted histogram of oriented gradients (HoG) algorithm and SVM algorithm [6]. In this project the EEG based emotion identification is done using SVM classifier and electroencephalogram is used as it's a non-invasive, portable, inexpensive device [7]. In this project a recently developed emotional detection structure dependent on a recording feed in real time is done by employing a support vector machine (SVM) which is a reliable classification algorithm [8]. In this project a deep convolutional neural network model, EEGNet, Vivado and its hardware implementation, has been evolved for obtaining generalization towards different BCI Paradigms for design in portable EEG based BCI's [9]. The technique used in this work is a future trail using SVM to distribute emotions and modern approach of preprocessing in the structure of local secular pattern in consequence by PCA were used to feed to SVM classifier model [10]. Haar attribute by Viola and Jones is the earliest real time frontal-view face detector and is proposed in this project. In this paper, they have proposed the use of Haar-Like Feature and Ada Boost categorization to detect the face. Support Vector Machine (SVM) is the classifier used in this system [11]. In this paper they have used multilayer perceptron and artificial neural network (MLP-ANN) for classification of FPGA based embedded system [12].

A. Research Issues Identified

- We need to train on a large amount of data, which is not easy.
- Precision when the flag has to be classified into five recurrence groups.
- Issues in exact feeling acknowledgement.

B. Motivation

- Feeling Acknowledgments arrangements require a parcel of information to be prepared.
- Incorrect feeling pointers.

C. Objectives

- To recognize human feelings.
- To reduce the information dimensionality in expansion to making enhancements to the classification comes about.
- To progress exactness utilizing an SVM
- To actualize feeling acknowledgement on an FPGA

III. PROPOSED METHODOLOGY

A. Software Methodology

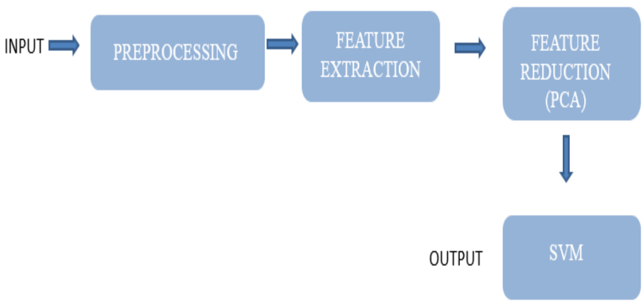

Fig 2: Proposed Software Methodology

(1) Preprocessing:
The preprocess is implemented on the raw dataset to filter the data or to obtain a clean data by checking the missing values or scaling to improve the facial expression frames.This step gives a new matrice with sampling frequecy of 200Hz and we utilized the 'Low Pass Filter' to decimate the highest frequency range above 75 Hz.
(2) Feature Extraction:
During Feature Extraction Process,we applied the wavelet filter bank technique to sepratre the preprocessed inputs into different frequencies sub-bands.This also reduces the complexity of data by dividing the freaquency range into high pass and low pass results and passed through filter until we get the targetted bandwidths for further process.
(3) Feature Reduction:
Principal Component Analysis, shortened as PCA is the more desired technique for dimensionality reduction. It transforms the correlated features in the data into orthogonal components thus, all the prime information will be recorded while reducing its dimensionality. PCA steps:(1) Mean Standardization (2) Computing co-variance (3) Computing Eigen Vectors to identify principal components (4) Creating feature vectors.

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(4) SVM (Support Vector Machine):
SVM is a familiar Supervised Machine Learning Algorithm. The goal of the SVM algorithm is to obtain a hyperplane in an N-dimensional plane that uniquely categorizes the features. Also, it handles classification as well as regression on both linear and non-linear data.

B. Implementation For Fpga

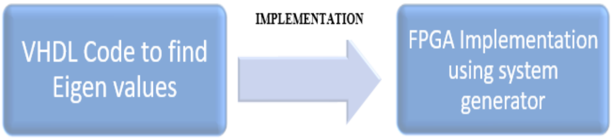


Fig 3: Proposed Hardware Methodology

A test bench was written in Xilinx 14.5 to determine the eigenvalues of a 2*2 matrix using VHDL (Very High-Speed Integrated Circuit Hardware Description Language) and the CORDIC (Coordinate Rotating Digital Computer) square root algorithm. VHDL code is Loaded into a black box to obtain the output of eigenvector calculations by Simulink. In addition, I used System Generator to create the hardware block.

IV. EXPERIMENTAL RESULTS & DISCUSSIONS

A. Dataset Collection

SEED dataset consists of EEG(Electroencephalogram) data of 15 subjects who watched 15 Chinese film clips subjected to emotions viz. positive, neutral, and sad. The data was stored in 45 Matlab files with a down sampling rate of 200 Hz. A bandpass filter of 0-75 Hz was being utilized for pre-processing. EEG signals arising were conserved using 62 channel ESI Neuro Scan System. Fifteen Chinese film clips involving positive, negative, and neutral emotions were presented to each fifteen Chinese subjects randomly. The duration of each clip was 4 minutes. The experimental procedures initially include 5 sec hints followed by clip of 4 mins and then self-assessment of 45 sec and finally 15 secs of rest.

B. Experimental Procedure

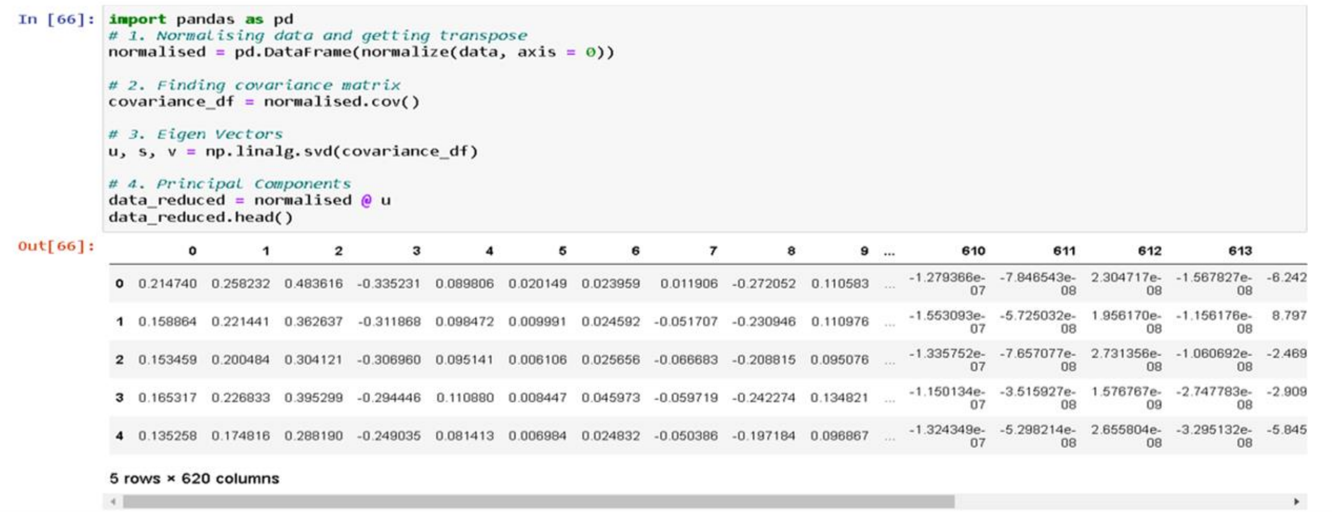
By creation, human beings are the only emotional animals. They are triggered and activated by emotions. Emotions are the operators of human behavior as they tell what is essential and inessential. Obtaining human emotions from the face instantly may lead to errors as humans may try to hide their real emotions. Using physiological methods like Electroencephalogram (brain signals) will overcome the drawbacks. The first step is to select records. Use a seed dataset. It consists of 45 Matlab files, each with 10 functions and 62 channels. The data is then pre-processed to remove noise and facilitate the classification of the input data. In the feature extraction phase, wavelet filter transforms are used to distinguish between delta, theta, alpha, beta, and gamma waves, and in the feature reduction phase principal component analysis is employed for feature reduction. After obtaining the principal components, classification is done using support vector machines.
A test bench written in Xilinx 14.5 to determine the eigenvalues of a 2*2 matrix using the VHDL programming language and the CORDIC square root algorithm (coordinate rotation digital computer). VHDL code is loaded into a black box and receives the output of eigenvector computations by Simulink. In addition, I used System Generator to create the hardware block.

C. Experimental Results

Table 1: comparison of results of different algorithms.

Algorithms	Min	Max	Mean	Accuracy
Log Reg	89.93	75.88	89.11	76.13
GNB	69.415	54.72	83.13	59.70
KNN	82.15	77.88	88.99	81.99
SVM	96.98	88.19	93.52	87.30%

On the account of the above table based on the comparative study of various algorithms we have analyzed different factors to compare the quality and efficiency of the algorithms. Thus, finalizing the SVM algorithm to be the best of all as it is the finest classifier.



```
In [66]: import pandas as pd
         # 1. Normalising data and getting transpose
         normalised = pd.DataFrame(normalize(data, axis = 0))

         # 2. Finding covariance matrix
         covariance_df = normalised.cov()

         # 3. Eigen Vectors
         u, s, v = np.linalg.svd(covariance_df)

         # 4. Principal Components
         data_reduced = normalised @ u
         data_reduced.head()
```

Out[66]:

	0	1	2	3	4	5	6	7	8	9	...	610	611	612	613	
0	0.214740	0.258232	0.483616	-0.335231	0.089806	0.020149	0.023959	0.011906	-0.272052	0.110583	...	-1.279366e-07	-7.846543e-08	2.304717e-08	-1.567827e-08	-6.242
1	0.158864	0.221441	0.362637	-0.311868	0.098472	0.009991	0.024592	-0.051707	-0.230946	0.110976	...	-1.553093e-07	-5.725032e-08	1.956170e-08	-1.156176e-08	8.797
2	0.153459	0.200484	0.304121	-0.306960	0.095141	0.006106	0.025656	-0.066683	-0.208815	0.095076	...	-1.335752e-07	-7.657077e-08	2.731356e-08	-1.060692e-08	-2.469
3	0.165317	0.226833	0.395299	-0.294446	0.110880	0.008447	0.045973	-0.059719	-0.242274	0.134821	...	-1.150134e-07	-3.515927e-08	1.576767e-09	-2.747783e-08	-2.909
4	0.135258	0.174816	0.288190	-0.249035	0.081413	0.006984	0.024832	-0.050386	-0.197184	0.096867	...	-1.324349e-07	-5.298214e-08	2.655804e-08	-3.295132e-08	-5.845

5 rows × 620 columns

Fig 4: Principal component analysis output

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```
In [121]: #Support Vector Machine
          from sklearn.metrics import confusion_matrix
          from sklearn.metrics import accuracy_score
          from sklearn.svm import SVC
          Xtrain, Xtest, y_train, y_test = train_test_split(X, y, test_size=0.3, random_state=0)
          classifier = SVC()
          classifier.fit(Xtrain,y_train)
          y_pred = classifier.predict(Xtest)
          cm = confusion_matrix(y_test,y_pred)
          accuracy = accuracy_score(y_test,y_pred)
          print("Support Vector Machine:")
          print("Accuracy = ", accuracy)
          print(cm)

          Support Vector Machine:
          Accuracy =  0.873015873015873
          [[16  1  2]
           [ 5 22  0]
           [ 0  0 17]]
```

Fig 5: Support Vector Machine Code and Outcome

To compute a square-root with CORDIC the number is yielded by multiplying, adding and testing.

L	2^L	y	x=	12056
		0	initial value	
7	128	0	128 x 128 > 12056	do nothing
6	64	64	64 x 64 < 12056	add 64 to $y_{initial}$ --> 64
5	32	96	$(64 + 32)^2$ < 12056	add 32 to last y --> 96
4	16	96	$(96 + 16)^2$ > 12056	do nothing
3	8	104	$(96 + 8)^2$ < 12056	add 8 to last y --> 104
2	4	108	$(104 + 4)^2$ < 12056	add 4 to last y --> 108
1	2	108	$(108 + 2)^2$ > 12056	do nothing
0	1	109	$(108 + 1)^2$ < 12056	add 1 to last y --> 109
-1	0.5	a.s.o.	and so on	and so on

Fig 6: An example of calculating the square root of the algorithm.

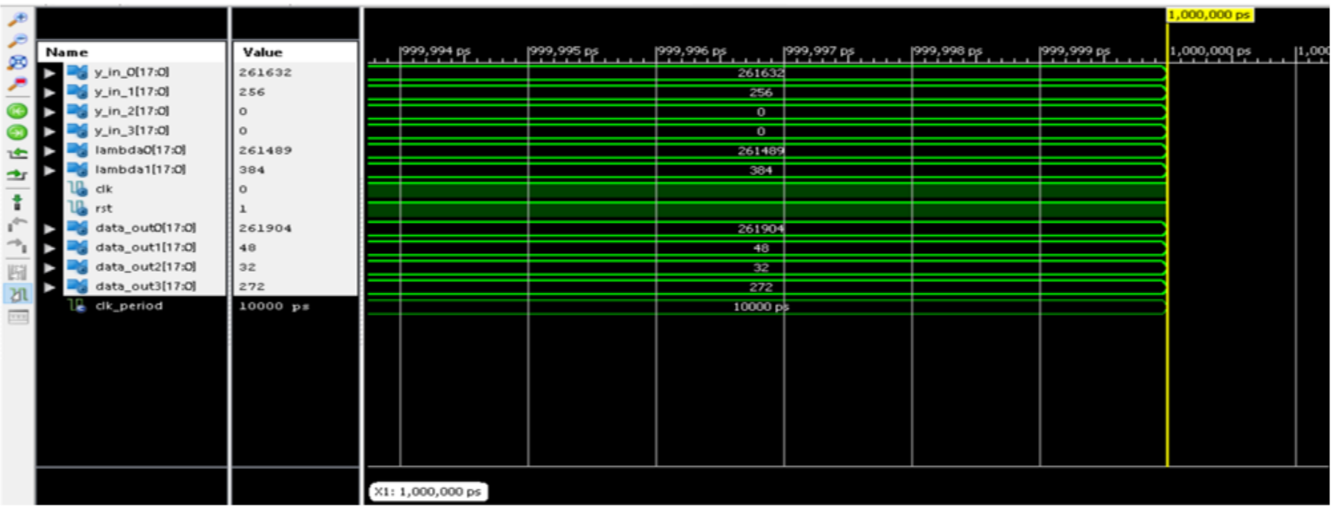

Fig 7: Simulation waveform of eigenvector calculation

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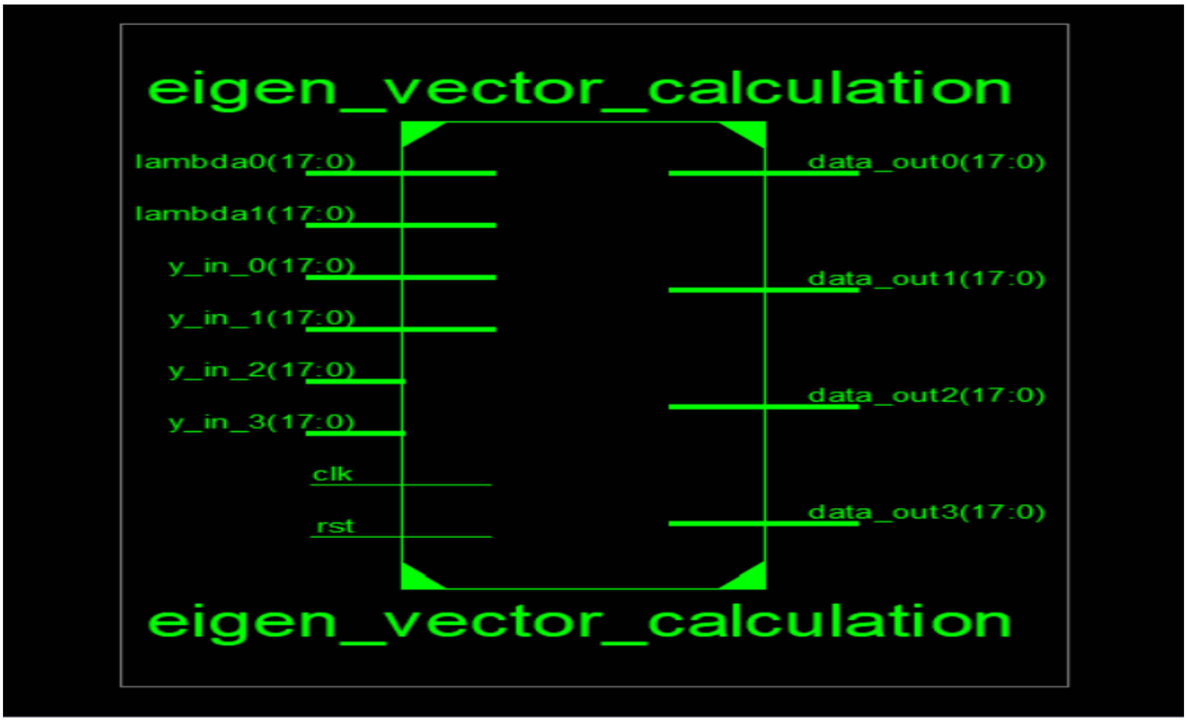

Fig 8: Schematic diagram of eigenvector calculation

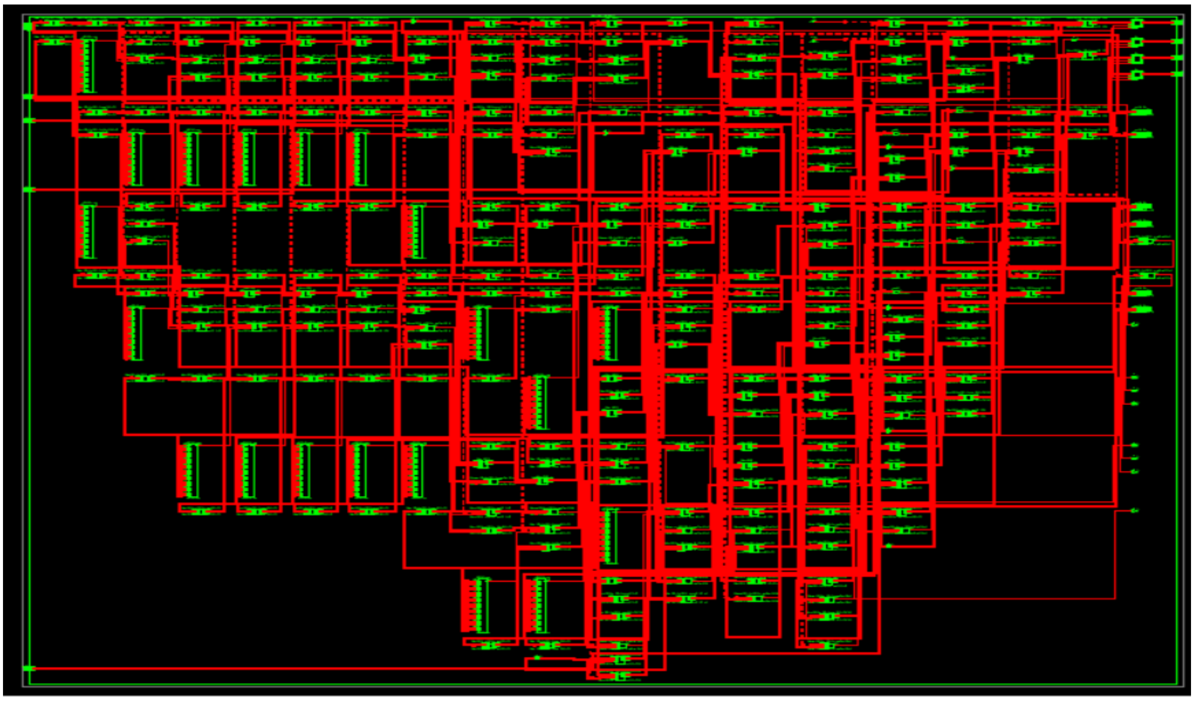

Fig 9: RTL Schematic

Table 2: Device utilization summary

Device Utilization Summary (estimated values)			
Logic Utilization	**Used**	**Available**	**Utilization**
Number of Slice LUTs	5070	27288	18%
Number of fully used LUT-FF pairs	0	5070	0%
Number of bonded IOBs	146	218	66%
Number of BUFG/BUFGCTRL/BUFHCEs	1	16	6%
Number of DSP48A1s	22	58	37%

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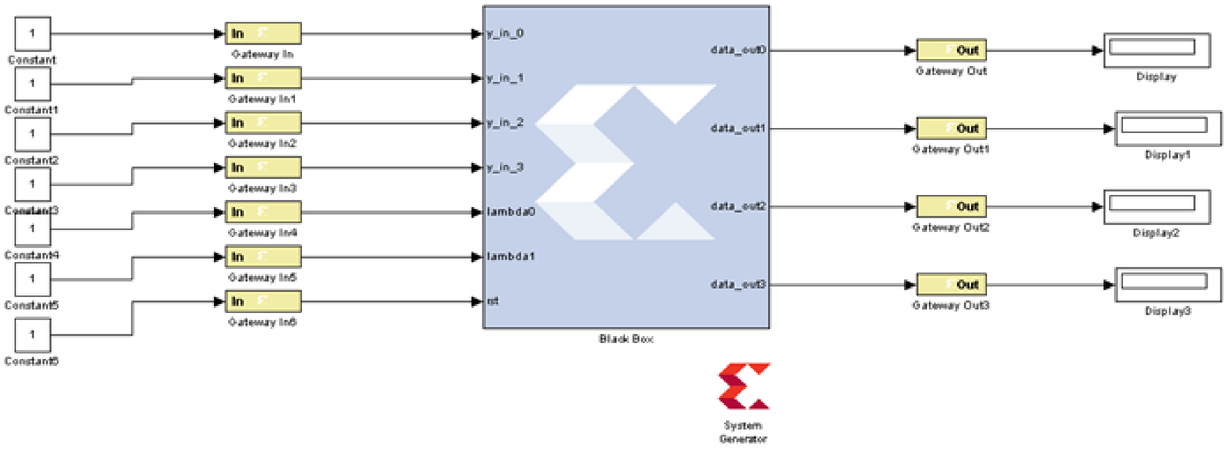
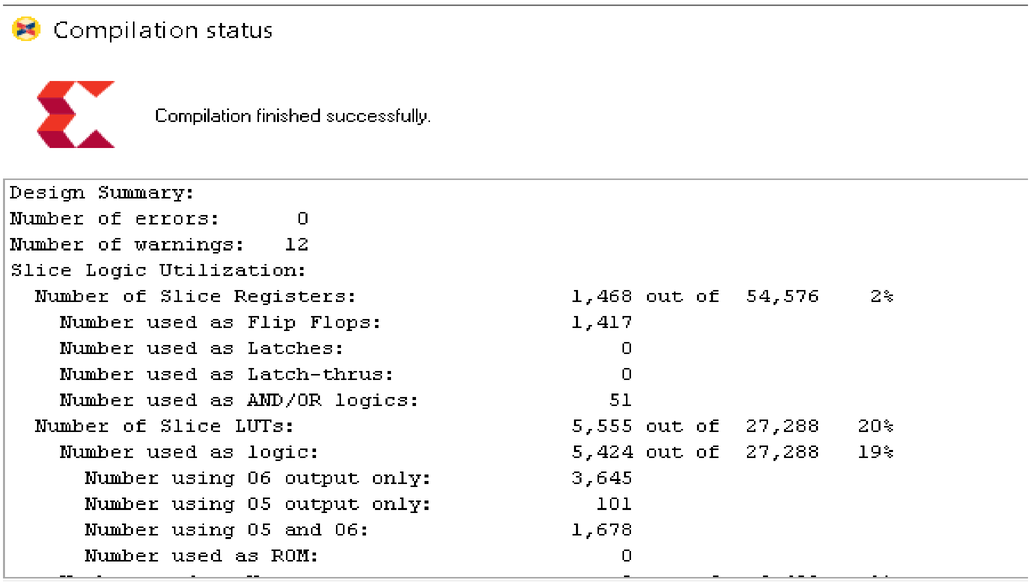


Fig 10: Simulink model for computing eigenvectors


Compilation status

Compilation finished successfully.

```
Design Summary:
Number of errors:      0
Number of warnings:   12
Slice Logic Utilization:
  Number of Slice Registers:              1,468 out of  54,576    2%
    Number used as Flip Flops:            1,417
    Number used as Latches:                   0
    Number used as Latch-thrus:               0
    Number used as AND/OR logics:            51
  Number of Slice LUTs:                   5,555 out of  27,288   20%
    Number used as logic:                 5,424 out of  27,288   19%
      Number using O6 output only:        3,645
      Number using O5 output only:          101
      Number using O5 and O6:             1,678
      Number used as ROM:                     0
```

Fig 11: Summary of the design in the Simulink model

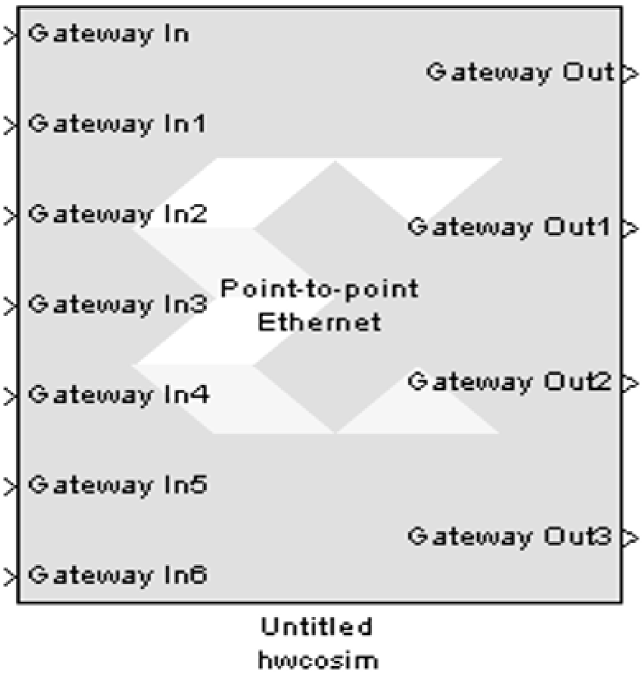


Fig 12: Simulink generates a hardware model.

V. CONCLUSION

This paper will present a new approach to recognize correct human emotion using EEG signal and implementation of the same on FPGA. This approach consists of preprocessing technique which filters the frequency range of 0 - 75Hz and sampling frequency of 200Hz. After feature extraction is completed, we obtain EEG preprocessed data yielding 620 features. We apply PCA Principal component analysis. The PC's will be fed to Support vector machine Classifier to obtain output. A VHDL code and test bench is written for 2*2 matrix and waveform, RTL schematic is obtained on Xilinx 14.5. Simulink model is used for FPGA implementation.

FUTURE SCOPE

The research on emotion recognition deployed on EEG signals has been developing rapidly. Emotions are affecting every aspect of human life. The research is in the initial stages and there is broad space in development. Emotion recognition procedure with the use of EEG signals has an extensive application along with signal processing and artificial intelligence.

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This processing way can be applied for a large range of processing and AI applications. The improved PCA design can be used in compression and reduction of dimensionality algorithms.

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DECLARATION

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Conflicts of Interest/ Competing Interests	No conflicts of interest to the best of our knowledge.
Ethical Approval and Consent to Participate	No, the article does not require ethical approval and consent to participate with evidence.
Availability of Data and Material/ Data Access Statement	Not relevant.
Authors Contributions	All authors having equal contribution for this article.

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Meghana M Totiger, Graduated from Department of Electronics and Communication Engineering, SJB Institute of Technology, Bangalore India. Graduating from ECE branch I have been more inspired by the concepts in electronics field and have made my mind in creating the path in electronics field. I am passionate about this field and hoping to make more and take more from the knowledge I have.

Priyanka N, Graduated from Department of Electronics and Communication Engineering, SJB Institute of Technology, Bangalore India. Graduated from Electronics and communication engineering. My pastime includes reading books and playing badminton. I focus on gaining knowledge and lifelong learning. My passion is to gain more and more Knowledge.

Spurthi A, Graduated from Department of Electronics and Communication Engineering, SJB Institute of Technology, Bangalore India. My hobbies include reading books and singing. My future intend is to grow more in VLSI design and verification field. Being an electronics and communication engineer, this field inspires me a lot and makes me more active to learn more on the concept.

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