An ingenious MMC topology appropriate for motor drives across their entire frequency spectrum

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Article history:

Received Sep 25, 2022 Revised Dec 19, 2022 Accepted Jan 3, 2023

Keywords:

Energy balance Field-oriented control High-frequency mode Low-frequency mode Modular multilevel converter

Article Info ABSTRACT

Modular multilevel converter (MMC) modules have popped up as among the best choices for medium and high-powered uses. This paper proposes a control scheme for the entire frequency range of operation for the MMC, focusing on supplying a three-phase machine. The machine is required to be controlled in the outer as well as the inner loop. Standard field oriented control (FOC) manages the three-phase machine in the outer closed loop while the inner control has to come up against the problem of energy balancing. That is unevenly distributed and stored in the capacitance of the upper and lower arms of the converter. There are two operating methods used in the inner control loop: a low-frequency method is used for start-up and low-speed operation, and a high-frequency method is for higher speed. In low-frequency mode (LF-mode), a special control strategy has to be implemented to minimize the energy oscillation in the capacitances of the converter arms. It makes utilization of the 3-phase machine's common mode voltage (Vc) as well as internal circulatory currents to verify a symmetrical energy distribution inside this MMC arms and also to avert whatever AC currents inside the DC source.

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1. INTRODUCTION

The process and control of electrical energy is an essential aspect of power transmission. This task was contented by the conception of power converters such as various levels of multilevel inverters like threelevel inverters [1] and five-level inverters [2]. Modular multilevel converter (MMC) has been receiving a lot of attention and advancement ever since its inception because of its numerous advantages such as outstanding performance, high modularity, easy quantification, as well as low voltage and current rating request for switching devices [3], [4]. These would be big benefits of the MMC over conventional two-level as well as configuration converter topologies [5]. The initiation of MMC took place in 2002 for the employment of high-voltage direct current (HVDC) systems in high-voltage transmission implementations [6]. In 2003, Marquardt was the first to present MMC and it turned out to be progressively appealing owing to modularity, high efficiency, excellent *Vout* waveform, redundancy, and avoiding separate dc sources [7]. The first papers

concerning the MMC as a motor drive implementation were publicized in 2009 [8]. Considering the innovation of the MMC, henceforth the topology has been implemented in traction applications [9], HVDC transmission [6], and large electric drives [10], [11], especially for hybrid electric vehicles [12], [13]. This paper is concerned with the behaviour of the HVDC link structured on MMC i.e. dynamic stability. Such articles describe the structure's key principles, the orientation of the inner elements, and thus the management of the MMC. They primarily specialize in a constant frequency operative intent, where almost no special management of levelling the internal energy kept within the cell capacitors has been required. The help managing that has been conferred does not allow function at a low output frequency. Over the years, so many MMC circuit configurations and their computer simulations have been formed, as well as the emergence and technological difficulties of classical and model predictive control techniques have been presented [14]. The first involvement regarding the MMC's low-frequency mode (LF-mode) has been made in [15]. Its basic design has been used in [4] to create an efficient system for MMC low-frequency operation, medium, as well as high voltage applications [16]. It also reduces absorbed current energy and dc fault current [17], [18]. Grid systems and isolated MMC-based DC-DC (IMMDC) inverter help DC buses match voltage, isolation, and transfer power [19]. Such a study elaborates [20]'s strategy for LF-mode associated with the more wellknown high-frequency mode (HF-mode). Initially, the management system converts the three-*Iph* values, as well as the *V* of the arm capacitance, into space vectors [21] and zero sequence aspects. This technique produces inner energy levelling while preventing any *Iacon* on the DC side. The input, output, as well as inner currents all, can be modified independently using the field oriented control (FOC) [2].

Those who deliver *P* transfer from DC supply to the motor or in reverse, as well as internal energy transferring here between arms. The real power elements within the converter arms are then assumed for the two operative modes. Sizable *V^c* variations whenever the machine is running at low speed. So, the extenuating variables i.e. circulating currents and *Vcommon* mode are used in the low-frequency spectrum to reduce voltage instability [22]. On the flip side, the machine is operated in a high-frequency spectrum when extenuating signals are no longer required. The equations have been derived for the employee during a feed-forward control system to ensure a constant and symmetrical energy transfer. Finally, modelling an MMC with such a time-separate model validated the network [23], [24]. Figure 1 depicts the drive implementation, which includes a DC source, the MMC, and the machine as a 3-ph load. The MMC has been divided into three stages, each with an upper and lower arm n. One converter arm has been made up of m series-connected cells and an inductor. In this arrangement, one cell only needs to be a half-bridge with such a capacitor.

Figure 1. MMC configuration of the 3-phase machine [24]

So, every arm could indeed produce a variable voltage u_{py} and u_{ny} number of phases by switching half-bridges of the cells (*y*=1...3). This is same i_{pv} and i_{nv} . Currents in the arms have been described. The MMC produces the $3V_{ph}$, $ua10$, $ua20$, and $ua30$ corresponding to a DC source's midpoint 0 on the 3_{ph} -AC side. The V_c mode u_0 also was described as the voltage between the neutral point of the 3-ph load or motor and the DC source's reference potential.

The structure of an article is as follows. Section 1 describes the introduction. Sections 2 provide thorough analysis of motor drive and requirements of variable frequency drive (VFD), motor control with MMC employing current control method is described in section 3. Section 4 discussed recommended method for horizontal (HBC) and vertical balance control (VBC) of MMC with study system modelling and simulation in MATLAB. Section 5 provides and examines the findings of the system simulation of several instances. Finally, article is concluded with the conclusion section 6.

2. MOTOR DRIVE

A motor drive regulates the speed, torque, direction, as well as horsepower produced by a motor. In electro-mechanical drive systems, a VFD regulates AC motor speed and torque by adjusting the input and voltage [25], [26]. The VFD is another technique for regulating the speed of electrical machines. Once users investigate electrical machines or electrical generators, users believe that the speed of rotation of the machines has been governed by the *V* and *f* of the stator side of the motor. Modify the torque (1), to alter the speed.

$$
T = \frac{K_s E^2 R}{R^2 + (sX)^2}
$$
 (1)

Specifically, *T* is torque generated by the motor; K_s is constant term; *E* is rotor EMF induced; *R* is rotor inductive resistance; *X* is rotor-induced reactance; and *s* is slip of the motor.

If a motor torque is less than the load torque, the motor speed has been reduced; if a motor torque exceeds the load torque, the motor speed is increased. Modifying the stator frequency (*f*) and the number of poles (*P*) is yet another simple method of controlling the speed of electrical machines [27]-[29]. The rotational speed of a rotating magnetic field has been signified by *Ns*, which further depend solely on the as well as *P*. If the *P* remains unchanged, the only way to change the *N^s* is to use (2) to modify the *f*.

$$
N_s = \frac{120f}{P} \tag{2}
$$

Where N_s is synchronous speed of induction motor, f is stator supply frequency, and P is number of poles of the motor.

On the stator side, parameters such as supply frequency, supply voltage, number of poles, and external stator resistance could be operated to regulate the speed. VFD is an abbreviation for variablefrequency drive, also known as frequency converters that has experienced relatively fast shifts, due primarily to the advancement of a microprocessor as well as semiconducting devices and their cheaper costs. Even so, the fundamental operating precepts of frequency converters stay intact. A VFD has become a type of motor speed controller that controls the frequency and voltage supplied to an induction motor. As shown in the block diagram in Figure 2, a VFD primarily consists of a rectifier, an intermediate circuit, as well an inverter to transform back dc voltage into the ac voltage.

Figure 2. Schematic representation of VFD

Its construction depends on the rectifier and inverter. One transform converted voltage to DC [30], and the other does not. It smoothes the inverter's pulsating direct voltage input. The rectifier converts DC to AC. The

inverter seems to be the final major component of the VFD drive. In addition to generating the output AC voltage and frequency, the inverter process represents the final stage. The ac voltage that is implemented to the motor is generated by the inverter. The frequencies converter's fourth major portion, the control loop, also referred to as the card, controls the semi-conductors, exchanges data with external hardware, assembles and reports problem signals, and protects the converters associated motors. Microprocessors it has enhanced the speed of the controller, growing the variety of applications appropriate for drives as well as reducing the number of estimations required [31], [32]. A processor in microprocessors has been incorporated into the frequency converter and thus can figure out the optimal pulse pattern for each operational state.

3. MOTOR CONTROL WITH THE MMC

Figures 3 and 4 shows the cumulates system model for the MMC and govern mechanisms is shown, that offers the reference attributes for the V_{out} (space vector u_a with a magnitude \hat{U}_a as well as angle γ_a). In this case, a field-or rotor-oriented control serves to completely separately adapt the magnetic flux and torque *Mi1* of the machine by i^*_{ad} and i^*_{aq} . In the torque closed loop, a speed controller has been got to add. A zero sequence voltage has been provided in the block "computation of *Vph*" of Figure 3 and will be discussed clearly.

Figure 3. A summary of the MMC-control strategy

Figure 4. A *3-Ph* machine's field/rotor control

The preferred V_{ph} u^{*}_{ay0} forms the input of the MMC-control block, described in the following sections, along with the measured *V* and *I* of the (i_{py}, i_{ny}) and the load (u_{ay}, i_{ay}) . The "modulator and cellbalancing" block serves as a link here on its own. It creates the pulse patterns *(gpxj, gnxj*) for the cell switches

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based on the required arm voltages u^*_{py} and u^*_{ny} . [33], [34] define a feasible approach that involves cell balancing. The energy in the arms has been represented by the total amount of the V_c of each arm (u_{Cpv} and $u_{C_{IV}}$). They are brought back to the MMC-control unit through the modulation scheme.

3.1. Current control

It provides a comprehensive explanation of the decoupled control structure for both i/p and Io flow [35]. In summary, the I_0 *i*_{*ay*} has been split in half, and also the $i_{e\gamma}/phase$ flows over the DC-source specified in (3). It thus causes the arm currents:

$$
ipy = iey + \frac{iay}{2}, \text{iny} = iey - \frac{iay}{2} \tag{3}
$$

$$
\Rightarrow iey = \frac{1}{2}(ipy + iny) \Rightarrow iay = ipy - iny \tag{4}
$$

Independent control of i_{ey} and i_{ay} is possible by adjusting the difference and the sum of the arm voltages given in (4):

$$
iay = \frac{1}{2La}(uny - upy - 2(Ra * iay + uiy))
$$
\n(5)

$$
iey = \frac{1}{2L}(Ue - (uny + upy))
$$
\n⁽⁶⁾

Its requested arm voltages u^*_{py} , as well as u^*_{ny} , are calculated utilizing voltage loops of every phase provided in (5) and (6) of the MMC. A transition matrix C presently transforms one such strategy in *αβ*0 coordinates to govern the currents seen in Figure 5. The sub-ordinate input current controllers' input signals seem to be the *αβ0*-elements of the current.

Figure 5. Space current control of the input currents

In Figure 6, the α and β indices are the DC and AC amounts for the internal energy density controller parameters current flow [36] and thus the LF mode feed-forward component (substring v). The 0-component transfers DC energy. DC sources cannot generate *Iac* because they lack AC signals.

3.2. Functioning mechanisms based on output frequency

Table 1 interprets output *Vph* depending on the outputting space vector and zero sequence voltages. Identically, the correct output currents inside the phases have been characterized, so although ϕ_a has been the phase difference of the *ZL*.

$$
ia = \hat{I}ae^{j(ya - \varphi a)} \tag{7}
$$

The input currents to further have I_{e_y} , \hat{I}_{e_y} elements, to the input stage provided by (7) and is employed for energy balance control as in (8).

Figure 6. Formation of input current control levels

The arms power must be recognized when acknowledging balance and coordination in both the HF and LF modes:

$$
ppy/ny = upy/ny * ipy/ny = \left(\frac{be}{2} + uay + u0\right) * (ley + \hat{f}ey\cos(\gamma ey) \pm \frac{iay}{2})
$$
\n(9)

The real power components for such 2 operating conditions could be calculated using (9). − HF-mode

In the HF mode, (10) gives a fixed frequency of the *Vout*:

$$
\gamma a = \omega a t \tag{10}
$$

Besides the real power supplies of the input and output sides, a further real power component could be produced by varying the \emptyset of the I_{inner} in (11) γey through relation to the output V_{ph} currents in (11) (Table 2).

$$
\gamma ey = \omega at - \frac{2(y-1)\pi}{3} \tag{11}
$$

So, the arms have different signs, that element was utilised to balance energies vertically among the upper and lower arms by altering the magnitude *Îey* of an internal current.

LF-mode

The core concepts of the LF mode have been fully discussed in [5], [6]. The *V^o* and *I^o* are presumed to be the DC values provided in (12):

$$
\gamma a = \text{Constant} \tag{12}
$$

In comparison to an HF mode, the *Iinner* used to balance energy in the arms must correlate to an extra zero sequence AC-voltage \hat{U} 0 $\cos(\omega 0t)$. As a result, the Ø of the inner currents has been provided in (13):

Bulletin of Electr Eng & Inf ISSN: 2302-9285

 $\gamma ey = \omega 0 t$ (13)

The uncontrolled AC- *V^c* modes frequency has been labelled as *ω0*. Table 3 shows the estimated real power components in the LF mode.

Since the total amount of the real power supplies have to be nil, the *Idc* for energy transfer from the DC source as well as the magnitude of the AC for inner balance is calculated as shown in (14).

$$
ley = \frac{1}{ue}(uayiay + U0iay)
$$
 (14)

$$
\hat{I}ey = \frac{1}{\hat{U}^0} \left(\frac{1}{2} U \hat{e} iay - 2 uay \hat{e} y - 2 U 0 iey \right) \tag{15}
$$

Users have been afterward utilized in the LF mode for feed-forward control. Since any I_{ac} must be presented at the DC-source, the total amount of the *Iac* must be taken into account. The solution to this circumstance has been given in (15). In the LF-m, *U⁰* acts as feed-forward control for the zero sequence component of vertical balancing.

$$
\sum_{y=1}^{3} \hat{I}ey = -\frac{3\hat{U}a\hat{I}a}{U\hat{U}0}(2U0\cos(\varphi a) + \frac{1}{2}\hat{U}a\cos(3\gamma a - \varphi a))
$$
\n(16)

$$
\Rightarrow U0(\gamma a) = -\frac{1}{2} \frac{\partial a}{\cos(\varphi a)} \cos(3\gamma a - \varphi a)
$$
\n(17)

The schematic representation in Figure 7 seems to be the result of (16), and (17) for producing the *Vph* by introducing the zero sequence.

Figure 7. An estimation of the *Vph*, along with zero sequence voltage

4. PROPOSED METHOD

The strategies introduced for energy balance in the HF as well as LF modes have now been converted into *0*- coordinates. One such solution allows the inner energy sources held in the capacitor of each arm across total energy control, along the horizontal axis among MMC's 3-Ph as well as the reference point among arms p and n. The evaluated and converted amplitude of the *V^c* in the arms must be filtered due to the pulsating energy in the arms.

4.1. Energy control

The output of power/phase has been calculated using the following (18):

$$
Pay = \frac{1}{3} \cdot \frac{3}{2}. Re\{ua.ia\} = \frac{1}{2}(uaaia\alpha + ua\beta ia\beta)
$$
\n(18)

It is employed in the feed forward control of the I_{dc} from the source specified by (19).

$$
ieDCv0 = \frac{Pay}{Ue} \tag{19}
$$

An energy controller in Figure 8 adjusts to any changes in normal circumstances as well as maintains the V_{avg} of the arm capacitance at U^*c .

Figure 8. Energy control

4.2. Horizontal and vertical balance control

The govern technique through Figure 9 distributes energy equally among the 3 phases, which are represented by the sum of *uCpy* and *uCny*. The internal DC-currents in *αβ* -elements cancel out the influence change among phase. The VBC in Figure 10 determines the magnitude of the internal currents to confirm that the upper and bottom arms have equivalent average energy entirety. The magnitude of the inner currents $\hat{\iota}eAC\alpha/\beta/0$ must be switched over based on the F_o , by the inference of the two operating conditions.

Figure 9. Horizontal balance control

The inner currents must relate to the HF mode's output frequency ω_0 and the LF mode's zero sequence voltage frequency ω_a . A continuous slider shift inside the interval here is between output limits converts the information into HF into LF form ω_{a1} and ω_{a2} . Figure 11 depicts the VBC concept. The magnitude of the inner currents has been magnified by the zero sequence voltage of AC reference curves. These α and β aspects are directed directly to the handover blocks with the necessary internal present value. The extra DC zero sequence influences $u *_{0BCsym}$ the $\alpha\beta0$ component, which characterizes the distinction between the total the three phases' amount of energy, from highest to lowest.

Figure 10. Vertical balance control

Figure 11. Vertical balancing in the HF and LF modes

Such a technique, as expected, manages to avoid any *Iac* in the *DCsource*. Having similar objective is accomplished inside the HF mode by using a positive and a negative sequence current mechanism for vertical balances [37]. The feed-forward control currents changed in a similar manner, as shown in Figure 12.

Figure 12. Switchover of the feed-forward balancing control

5. RESULT AND DISCUSSION

Computation in MATLAB surrounding a PLECS block set for such MMC validates the suggested control approach. To ignore the modulator's switching states as well as cancel the ripple currents inside the computation, the arms of the MMCs have been modeled by voltage govern sources. Using given by (20), the voltage of the arm capacitance has been determined step by step.

$$
uCxy, k = uCxy, k - 1 + \frac{TA}{c} \cdot \frac{uxy.k.ixy.k}{ucxy.k - 1}
$$
\n⁽²⁰⁾

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3*-αβ0*-transformation is given by (21):

$$
\begin{bmatrix} y\alpha \\ y\beta \\ y0 \end{bmatrix} = C \cdot \begin{bmatrix} x1 \\ x2 \\ x3 \end{bmatrix} = \begin{bmatrix} \frac{2}{3} & \frac{-1}{3} & \frac{-1}{3} \\ 0 & \frac{1}{\sqrt{3}} & \frac{-1}{\sqrt{3}} \\ \frac{1}{3} & \frac{1}{3} & \frac{1}{3} \end{bmatrix} \cdot \begin{bmatrix} x1 \\ x2 \\ x3 \end{bmatrix} \tag{21}
$$

Polar-to-Cartesian (P2C) and Cartesian-to-polar (C2P) transformations are depicted by the following (22):

$$
\begin{bmatrix} y\alpha \\ y\beta \end{bmatrix} = r \cdot \begin{bmatrix} \cos(\theta) \\ \sin(\theta) \end{bmatrix} \cdot \begin{bmatrix} r \\ \theta \end{bmatrix} = \begin{bmatrix} \sqrt{x\alpha^2 + x\beta^2} \\ \arctan(\frac{x\beta}{x\alpha}) \end{bmatrix}
$$
(22)

In (23), rotation of the vector by angle γ is given by:

$$
\begin{bmatrix} y\alpha \\ y\beta \end{bmatrix} = \begin{bmatrix} \cos(\gamma) & -\sin(\gamma) \\ \sin(\gamma) & \cos(\gamma) \end{bmatrix} \cdot \begin{bmatrix} xd \\ xq \end{bmatrix}
$$
 (23)

Table 4 depicts the specifications of the modeled MMC. A $3-I_{ph}$ source with the same ($\varphi_0 = 0$) as the V_o space vector could provide the *Io*.

Table 4. Computational specifications for MMC

Parameters	Specifications
Sampling time	$T_A = 125$ microsec
Input DC voltage	$U_e = 100$ V
Arm capacitance	$C = 4.400$ microF
Arm capacitance ref. voltage	$U^*c=120$ V
Zero sequence AC-frequency	ω 0=2 π .50 Hz
Switchover frequencies	$\omega a1 = 2\pi.15 Hz, \omega a2 = 2\pi.20 Hz$

Figure 13 illustrates the outcomes of simulating the HF mode. At, t=0.05 s, the *Vout* will be set to $\hat{U}a=40$ V, and the output frequency will be set to 40 Hz. The *V*_{common} mode is display in Figure 14. The DC component of the LF mode that is associated with the $3rd$ harmonic of the V_c mode is preserved, which allows for the modulation amplitude to be increased. The addition of the V_c is displayed in Figure 15 for each of the six arms, and it is after the step at t is 0.05 s that the efficiency of the inner balancing control can be seen to have been achieved. Figure 16 depicts an illustration of each of the concern *3-Ph* input currents. As was to be expected, there is no discernible presence of an AC component in the overall input current denoted by i_e in Figure 17. Figure 18 contains illustrations that illustrate the LF mode.

The extra AC-*Vcommon* mode is display in Figure 19 seems to be noticeable at the output *Vph*. An energy pulse inside the capacitor of the arm has been moved to significantly high frequencies, as shown in Figure 20. As a result, the energy pulse, and thus the required *Carm*, has been drastically decreased. This configuration, depicted in Figures 21 and 22, requires a higher inner AC. In comparison to the HF mode, the total amount of these *Iac* has always been zero.

High-frequency operation

Figure 13. Output phase voltages and currents in the load

Figure 14. Common mode voltage

Figure 15. Sum of capacitor voltages

Figure 16. Input current of each phase

Figure 17. Total input current in DC-source

Low-frequency operation phase voltages of the output $u_{\alpha y 0}$ [V] 25 \mathbf{o} -25 -50 0.05 0.1 0.15 0.2 0.25 0.3
output currents in the load 0.4 0.45 0.5 0.35 $\int_{ay} [A]$ ś 0.2 0.05 0.1 0.15 0.25 $0.3\,$ 0.35 0.4 0.45 0.5 O

Figure 18. Output Vph and current in the load

Figure 19. Common mode voltage

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Figure 20. Sum of capacitor voltages

Figure 21. Input current of each phase

Figure 22. Total input current in DC-source

Figure 23 depicts the transition between the two control techniques. Figure 24 depicts the mode of *V* of the switchover mode of operation. The frequency and the V_o are risen by $\theta a = 100 \frac{V}{s} t$ and $\omega_0 = 2 \pi$, $60 \frac{Hz}{1}$ S . The change can be observed at time 0.25 s to 0.3 s. Such results indicate a smooth crossover here between LF and HF modes, with very few transients in the V_c of the arms that is shown in Figure 25 than during a hard switchover. Figures 26 and 27 show the input current of every phase as well as the total DC of the source in the switchover operational mode.

Setup of a 3ph system including operation mode

Figure 23. Output *Vph* and current in the load

Figure 25. Sum of capacitor Vs

Figure 26. Input current of each phase

Figure 27. Total input current in DC-source

6. CONCLUSION

It is addressed a coherent approach for regulate the MMC that could produce *Vout* across the entire frequency range of a *3-Ph* machine. The contest of balancing *V^c* throughout all operating conditions has been encountered in this. The remedy has been demonstrated by deriving the decoupled and transmogrified current control on the one hand as well as the real balance of power on the other. The control system has been split into two operation modes: low and high frequency, with separate governs mechanisms. A sliding switchover connects the two strategies. Modeling has been employed to validate this total govern strategy. The findings suggest that the inner balance govern works effectively. The control system ensures that no *Iac* flow through the DC source at the same time. To conclude, this methodology seems to be capable of controlling an MMC across its entire output frequency range.

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