

L-Index Based Weak Area Identification of IEEE 118 Bus System using Dynamic Simulation in PSS®E

Kamaljyoti Gogoi, Saibal Chatterjee

Abstract: Voltage stability is a relevant part of power system stability analysis ever since many voltage collapse incidences have occurred at different regions of the world. In this paper we take up IEEE 118 bus system as it represents a close approximation of standard Grid system. This IEEE 118-bus system has fixed number of predefined generators, synchronous condensers, transmission lines, transformers and loads. In this work IEEE 118 bus system is simulated and load flow simulation is computed using the software PSS®E. The weak buses of the IEEE standard 118 bus network are predicted with the help of L-Index Algorithm in MATLAB. Further dynamic simulation is also performed in the weak buses obtained from L-Index as it opens up scope of more detail analysis of the system. An initial transient disturbance is then introduced in the weak buses and then the resultant maximum frequency deviation and the recovery time of the voltage is computed which finally helps to detect the weak areas in the transmission network. Further Critical Clearing time of the weakest bus is also computed.

Keywords: Dynamic Simulation, L-Index, Load flow studies, PSS®E, Power system modelling.

I. INTRODUCTION

Growth of industry in the nation necessitates highly increased consumption of electrical energy. Increase of load in popular cities have made the transmission system highly essential as generating stations are usually at remote places[1]. The importance of providing energy at distant places in cost effective manner with the ever reducing natural assets have led to the establishment of isolated generating stations and ever improving transmission system[2]. In fast growing economies like India, the demand of load increases after every five to six years so subsequently generating stations number and capacity should also be increased proportionately[3][6][7]. Study of power flow leads to the process of preparation of expansion or modernization of both transmission and generation amenities[4]. Besides planning and designing, ahead of execution of the network, study of load flow is carried out by engineers for ensuring proper and efficient performance and also to ensure that standard safety measures have been followed[5-6]. Power flow in and out of each of the terminal buses is the summation of power flowing through all the lines linked to that bus[6-7]. Power flow also call as Load Flow is the steady state analysis that determines

the current, voltage and power flowing in a network in a given pre-loaded condition[8]. A number of algorithms have been developed that helps in resolving the power flow study cases with the increasing of processing power of computers[5][7].

Major power grids of the world have faced power failure in the past. Cascading failure of Indian Power Grid and maximum line loading capacity of inter-region is discussed in[9].The power grid failure of Israel Electric Corporation is emphasized in [10]. The simulation studies of the North-Western American grid after disturbances and subsequent blackout are described in[11-12]. The details of 2003 North America and Europe grid shutdown are discussed in [13-15], it also analyses the consequences during the power failure. Reference [16-17] discusses the post blackout simulation studies of the Athens grid. The graph theory based topological study of Indian Eastern Grid is described in[18].

Study of various grids [9-18] have led to the need of identification of various parameters like the maximum frequency deviation, the recovery time of the voltage after an initial disturbance and critical clearing time. Since only weak area identification is not enough and severity of the weak area parameters are equally or more important for a viable study of the grid. Identification of weak area of network depending upon weak area of voltage sag based on sag severity index a new index called Bus Performance Index is formed based on which bus performance is calculated is described in [19]. Sag severity index are analyzed and discussed using single-event indices incorporating numerical and fuzzy voltage sag index for weak area identification of the network is described in [20]. A PV analysis based voltage stability analysis to determine weak buses in bulk transmission systems, is discussed in [21]. Weak area identification of power system using different ANN(Artificial Neural Network) algorithm is described along with a comparison between voltage collapse calculation based on ANN and L-index in [22]. Modelling and study of steady state and fault analysis of Indian Eastern and North Eastern Regional grid is studied in[23-24].

The work has been ordered as: Section II defines the IEEE standard 118-bus system, Section III is a brief overview of voltage collapse proximity indicator (L-index), Section IV gives a overview about the software package used to model to IEEE 118 bus system,

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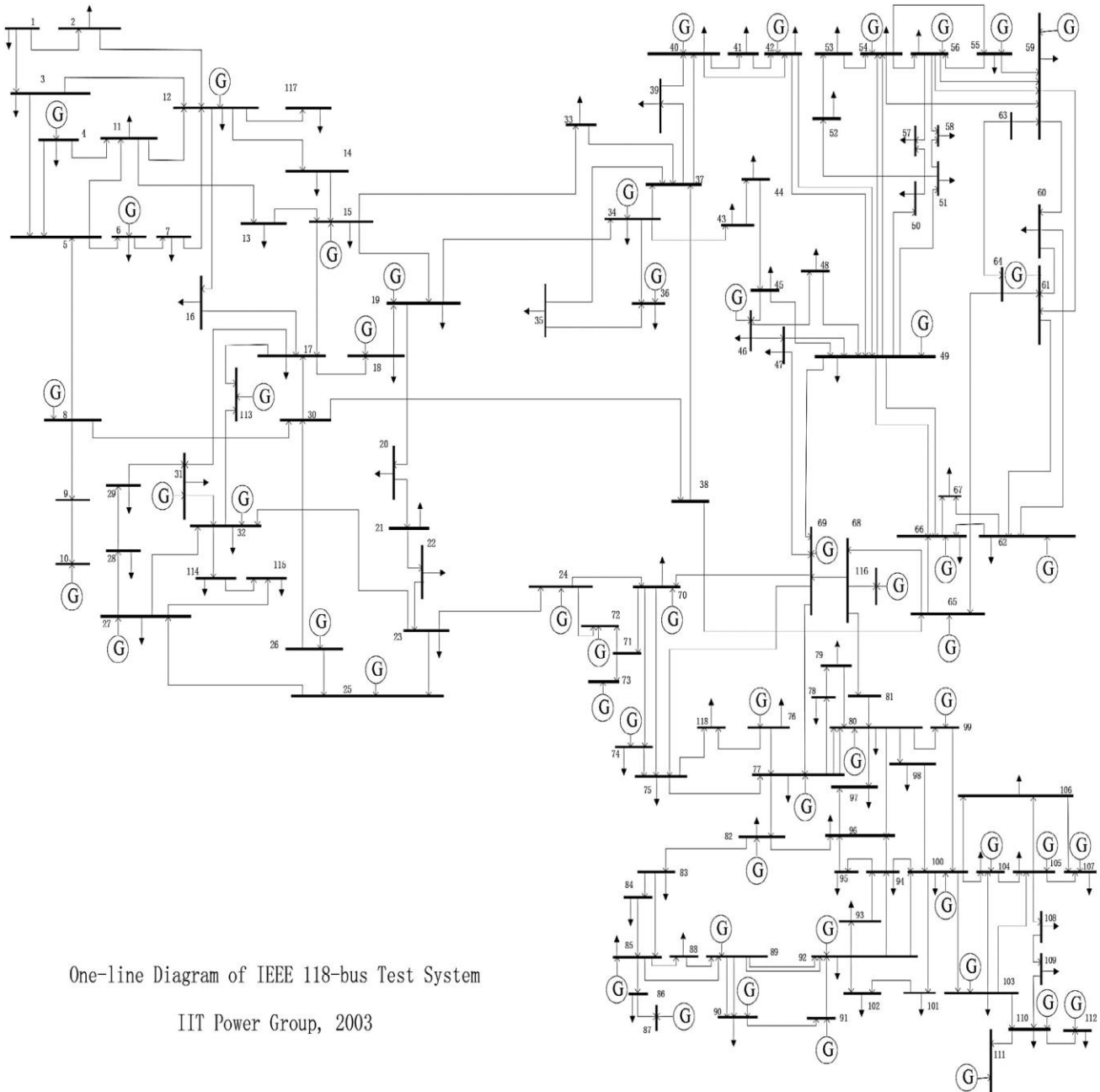
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Section V describes about Dynamic simulation and Critical clearing time, Section VI comprises of discussion of the results followed by Section VII where conclusion and the findings are summarized.

II. DESCRIPTION OF THE SYSTEM

A part of U.S. Midwest Interconnect System was published in 1962, which latter on became well known as IEEE 118 bus network or system[25-26]. It consists of 118 buses and 177 transmission lines, 9 transformers, 91 loads and 19

generators with a total generation capacity of 4,377 MW and compensators of 574 MW capacity[26]. Out of the 118 buses, 32 bus are generator bus belonging to a single zone. IEEE 118-bus system is one of the more complex and bigger system which represents a close approximation of standard Grid system which is the main reason for choosing this system. Here the standard IEEE 118 bus system shown in Fig 1 has been modelled and load flow simulation is computed using PSS®E software.



One-line Diagram of IEEE 118-bus Test System
IIT Power Group, 2003

Fig. 1. IEEE 118 BUS Network[26].

III. L-INDEX VOLTAGE COLLAPSE INDICATOR[30]

L-Index based voltage collapse indicator was introduced by Kessel and Glavitch [22][30] for the two bus network, subsequently it was generalized for multi-bus network[22].

Considering a network of x number of buses which includes generator buses and load buses as 1, 2, ..., g and $(g+1)$, $(g+2)$, ..., x respectively. A load flow analysis is performed and the



subsequently with its help L-Index value are computed as in (1).

$$L_j = \left| 1 - \sum_{i=1}^g F_{ji} \frac{V_i}{V_j} \right| \quad (1)$$

where ,

$j = (g+1), (g+2), \dots, x$ i.e., $j \in C$ set of load buses.

$i \in C$ set of generator buses.

V_i and V_j are the bus voltages at buses i and j , respectively. The values of F_{ji} in (1) is obtained from the Y-bus matrix and is given by (2).

$$F^{LG} = [Y^{LL}]^{-1} [Y^{LG}] \quad (2)$$

where $[Y^{LL}]$ and $[Y^{LG}]$ are sub matrices of the Y-bus matrix[22].

The range of L-Index value is [0, 1]. When L tends to 1 systems slants towards voltage collapse. The value between L-index and 1 is the load margin or stability margin for a given predefined loading condition[22]. At a given loading condition, load flow analysis is performed and with its help L-index values are computed. The bus having the largest value of L-index is the most prone to voltage collapse[30]. Thus the weakest link within the system can be easily locate thereby, prompting electric power utilities to take protective measures preventing the system to become unstable.

IV. SOFTWARE PACKAGE USED FOR MODELLING[27-29]

Power System Simulator for Engineering(PSS/E) [27-29] is a program from Siemens Power Transmission & Distribution, Inc., Power Technologies International (Siemens PTI) for simulating, analyzing and optimizing the performance of power system[27]. User is equipped with vast majority of scenario in different technical areas, including: load flow, fault calculation, dynamic analysis, pricing and policies, transfer interval analysis and network simplification etc. It runs in both graphical user interface mode as well as automation mode which includes all the options like load flow studies, different types of fault analysis, optimal power flow, various switching studies etc. PSS@E also provides the user with wide variety of options like automated raw data input, standard data input, output in different standard format, manipulation, preparation and analysis etc. Additionally user can take the benefit of its computational tools by preparing graphical, tabular or spreadsheet representation. It can also be integrated with different recognised software packages to increase its efficiency and usefulness.

V. DYNAMIC SIMULATION AND CRITICAL CLEARING TIME

In dynamic simulation it is assumed that the plant is in a state of change. A dynamic simulation takes time as a variable of consideration. It assumes that the system is in a state of continuous variation with respect to time [31]. Dynamic simulation is used to examine different control techniques, by introducing disturbances or faults to the plant, and seeing how well the controllers respond to the problem. [32-33]

Critical Clearing Time (CCT) is the extreme time for which fault can be induced in a system depriving the system of becoming stable[34]. It determines the quality of the protection necessary for the power network. In calculating critical clearing time generally three-phase fault is a constraint which is applied as it shows the transient stability boundary of the system [8]. The basis of this analysis is that it searches within a user-specified data range. Each step of the analysis is performed by an automated run of the program or simulation process thereby producing either unstable or stable states depending upon the time factor [7]. The user determines the stable and unstable states based on his pre-determined conditions specified earlier. The first unstable state and last stable state is hereby fully analyzed to compute the critical clearing time [35].

VI. RESULTS

IEEE standard 118 bus system has been modelled[36] in this paper using PSS/E. The power flow results has been used to calculate the L-Index of all the buses. The results are then arranged in descending order and buses having higher L-Index value are presented as shown in Table I. The bus having higher L-Index value are more prone to voltage collapse which helps us to predict the weak bus[22].

TABLE I. L-INDEX VALU

Sl. No.	Bus No	Bus Voltage	L-Index
1	95(CALDWELL)	138	0.0158
2	71(NPORTSMT)	138	0.0135
3	44(WMVERNON)	138	0.0116
4	43(S.KENTON)	138	0.0113
5	41(S.TIFFIN)	138	0.0109
6	82(LOGAN)	138	0.0109
7	96(BAILEYSV)	138	0.0096
8	83(SPRIGG)	138	0.0093
9	51(NEWCMRST)	138	0.0089
10	75(STHPOINT)	138	0.0089

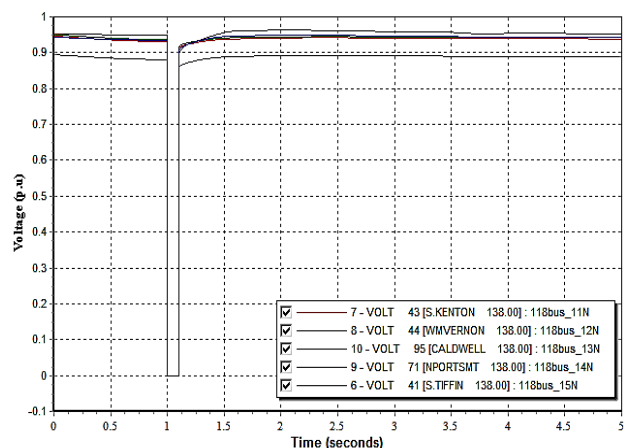


Fig. 2. Recovery time of the voltage.

Further dynamic simulation are performed and buses which takes maximum time to reach a state of stability after the initial disturbance are presented as shown in Table II and buses having higher recovery time are plotted as shown in Fig 2 and Fig 3 respectively.

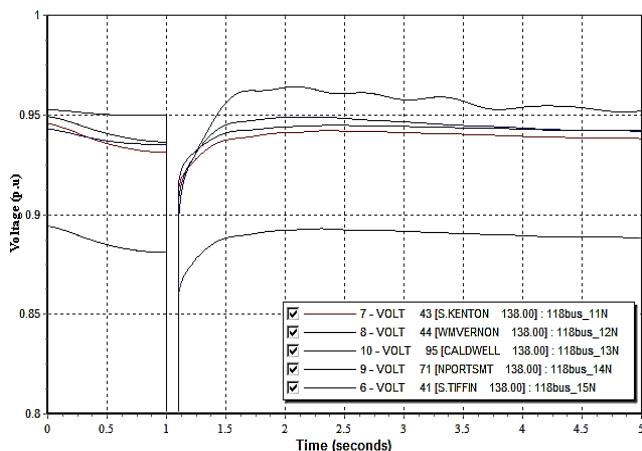


Fig. 3. Enlarged view of the recovery time.

TABLE II. voltage Recovery Time

Sl. No.	Bus No(Name)	Bus Voltage	Time for voltage recovery
1	95(CALDWELL)	138	4.624999
2	71(NPORTSMT)	138	3.616665
3	44(WMVERNON)	138	3.591666
4	43(S.KENTON)	138	3.399999
5	41(S.TIFFIN)	138	3.283332
6	82(LOGAN)	138	3.194799
7	96(BAILEYSV)	138	3.174899
8	83(SPRIGG)	138	3.166666
9	51(NEWCMRST)	138	3.058332
10	75(STHPOINT)	138	3.041666

TABLE III. Summary

Sl. No.	Bus Name (Voltage Level kV)	L-Index Bus order	Dynamic simulation Bus order	Voltage Recovery time	Critical Clearing Time(ms)
1	CALDWELL (138.00)	0.0158	CALDWELL	4.624999	217

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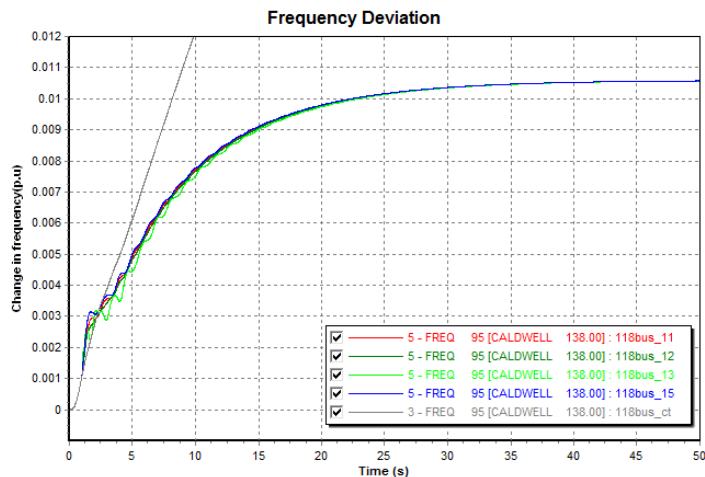


Fig. 4. Calculation of Critical clearing Time

Frequency deviation of Caldwell bus with increase in transient disturbance is shown in Fig 4. Further while calculating critical clearing time transient disturbance was increased by 10ms but closer to the probable CCT for better accuracy it was reduced to 1 ms. Critical clearing time of Caldwell Bus is found to be 235ms from simulation results.

VII. CONCLUSION

This work presents a detailed study of IEEE 118 bus network. Power flow analysis is performed after modelling the standard IEEE 118 bus system in PSSE. L-Index of the buses are calculated which predicts the weak bus in the system. 95(CALDWELL) is found to be the most vulnerable bus which is closely followed by 71(NPORTSMT), 44(WMVERNON), 43(S.KENTON) and 41(S.TIFFIN). Further dynamic simulation is also performed at the buses which further validates the weak buses. Thus from the above inference we can conclude that 95(CALDWELL) is the weakest bus and area near it ultimately becomes more prone to voltage collapse as presented in Table III. So necessary precautionary measures should be taken to avoid any cascading failure of the system. The Critical Clearing time from the dynamic simulation was found to be 217ms.

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