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Abstract: The distributed generation scheme with integration of renewable energy sources furnishes the feasible solution to acquire the stable power demand in grid-connected system under sudden load interruptions. Over the various compensation devices, the highly recognized multi-purpose Static Compensator is integrated to grid for attaining enhanced power quality features. A co-generation based grid-integrated renewable sources are used but, the output of these sources are maintained un-constant due to presence of variable source. In this paper, a novel current controller was developed to maintain output of co-generation system as constant and achieve the load demand continuously. But, this scheme has two major disadvantages, primary one is current controller prerequisites the traditional PI controller, which is controlled by intelligent based Fuzzy-Logic controller that is adopted. The second is, the regular 3-level voltage-source inverter is used in STATCOM which has square-wave output voltage requires high range of filter units, more common-mode voltage, high dv/dt stress, more switching losses, low efficiency, so on. These issues are counteracted by utilizing multilevel inverter with attractive modulation scheme. The performance evaluation of proposed Multilevel Inverter based STATCOM is verified by traditional PI and proposed Fuzzy-Logic current controller by using MATLAB/SIMULINK tool and results are conferred with comparisons.

Keywords: Advanced Current Controller, Fuzzy-Logic Controller, Multi-Level Inverter, Multi-Purpose Static Compensator, PI Controller, Power Quality Improvement.

I. INTRODUCTION

The growth of global population, increased utilization of power-electronic devices which consumes electrical energy and demand of electrical energy has been extended to unpredicted levels. In present scenario, the electrical energy has been diminished due to limited fossil fuels, social, environmental and geographical issues [1], [2]. To fulfill these issues, several attempts have been constituted to meet the energy demand natively through Distributed Generators (DG's) [3]. The incredible establishment of DG scheme provides the greater energy demand, clean energy, sustain the stable real-power exchanging under sudden-load variations with the help of Renewable Energy Sources (RES) [4], [5]. Renewable sources like Wind-Turbine (WT), Fuel-Cell (FC)

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and Solar-Photovoltaic (SPV) systems are directly interfaced to grid system by employing power-conditioner systems. Harnessing the electric energy from SPV system furnishes the clean energy, extensive nature in present climatic situations, pollution free, long life and greater efficiency characteristics that are the key merits of SPV system.

Over the single sourced DG scheme, two or more RES's are integrated as hybrid formation called as co-generation system for maintaining continuous load demand eliminate the sudden changes, etc. Generally, the output of SPV is very low in nature and are integrated to PCC/load with the accomplishment of high-voltage gain DC-DC boost converter and DC-AC Voltage-Source Inverter (VSI) is utilized with attractive control objective. Many industrial and domestic appliances utilize the power-electronic converters which lead to poor power quality standards in grid connected system. Severe economic technical impacts are greatly experienced because of current and/or voltage imperfections at PCC level [6]. These concerns are frequently appeared due to presence of non-linear loading, faulty conditions, sudden-loading conditions, etc.

The custom-power technology is introduced in [7], which adopts the group of apparatus furnishes the enhanced PQ features over the passive compensation methods. In former days, the traditional passive compensation schemes are accessible for PQ enhancement consisting of passive filter modules, which are connected to PCC either shunt formation or series formation. Several de-merits are highlighted in [8], such as fixed compensation scheme, low dynamic response, massive size, resonance issues etc. Multilevel Inverter (MLI) topologies have been broadly accepted for high-power medium voltage applications and have superior performance over the traditional 2-level inverters. Generally, MLI involves utilization of higher range of active semi-conductor switches to fulfill the power-transformation based on very small voltage steps. Several advantages are achieved over the formal approaches such as, generation of good qualitative RMS waveforms, low harmonic content, minimum dv/dt stress, low EMI compatibility issues, etc. The MLI's are framed as series connected semiconductor switch which enables high-voltage operations and produces the staircase output voltage and/or current waveform.

The proposed multi-level multi-purpose Static Compensator (MLI-STATCOM) reduces the necessity of additional DG-VSI topology and minimizes the overall

compensation costs which increase the power demand capability. The primary task of

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proposed MLI-STATCOM is harmonic elimination, reactive power improvement, power-factor correction, unbalanced load, and compensates all current related issues [9]. The secondary task of MLI-STATCOM is integration of co-generation system into local load as DG-inverter for regulating the sudden load demand, etc. In this paper, CO-generation based solar PV and Wind Conversion scheme is used as secondary source of grid-connected DG system integrated to PCC level. But the output of these two source are varied with respect to changes in wind flow and light intensity of SPV arrays which affects the output of DG-VSI and the DC-link voltage as un-constant, may create un-balanced grid voltages. To eliminate the issues coming from PI controller, a new intelligent based Fuzzy-Logic Controller is pre-requisite enhancing better performance of multi-purpose for MLI-STATCOM with improved PQ compensation characteristics with a good stability index. A critical evaluation of SPV-Wind CO-generation scheme based MLI-STATCOM is validated in both PQ enhancement and DG integration scheme by using MATLAB/SIMULINK tool and confers the simulation results with appropriate comparisons.

II. PROPOSED DIODE-CLAMPED MULTI-LEVEL STATCOM

The MLI topologies are generally categorized as Diode-Clamped Multi-level Inverter (DC-MLI) type, Flying Capacitor Multi-level Inverter (FC-MLI) type and Cascade H-Bridge Multi-level Inverter (CHB-MLI) type (covering both Symmetrical and Non-Symmetrical versions). Over the several MLI topologies, CHB-MLI is highly recognized for many applications because of greater-modular design, no need of auxiliary components, etc. The CHB-MLI have several disadvantages like, it is limited to certain range of levels due to high number of switches and DC sources which are required for generating increased output voltage levels [10]. When it is operated for higher number of voltage levels, it can produce high switch stress pertains to more switching losses and decreasing the over-all efficiency. Thus DCMLI is suitable and preferable in STATCOM for compensating all voltage and current related issues and also DG integration scheme under sudden-load changes [11].

The MLI-STATCOM is mainly integrated to grid-connected systems to improve PQ features and it comprises of three-phase Diode-Clamped MLI module, reference current generator, gate-drive circuit, DC-link capacitor, sensing elements, second-order LC filters, etc. The DCMLI-STATCOM is acted as active compensation device operated in a current control mode for harmonic elimination coming from non-linear loads and also utilized as integrated converter in DG operation [10]. In this scheme grid is supporting the dual non-linear loads, at initial stage load-1 is connected and the second load is connected suddenly at a time of 0.3 sec. Due to this sudden switching of additional load apparatus, the requirement of load current is extracted from the grid which affects the PCC and requirement of load power was increased. During DG operation DCMLI is operated in voltage-controlled mode to provide active-power to the load. Generally, the DG scheme requires additional source as

battery energy storage system (BESS), but it requires additional charge management schemes. Over the BESS, the renewable energy plays a significant role in DG schemes and in that SPV-Wind is the major power producer as CO-generation scheme [12].

A.DIODE-CLAMPED MLI TOPOLOGY

The schematic diagram of 5-level DC-MLI topology is shown in Fig.1. It comprises of 2(n-1) switches, named as positive group switches S_{1a} , S_{2a} , S_{3a} , S_{4a} , and negative group switches are $S_{1a'}$, $S_{2a'}$, $S_{3a'}$, $S_{4a'}$; (n-1) x (n-2) clamping diodes are D_{1a} , D_{1b} , D_{2a} , D_{2b} , D_{3a} , D_{3b} and $D_{1a'}$, $D_{1b'}$, $D_{2a'}$, $D_{2b'}$, $D_{3a'}$, $D_{3b'}$, (n-1) DC-link capacitors, without clamping capacitors.



Fig.1 Schematic Diagram 5-Level DCMLI Topology

The DC-link voltage (V_{dc}) is split into five voltage levels by utilizing four series-interfaced bulk capacitors such as, C1a, C2a, C3a, C4a; these are separated with a middle-point as a neutral point (N). The output voltage Vo has five voltage levels such as $V_{dc}/4$, $V_{dc}/2$, $-V_{dc}/4$, $-V_{dc}/2$ and $0V_{dc}$ with respect to neutral point N. The switching pattern of DC-MLI for switching the consequent switches is illustrated in Table.1. In that, "H" represents the switch-ON state and "L" represents the switch-OFF state. To demonstrate how 5-level stair-case voltage is synthesized, a load is connected in between the output point (Vo) and neutral point "N". In mode A, the upper switches S_{2a} , S_{3a} , S_{4a} and lower switch S_{1a} is conducted in positive half cycle for generation of $V_{dc}/4$ at output node $V_{o.}$ In mode B, the upper switches S_{1a} , S_{2a} , S_{3a} , S_{4a} are conducted in positive half cycle for generation of $V_{dc}/2$ at output node V_o . In mode C, the upper switches S_{3a} , S_{4a} and lower switches S_{1a} ', S_{2a} 'are conducted for generation of $0V_{dc}$ at output node V_o.

Table.1 Switching Pattern of DCMLI topology

Output	Operated Switching States							
Voltag								
e (V ₀)	S _{1a}	S _{2a}	S _{3a}	S_{4a}	S _{1a} '	S _{2a} '	S _{3a} '	S _{4a} '
$V_{dc}/4$	L	Η	Н	Н	Н	L	L	L
$V_{dc}/2$	Н	Н	Н	Η	L	L	L	L
0 V _{dc}	L	L	Н	Н	Н	Н	L	L
-V _{dc} /4	L	L	L	Н	Н	Н	Н	L
-V _{dc} /2	L	L	L	L	Н	Η	Η	Н



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In mode D, the upper switch S_{4a} and lower switch S_{1a} ', $S_{2a}'S_{3a}'$ are conducted in negative half cycle for generation of $-V_{dc}/4$ at output node V_o . In mode E, all the lower switch S_{1a}' , $S_{2a'}, S_{3a'}, S_{4a'}$ are conducted in negative half cycle for generation of $-V_{dc}/2$ at output node V_o with respect to furnishing of optimal switching state. The SPV-Wind is the primary source of DCMLI, commonly which is integrated to DC-link capacitor C_{dc} by utilizing high-voltage gain DC-DC converter. This boost converter transforms the low-level SPV-Wind voltage into high-level required voltage to drive the DCMLI-STATCOM and sustains DC-link voltage V_{dc} as constant [13]. An interfacing LC filter modules are allied to sustain the proper compensation characteristics at PCC of grid system. The performance evaluation of multi-purpose DCMLI-STATCOM is well established based on active-filtering technique immensely pre-requisite of harmonic mitigation as in-phase compensation process [14]. It supervises the injection currents at PCC level for appropriate harmonic mitigation, reactive power exchanging, power-factor improvement and active power injection by utilizing attractive reference current control scheme. The schematic diagram of proposed DCMLI-STATCOM with CO-generation scheme is illustrated in Fig.2.



Fig.2. Schematic Diagram of Proposed Multi-Purpose DCMLI-STATCOM Driven by CO-Generation SPV-Wind System

III. PROPOSED ADVANCED CURRENT CONTROLLER

In this paper advanced Icos Φ controller control technique is used for generation of switching states to static compensator. Control technique of the proposed system is shown in Fig.3. In this case input current and voltage is sensed from sensor and input to Fourier and phase lock loop block respectively. Fourier block generates fundamental current magnitude and cosine displacement. Similarly phase lock loop generates active component after active component product with PI control output. This output is adding with product of fundamental current magnitude and cosine displacement and finally generates reference signal. This reference signal compared with actual source current and gives to Hysteresis Current Controller (HCC) for generation of switching states to IGBT gate terminal in static compensator circuit. According to pulse input switching devices IGBT can be operated and static compensator output can be control. In Icos Φ control techniques I is fundamental load current amplitude and cos Φ is displacement power factor of load and their product is Icos Φ so proposed controller is named as Icos Φ controller.



Fig.3 Block Diagram of Proposed Control Circuit

A.Fuzzy-Logic Controller

The Fuzzy-Logic (FL) controller authorize based on knowledge system which includes Fuzzy membership functions and Fuzzy rule-base to assimilate the human knowledge for getting subjective decisions. Some efforts have been developed to attain improved characteristics on system performance by integrating learning mechanism by regulating membership functions and rule-base system of the Fuzzy-controller. The heart of the Fuzzy controller is a knowledge system which comprises of information unit for providing linguistic variables and fuzzy rule base.





The system associated with database is used to

characterize the fuzzy-rule functions and manipulation of fuzzy data in a Fuzzy-Logic controller and the heuristic rules of the knowledge which are highly influencing the controller performance. The inference mechanism decides how the fuzzy-logical operations are accomplished, and knowledge base simultaneously determines the output of fuzzy logic controller based on IF-THEN rules. The Fuzzy-Logic controller is used to furnishing the reference current signal for generation of optimal switching states to compensator based on mamdani structure. In that single FL controller is used for error regulation in DC-link controller between the actual and reference DC-link voltage in an advanced current control theory for generation of reference current signals [15]-[17].

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The error is attained from comparison of actual and reference components in terms of current imperfections are considered as input or output for FL controller with seven linguistic variables. All the membership functions of error, change in error and output are considered as triangular functions because of simple control functions as linearity principle. These membership functions are transformed to fuzzy data by using fuzzification process for making the favorable decisions as rule-base system and provide the output signals and again re-transformed into general data by using centroid method of defuzzification process. The utilized membership functions are Zero (ZE), Positive-Large (PL), Positive-Medium (PM), Positive-Small (PS)and Negative-Large (NL), Negative -Medium (NM), Negative -Small (NS), respectively as depicted in Fig.5 and the rule-base is depicted in Table.2. The FL controllers generates the reference current signals and send to gate-drive circuitry for generation of optimal switching states to MF-STATCOM for achieving good compensation features.



Fig. 5 Membership Functions of FLC

Table.2Rule-Base of FLC

e							
Δe	NL	NM	NS	ZE	PS	PM	PL
NL	NL	NL	NL	NL	NM	NS	ZE
NM	NL	NL	NL	NM	NS	ZE	PS
NS	NL	NL	NM	NS	ZE	PS	PM
ZE	NL	NM	NS	ZE	PS	PM	PL
PS	NM	NS	ZE	PS	PM	PL	PL
PM	NS	ZE	PS	PM	PL	PL	PL
PL	ZE	NM	NS	ΕZ	PS	PM	PL



Fig.6 Block Diagram of Proposed Fuzzy Control Circuit

IV. CO-GENERATION SCHEME

Two or more renewable energy sources are connected to a grid connected system as hybrid or CO-generation electrical power system. In this paper dual SPV-Wind energy sources are used for providing energy to the loads under sudden interruptions coming from additional load with the help of DCMLI-STATCOM.

A.Solar PV System

The solar PV energy is primary energy source in CO-generation system and it relies on the photo-electric effect to produce the electrical energy [4]. In the dark circumstances of the PV cell is akin of normal diode, when the sun-light energy is higher than the semiconductor energy gap which provides the cell electrons becomes free and existed current travels through external circuitry. As PV cell have low voltage and more fragile, formed as modules and securing with an enclosed metallic case. Based on the requirement power levels, the PV cells are integrated as series and/or parallel to form as solar PV-array. The mathematical model of the PV cell is extracted by single diode model as depicted in Fig.7. The outcome of the PV cell resulted based on the physical attraction of the PV cell is relating with the I_{sv} ,

 I_{phv} , R_{sv} from the irradiation and temperature over the other.



$$Iv = I_{phv} - (I_{sv} [\exp q \, \frac{V_{PVv} + R_{sv} Iv}{NKT}] - 1) - \left[\frac{V_{PVv} + R_{sv} Iv}{R_{shv}}\right]$$
(1)

The Eqn.1 illustrates the current equation of PV cell, Where,

 I_{phv} - Photovoltaic current in amps (A)

 I_{sv} - Diode's reverse saturation current in amps (A)

q - Electron charge

 V_{PVv} - Terminal voltage of the diode in volts (V)

- K Boltzmann's constant value
- T Junction wise temperature
- N Ideality index factor of diode
- R_{shv} Shunt resistance of PV cell in ohms (Ω)
- $R_{\rm sv}$ -Series resistance of PV cell in ohms (Ω)

B. Wind Energy System

The Wind-Energy scheme acts as secondary source and it comprises of induction generator, due to variable speeds and no need of any additional field excitation circuits. The available wind energy is



$$P_w = \frac{1}{2}\rho A V_w^3 \tag{2}$$

Where, ρ is air-density, A is area of turbine, V_w is wind speed and impossible to extract the kinetic energy of wind, its extraction in terms of fraction of power in wind known as,

$$P_m = C_p P_w \tag{3}$$

$$P_m = C_p \frac{1}{2} \rho A V_w^3 \tag{4}$$

Then $A = \pi R^2$

V. MATLAB/SIMULINK RESULTS

The performance of proposed MF-STATCOM is verified under traditional PI and proposed Fuzzy-Logic control functions by using MATLAB/SIMULINK tool with operating specifications as depicted in Table.3.

Table.3 Operational Parameters of Proposed DCMLI-STATCOM

Parameters	Values
Source Voltage	415 V, 50Hz
Source Impedance	0.1+j0.05Ω
Load Impedance	8+j12Ω
DC-Link Capacitor	1500µF
VSI Filter Units	R-0.1; L-5mH
PI Controller Gains	Kp-0.8; K-0.5

Case A: Integration of Co-Generation Scheme in Grid Connected System by Using Multi-Purpose STATCOM with Traditional PI based Advanced Current Controller





(d) THD of Source Current under with MF-STATCOM under Traditional PI Controller Fig.8 Simulation Results of Proposed MF-STATCOM under Traditional PI Controller with CO-Generation

Scheme

The simulation results of proposed MF-STATCOM are evaluated under traditional PI controller with CO-generation scheme as shown in Fig.8. Fig. 8(a) represents the source voltage, source current, load current and shunt injected current, respectively. The electric power grid is driving the dual non-linear loads, the primary load is operated continuously but the secondary load is switched ON at time 0.3 sec. Due to these sudden interruptions, source current is maintained as un-constant because, non-presence of MF-STATCOM and the load also maintained same. That's why the shunt-injected current maintained as zero and affects the PQ features at PCC of grid connected system. When the compensator is integrated at PCC level, the source current is maintained as constant and sinusoidal nature because the

STATCOM compensates the harmonic distortions and sudden interruptions coming from load. But the load current maintained as constant with respect to sudden interruptions, the source current is maintained constant, balanced and harmonic free response by injecting the in-phase currents of the STATCOM powered by Co-generation scheme. The THD of source current without MF-STATCOM is 26.81% as depicted in Fig. 8(c), THD of source current with MF-STATCOM is 2.77% as depicted in Fig. 8(d). The THD of source current under presence of MF-STATCOM is within IEEE-519 standards.

Case B: Integration of Co-Generation Scheme in Grid Connected System by Using Multi-Purpose STATCOM with Proposed Fuzzy-Logic based Advanced Current Controller



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The simulation results of proposed MF-STATCOM are evaluated under Proposed Fuzzy-Logic controller with CO-generation scheme as shown in Fig.9. In that, Fig. 9(a) represents the source voltage, source current, load current and shunt injected current, respectively. When the compensator is integrated at PCC level, the source current is maintained as constant and sinusoidal nature because the STATCOM the harmonic distortions and sudden compensates interruptions coming from load. But the load current maintained as constant with respect to sudden interruptions, the source current is maintained constant, balanced and harmonic free response by injecting the in-phase currents of the STATCOM powered by CO-generation scheme. The THD of source current with MF-STATCOM under Proposed Fuzzy-Logic controller is 0.67% as depicted in Fig. 9(b). The THD of source current under presence of MF-STATCOM with Proposed Fuzzy-Logic controller is well within IEEE-519 standards which enhances the PQ features in a grid connected CO-generation scheme. The Solar PV output voltage is attained with a value of 800V and the output of Wind-Energy is attained with a value of 820V and the current of co-generation scheme is nearly 1200A as depicted in Fig.9 (c), (d) and (e) respectively. Over the classical PI controller, the proposed Fuzzy Logic controller has better reduction of source current distortions, the THD is well within IEEE standard limitations for attaining improved PQ features in a MP-STATCOM as depicted in Table.4 and the graphical view is depicted in Fig.10.

Table.4 THD Comparison of Source Current under Several Control Functions

THD (%)	Without Compensator	Classical PI Controller	Proposed Fuzzy-Logic		
			Controller		
Source Current	26.81%	2.77%	0.67%		



Fig.10 Graphical View of THD Analysis of Source Current under Traditional PI and Proposed Fuzzy-Logic Control Functions



(a) Classical VSI Output Voltage

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(e) THD of Proposed5-Level Output Voltage Fig.11 Simulation Results of Proposed 5-Level DCMLI and Classical VSI Output Voltages Table.5 THD Comparison of Source Current under

Classical VSI and Proposed DCML1 Topologies						
THD (%)	Without Compens ator	Under Cl	assical VSI	Under Proposed 5-Level DCMLI		
		Classical PI Controll er	Proposed Fuzzy-Lo gic Controller	Classical PI Controller	Propo sed Fuzzy -Logic Contr oller	
Source Curren t	26.81%	2.77%	0.67%	2.01%	0.43 %	

Over the classical VSI-STATCOM, the proposed DCMLI-STATCOM has better reduction of source current distortions. The THD is well within IEEE standard limitations for attaining improved PQ features as depicted in Table.5.

VI. CONCLUSION

The proposed DCMLI-STATCOM based advanced Fuzzy-Logic based current controller provides the optimal switching states under both PQ enhancement and DG operation. Under DG operation, the DCMLI-STATCOM doesn't require any additional VSI modules and control objectives for mitigating sudden interruptions coming from load. The proposed DCMLI-STATCOM has better features over the classical VSI-STATCOM and has carries low harmonic distortions, low switch stress, high efficiency, so on. The advanced control theory furnishes constant energy to the load under variations in SPV-Wind system by using attractive

control function which reduces the additional controller, minimizes the cost, size and space requirement etc. The performance analysis of proposed DCMLI-STATCOM is evaluated under various working modes by using MATLAB/SIMULINK tool and results are conferred with appropriate comparisons. The harmonic spectrum's of source current under traditional PI and proposed Fuzzy controller, classical and proposed DC-MLI is getting well within IEEE-519/1992 standards limitations.

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