

Manufacturing Capability of Micro-Transfer Printing

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Abstract

Micro-Transfer printing is a mass transfer process that provides fast and precise assembly of micro-components onto non-native substrates. The process was originally conceived at the University of Illinois [1, 2], and has been developed at X-Celeprint. The process involves the fabrication of retrievable micro-components, elastomer stamp retrieval and transfer-printing of the micro-components to a non-native substrate and finally the interconnection [3]. With this process a large array of micro-components can be transferred at one time with an accuracy of $\pm 1.5\mu\text{m}$ 3 sigma and transfer yields exceeding 99%.

A pilot line for micro-transfer printing is being built in Erfurt, funded in part by the EU. This project, called Microprince, includes the technology transfer and scale-up of several applications such as printed GaN HEMTs and GaAs sensors. The pilot line will have 200 mm wafer capable transfer printing tool and equipment to interconnect the transferred devices to the target wafer. This paper addresses the manufacturing capability of Micro-Transfer Printing as it is scaled up.

Silicon nitride microchips of various sizes will be used as vehicles to release, pick and print arrays of chips in several learning cycles that will be used to qualify both the process and the equipment. Statistically significant transfer yield and print precision data for multiple chip sizes will be presented to benchmark the capability of the pilot line. Process throughput will be discussed, as a function of process tact time, density of micro-components, chip size and size of the transfer array. Figure 1 presents an example of the source material that will be used. Figure 2 presents examples of printed arrays that will be characterized as a part of pilot line qualification.

Elastomer stamp life time is an important consideration for manufacturing. Elastomer stamp lifetime will be studied by using the same stamp over thousands of print cycles. In a previous study [4], no change in the performance of the stamp was observed after more than 30,000 print cycles. The impact of scaling up the stamp size on print yield and precision will also be characterized.

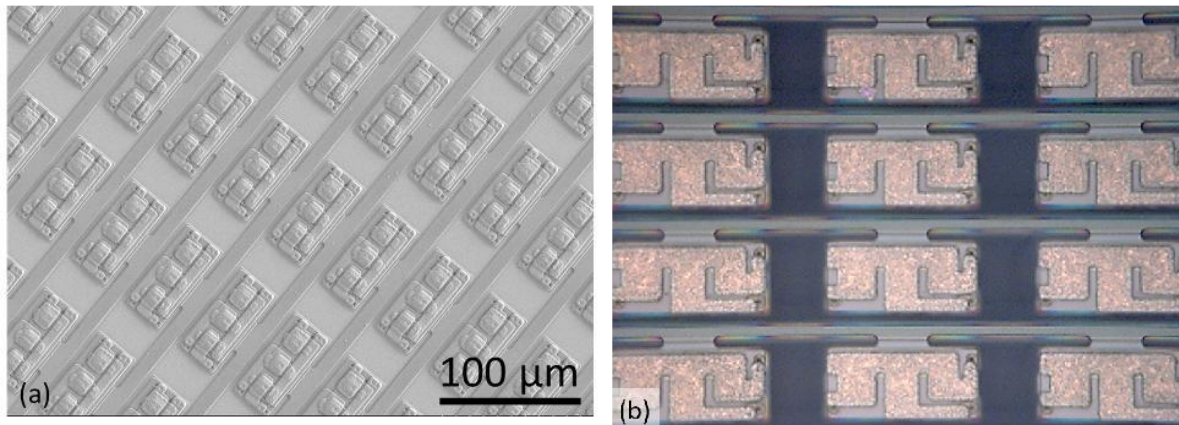


Figure 1: (a) SEM image of 70 μm x 35 μm chips before release and (b) optical image of the chips after release. The silicon nitride chips are at a pitch of 100 μm x 50 μm and are attached to the silicon substrate by anchors and tethers.

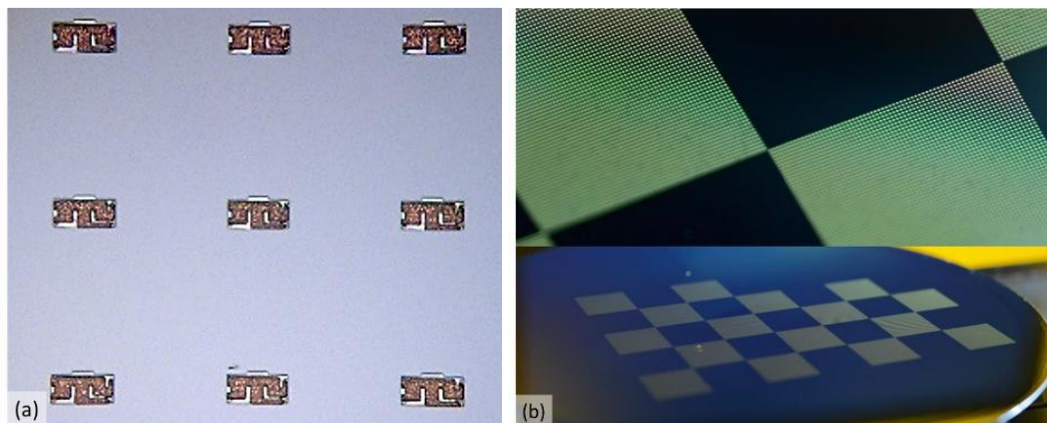


Figure 9: Print results from 70 x 35 μm chips in a 64x64 array (a) Image of printed 70 x 35 μm silicon nitride array (b) image of 16 printed arrays with a blow up of a section

References

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