

Nanoscale GaN LED arrays for chip-based optical nanoscope

Hutomo Suryo Wasisto^{1,*}, Jan Gülink¹, Steffen Bornemann¹, Sindhuri Vodapally¹, Klaas Stempel¹, Shinta Mariana¹, Tony Granz¹, Gregor Scholz¹, Agus Budi Dharmawan^{1,2}, Matthias Auf der Maur³, Aldo di Carlo³, Nicolai Markiewicz^{1,4}, Joan Daniel Prades⁴, Andreas Waag¹

¹ *Institute of Semiconductor Technology (IHT) and Laboratory for Emerging Nanometrology (LENA), Technische Universität Braunschweig, 38106 Braunschweig, Germany*

² *Faculty of Information Technology, Tarumanagara University, 11440 Jakarta, Indonesia*

³ *Department of Electronic Engineering, University of Rome Tor Vergata, 00133 Rome, Italy*

⁴ *MIND-IN²UB, Department of Electronic and Biomedical Engineering, University of Barcelona, 08028 Barcelona, Spain*

* Presenter, E-mail: h.wasisto@tu-braunschweig.de

Since the invention of high-power gallium nitride (GaN) light-emitting diodes (LEDs), enormous investigations have been carried out in the last decade, not only to improve the material quality and device performance but also to extend their applications. InGaN/GaN LEDs have been broadly employed in general illumination and backlight units because of their higher luminous efficacy and longer lifetime compared to conventional light sources. Furthermore, several innovative optoelectronic devices have been introduced into industrial markets (e.g., high-brightness display and optical sensors in smartphones). By integrating LEDs with CMOS control electronics, matrix-addressed and individually controlled GaN microLED arrays could be realized with a display luminance of 10^6 cd/m² (12 W/cm²), which is a factor of 10^3 higher than normal commercial displays [1]. However, their spatial resolution was still low, which resulted from the LED dimensions with pixel and pitch sizes of several micrometers. Thus, in this work, nanoscale InGaN/GaN LED arrays with individual pixel control were designed and fabricated to be integrated as a novel illumination source in a chip-based lensless microscope (i.e., ChipScope) for real-time monitoring of biological cells. The challenging 3D processing steps of the high-aspect-ratio nano-/microLED arrays have been optimized to create tiny optoelectronic modules. To fabricate the well-ordered high-aspect-ratio nano-/microstructures, a top-down approach comprising nanophotolithography and hybrid etching was employed [2]. In this case, GaN LED fins with smooth sidewalls could be realized from the sequential processes of SF₆/H₂-based ICP-RIE and KOH-based wet chemical etching (Figures 1(a) and (b)). As top and bottom surfaces of the structures are distantly separated by about 3.5 – 5 μm, device planarization plays a critical role for the feasibility of top metal contact deposition. Thus, different polymer filling materials have been carefully investigated (e.g., photoresist, spin-on-glass, and benzocyclobutene (BCB)). Along with the device fabrication, simulations of the light emission patterns have been conducted with different conditions of the integrated materials to optimize the nanoLED designs (Figure 1(c)).

References

- [1] J. Herrnsdorf, J. J. D. McKendry, S. Zhang, E. Xie, R. Ferreira, D. Massoubre, A. M. Zuhrdi, R. K. Henderson, I. Underwood, S. Watson, A. E. Kelly, E. Gu, M. D. Dawson, "Active-matrix GaN micro light-emitting diode display with unprecedented brightness," *IEEE Transactions on Electron Devices*, 62(6), 1918-1925 (2015). DOI: 10.1109/TED.2015.2416915
- [2] F. Yu, S. Yao, F. Römer, B. Witzigmann, T. Schimpke, M. Strassburg, A. Bakin, H.W. Schumacher, E. Peiner, H.S. Wasisto, A. Waag, "GaN nanowire arrays with nonpolar sidewalls for vertically integrated field-effect transistors," *Nanotechnology*, 28(9), 095206 (2017). DOI: 10.1088/1361-6528/aa57b6

Supplementary information

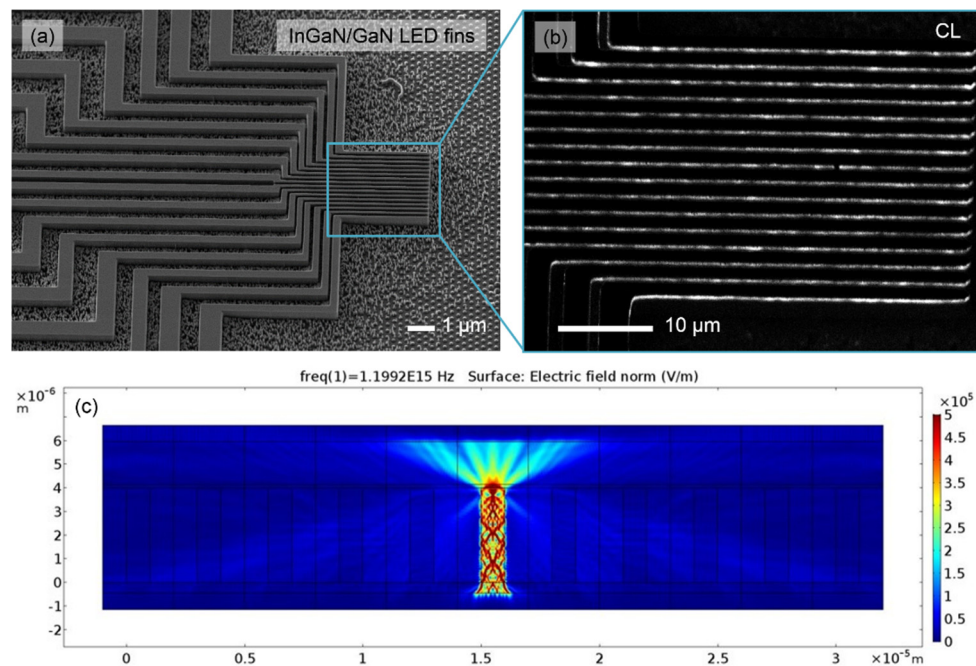


Figure 1. (a) SEM image of 1 μm InGaN/GaN LED fin array after ICP-DRIE and (b) its corresponding cathodoluminescence (CL) result showing the monochromatic emission at the peak wavelength of 465 nm. This nanoLED array is used in chip-based lensless microscope (ChipScope). (c) 2D simulation results of the light emission pattern from a nanofin LED.