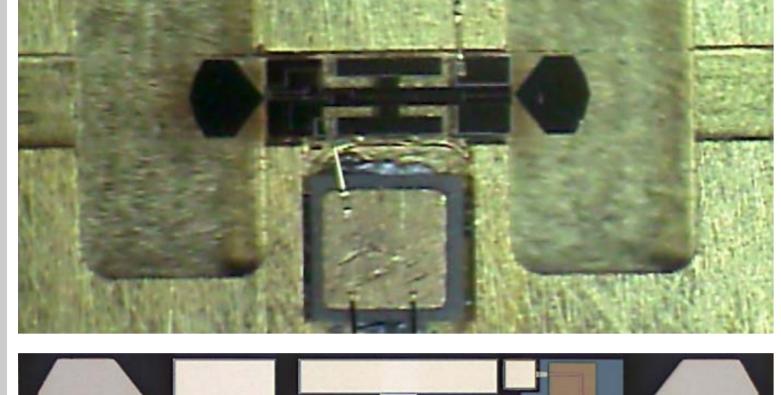


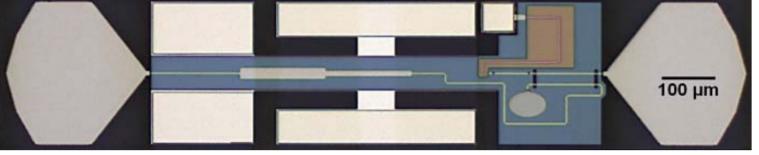
Advanced technologies for heterodyne radio astronomy instrumentation



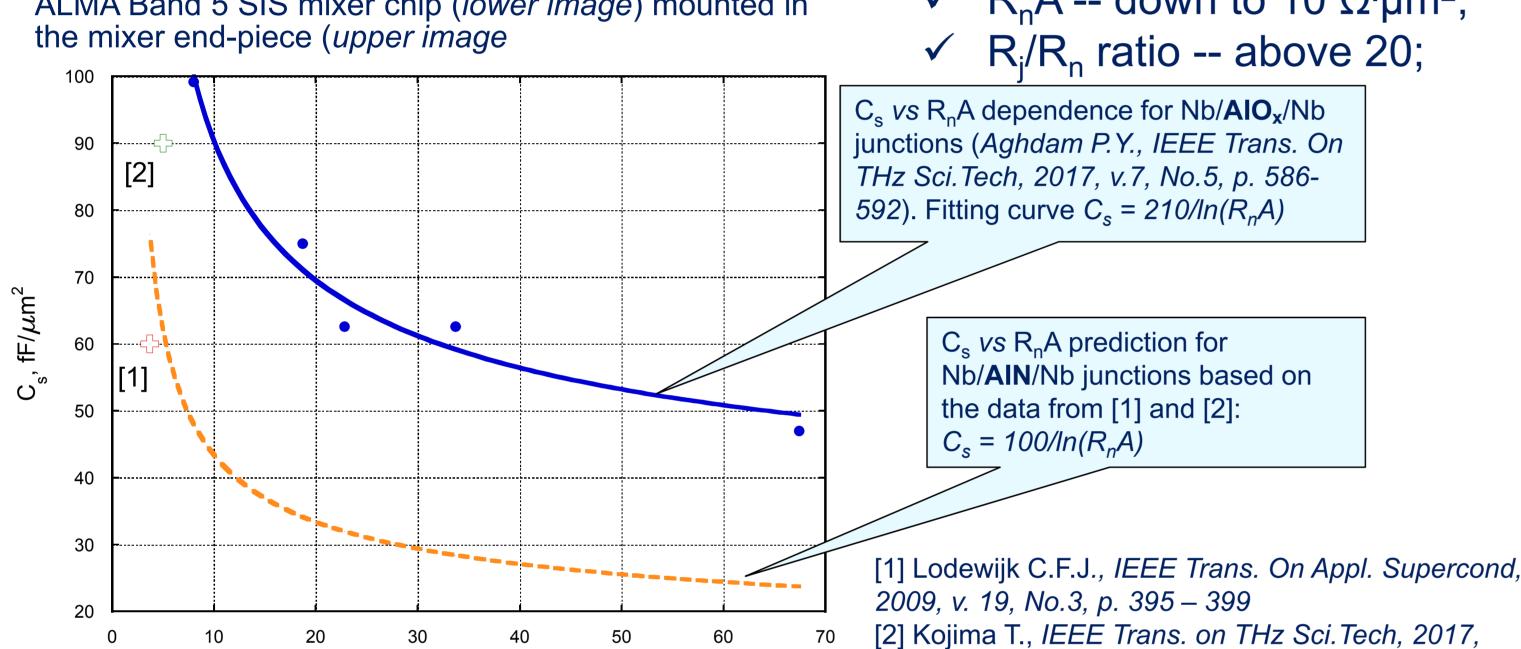
Thin Film Technology for SIS Mixers







ALMA Band 5 SIS mixer chip (lower image) mounted in



SIS processing summary

•Junctions:

• junction size -- down to 2µm²;

• Nb/**AIO**_x/Nb junctions:

 \checkmark R_nA -- down to 15 Ω ·µm²;

 \checkmark R_i/R_n ratio -- above 20;

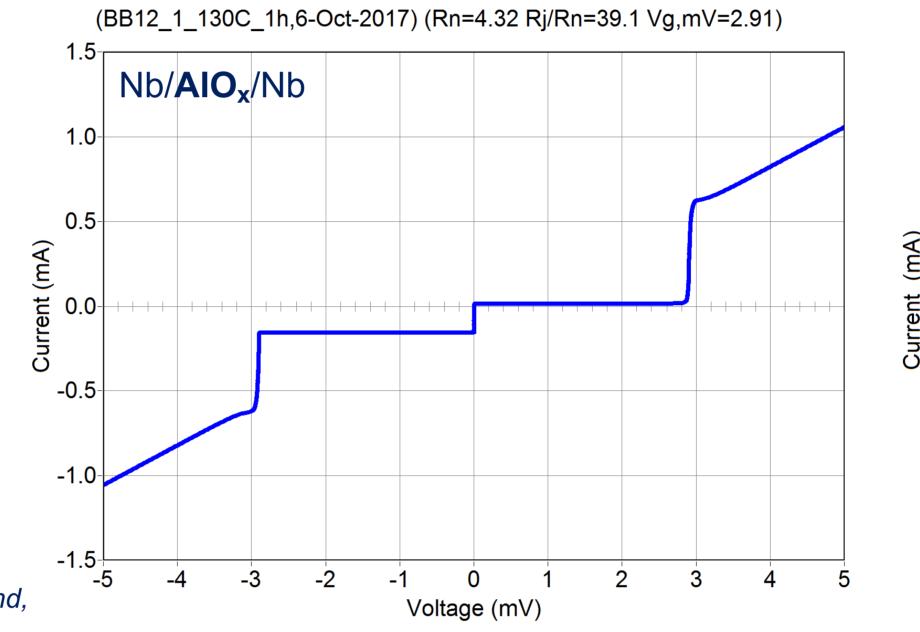
• (*new*) Nb/**AIN**/Nb junctions:

 \checkmark R_nA -- down to 10 Ω ·µm²;

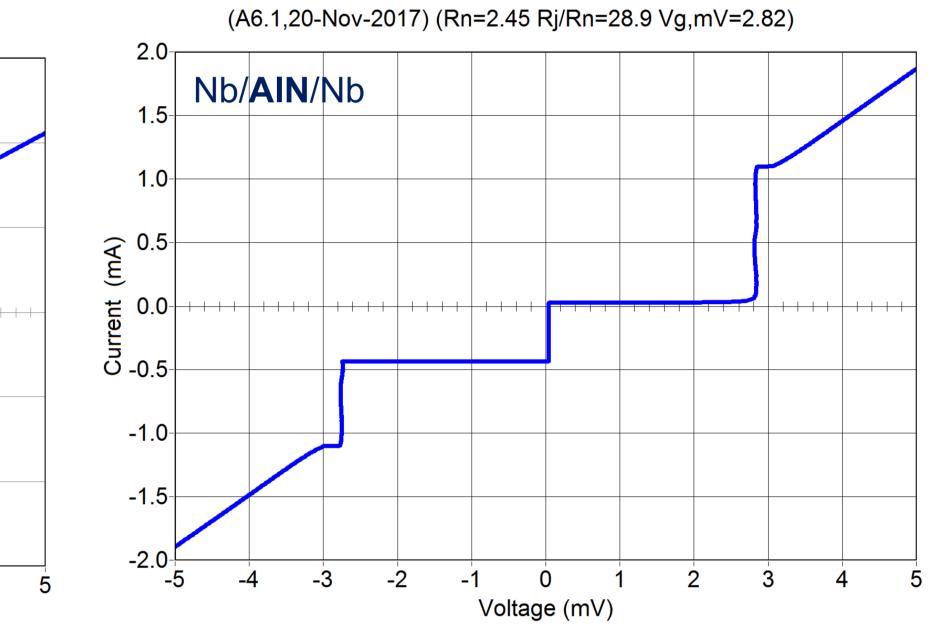
• Resistors: in the range $1...50\Omega/\Box$;

• Nb tuning circuitry:

 strip width -- down to 3µm; strip thickness -- up to 0.4µm





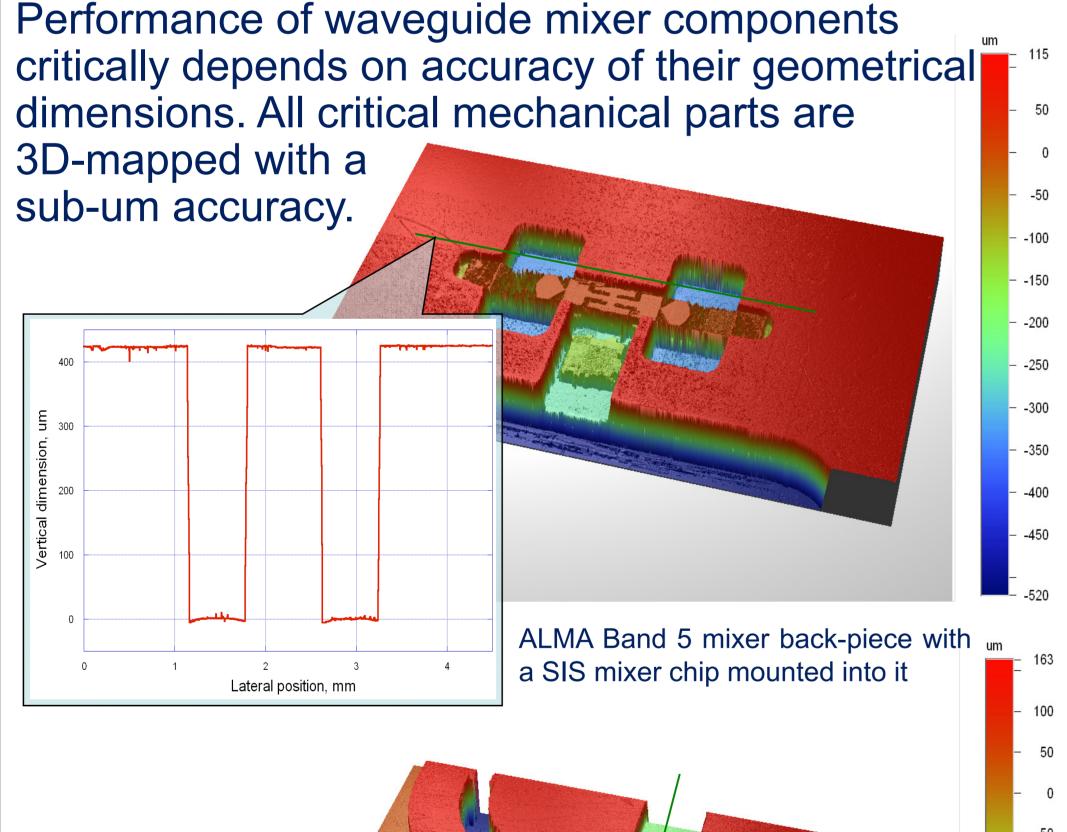


Typical current-voltage characteristics: (*left*) Band5 Nb/AlO_x/Nb twin junction, 3 μ m² each, ca. 25 Ω · μ m²; (*right*) Nb/AIN/Nb twin junction, $3 \mu m^2$ each, ca. $15 \Omega \mu m^2$

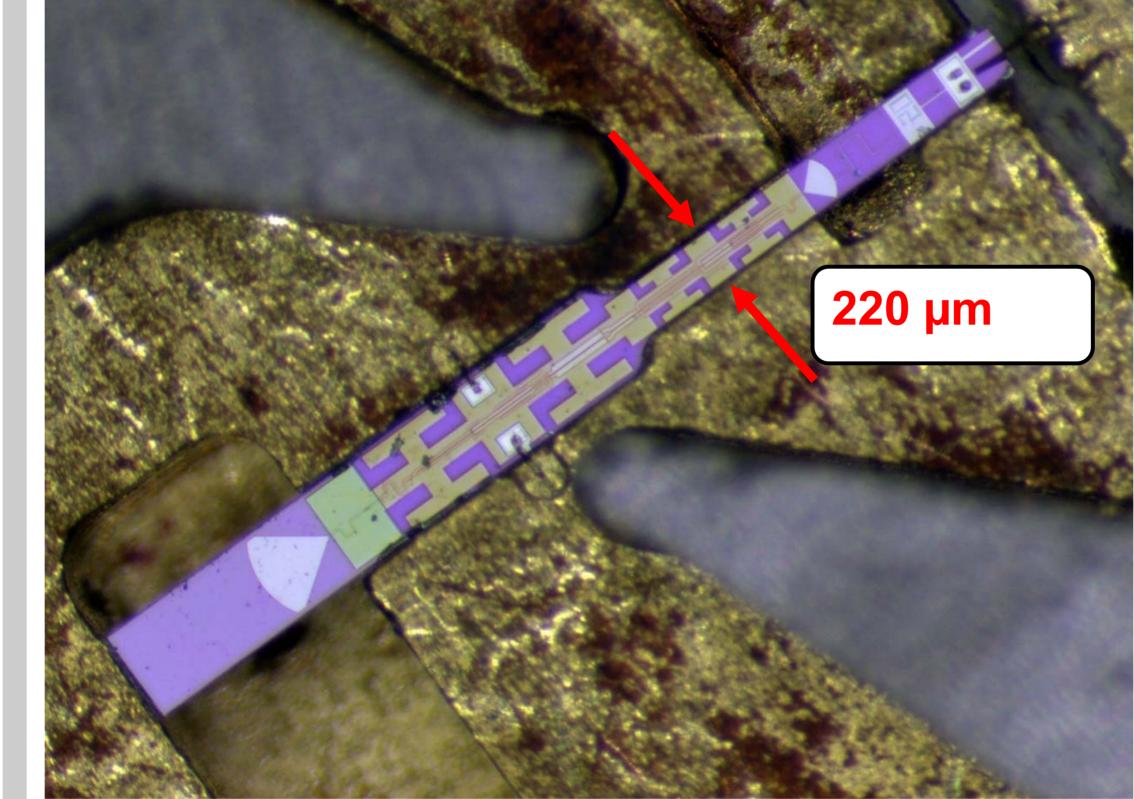
v.7, No.6, p. 694-703

3D Characterization of Mechanical Parts

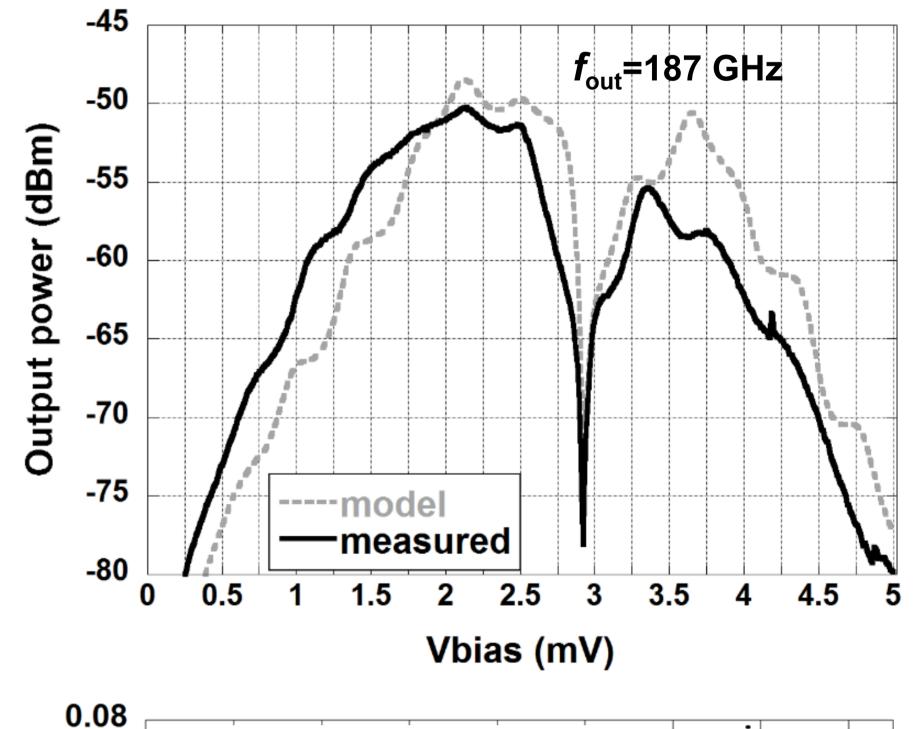
 $R_A, \Omega^* \mu m^2$

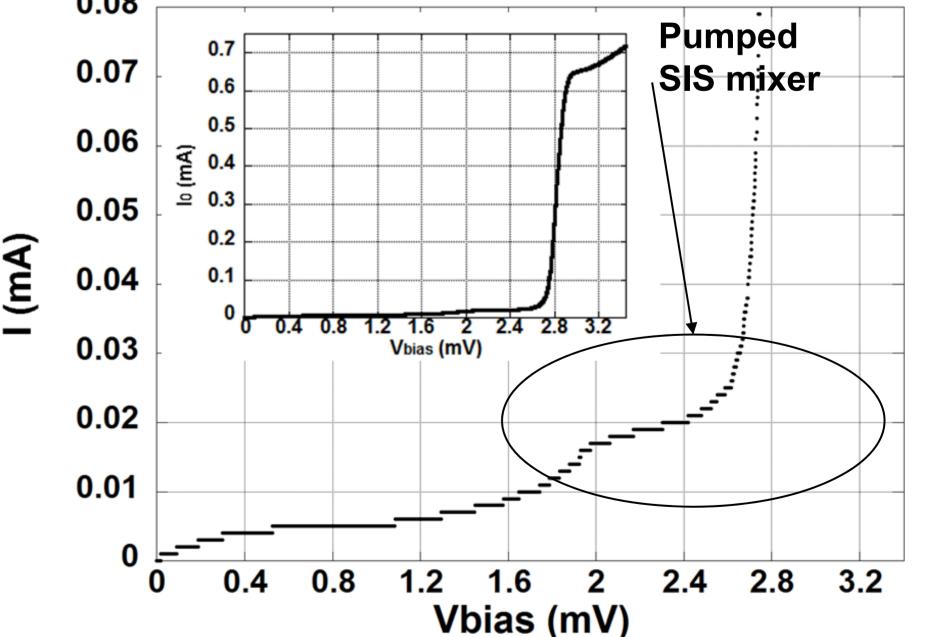


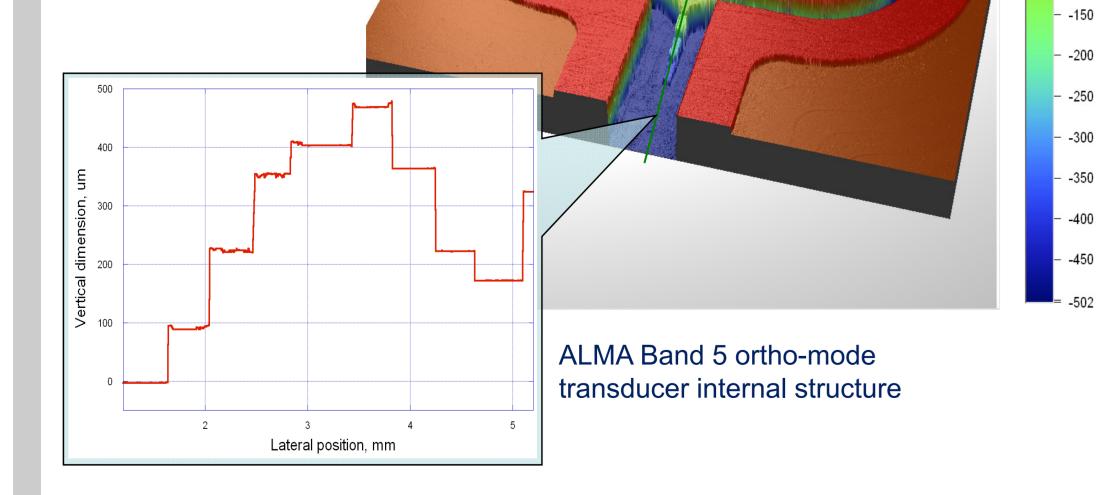
Superconducting frequency multiplier based on distributed SIS junction



Multiplier chip contains input and output waveguide to microstrip transitions, impedance transformers, input and







- output BP filters, power dividers and two distributed SIS
- junctions.
- The multiplication efficiency of the distributed SIS junction is 15-30 %
- The measured peak power of the generated signal at different frequencies is approximately constant over 10% fractional bandwidth.
- The frequency is able to pump an SIS mixer

Future: THz Waveguide Technology

GARD pioneered and further develops micromachining waveguide technology allowing submicron definition and surface quality (Rq \leq 30nm) of any complex structure within up to 3-layer technology. The technology has been successfully demonstrated over a wide frequency range: 385-500 GHz, 600-750 GHz and 1.29-1.35 THz. We see the potential for using this technology for up to 6 THz.

