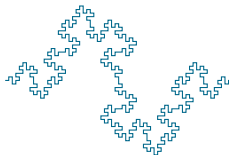


Influence of Slope Compensation on Operating Modes of Current Mode Controlled Converters

Marija Glišić, Predrag Pejović



INTRODUCTION

- ▶ current mode controlled converters
 - ▶ basic ones covered: buck, boost and buck-boost
- ▶ peak limiting technique
- ▶ nonlinear dynamics modeled, beyond linear model
- ▶ slope compensation (artificial ramp) influence analyzed
- ▶ discontinuous conduction modes focused

PEAK LIMITING CMC NONLINEAR DYNAMICS

- ▶ control variable is the peak value of the inductor current
- ▶ large signal behavior of interest
 - ▶ nonlinear (switching) effects included
 - ▶ limit cycle analyzed
 - ▶ open loop over output voltage, control variable kept constant
- ▶ different types of period-1 limit cycle instability observed
 - ▶ certain values of circuit parameters cause unstable period-1 limit cycle ...
 - ▶ ... which can result in multiplying period of initial orbit, period- n operation
 - ▶ ... or eventually lead to chaos
 - ▶ when and where?

PEAK LIMITING CMC NONLINEAR DYNAMICS

- ▶ continuous conduction mode
 - ▶ subharmonic oscillations occur for $D > 0.5$
- ▶ discontinuous conduction mode
 - ▶ orbits always periodic due to the interval of discontinuity
 - ▶ infinite number of periodic discontinuous modes, period- n
 - ▶ with very large period number n orbits resemble chaos
 - ▶ constant current loaded converters exhibit unstable period-1 limit cycle for $D > 0.5$
- ▶ slope compensation as a solution

SLOPE COMPENSATION

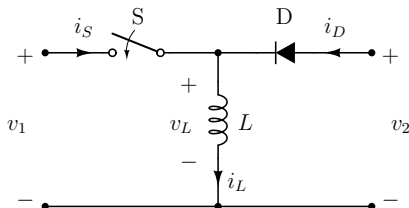
- ▶ artificial ramp signal with predefined slope
 - ▶ added to the switch current sense signal
 - ▶ aims to stabilize limit cycle to period-1 operation
- ▶ designed for continuous conduction mode
 - ▶ ... and could cause minor effects in discontinuous conduction mode ... ?
- ▶ reshaping trajectories
 - ▶ could change period number of periodic DCM orbit
 - ▶ and could push periodic DCM orbit into chaotic CCM

NONLINEAR DYNAMIC MODEL

- ▶ adequate model which can predict subharmonic behavior
 - ▶ iterative mapping, keeps track of discrete values
 - ▶ no averaging in the model core
 - ▶ simple enough, computer aided numerical simulation
 - ▶ frequency domain avoided
- ▶ predicts boundaries between different operating modes

SWITCHING CELL ITERATIVE MAP

- ▶ generalized model derived
 - ▶ covers all basic DC-DC converters
 - ▶ reduce to s switching cell as s common, essential part
 - ▶ identify inductor voltages, during charge (v_1) and discharge (v_2) phase



converter	Buck	Boost	Buck-Boost
v_1	$V_{IN} - V_{OUT}$	V_{IN}	V_{IN}
v_2	$-V_{OUT}$	$V_{IN} - V_{OUT}$	V_{OUT}

SWITCHING CELL ITERATIVE MAP

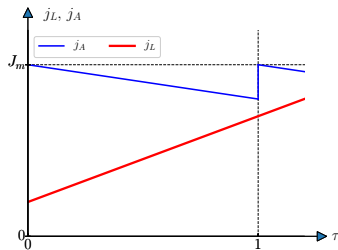
- ▶ normalized model
 - ▶ avoid units and make equations simpler
 - ▶ $m_X = \frac{v_X}{V_{IN}}$
 - ▶ $\tau = \frac{t}{T_s}$
 - ▶ $j_X = \frac{f_s L}{V_{IN}} i_X$
 - ▶ normalized inductor current charge and discharge slopes represented by normalized voltages
 - ▶ $m_L = \frac{dj_L}{d\tau}$

SWITCHING CELL ITERATIVE MAP

- ▶ identifies irregular (non-period-1) inductor current patterns
- ▶ three different possibilities over one cycle possible
 - ▶ (1) switch conducts during whole cycle
 - ▶ switch and diode conduct alternately
 - ▶ (2) no interval of discontinuity
 - ▶ (3) interval of discontinuity when both are off
- ▶ global conditions $m_1 > 0$, $m_2 < 0$, and $m_A < 0$
- ▶ constant current load assumed

SWITCHING CELL ITERATIVE MAP

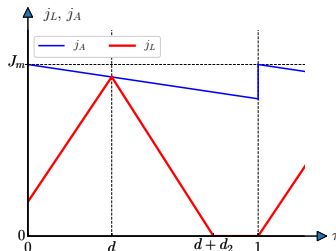
- switch conducts during the whole cycle



- $j_L(1) = j_L(0) + m_1$
 - under condition $j_L(0) < J_m + m_A - m_1$
 - inductor current does not reach the control peak value

SWITCHING CELL ITERATIVE MAP

- switch and diode conduct followed by a interval of discontinuity



- occurs when previous condition is not fulfilled
- $j_L(1) = 0 = J_m + m_A d + m_2 d_2$
 - diode conducts for $d_2 = -\frac{J_m + m_A d}{m_2}$
 - interval of discontinuity until the beginning of the next cycle

MODE BOUNDARIES

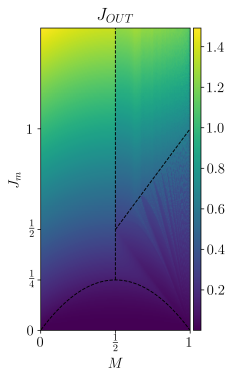
- ▶ boundary map
 - ▶ distinguishes continuous from discontinuous conduction mode
 - ▶ and stable limit cycle from unstable
- ▶ stable period-1 CCM operation
 - ▶ well known $\left| \frac{m_2 - m_A}{m_1 - m_A} \right| \leq 1$
- ▶ stable period-1 DCM operation
 - ▶ $i_L(1) = i_L(0) = 0$ condition
 - ▶ $J_m < \frac{m_2(m_1 - m_A)}{m_2 - m_1}$
- ▶ the rest is period- n ...
 - ▶ continuous with $n \rightarrow \infty$? discontinuous?

OUTPUT CURRENT MAP

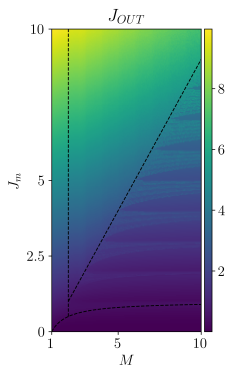
- ▶ dependence of the output current on the output voltage and the control variable
 - ▶ averaging introduced at this point
 - ▶ normalized output current related to averaged normalized inductor current
 - ▶ $\langle j_L \rangle = \langle j_S \rangle + \langle j_D \rangle$
 - ▶ $\langle j_S \rangle$ and $\langle j_D \rangle$ obtained from iterative map equations
 - ▶ (M, J_m) plane
 - ▶ m_A is fixed
 - ▶ numerical simulations performed

OUTPUT CURRENT MAP

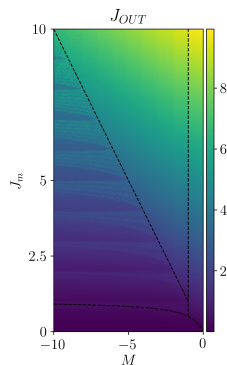
- ▶ normalized output current value in (M, J_m) plane
 - ▶ without artificial ramp



(a) Buck converter



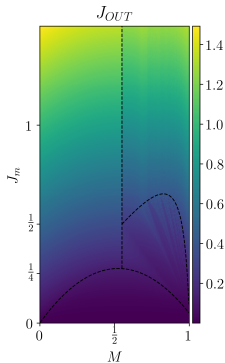
(b) Boost converter



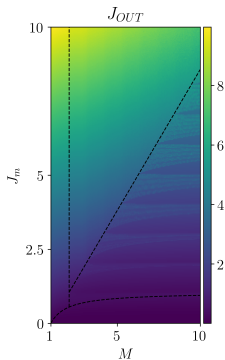
(c) Buck-boost converter

OUTPUT CURRENT MAP

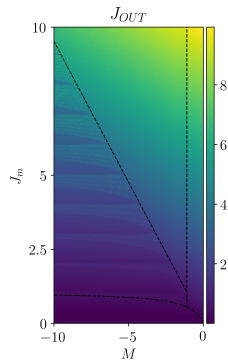
- ▶ normalized output current value in (M, J_m) plane
 - ▶ $m_A = -0.05$
 - ▶ matching with analytical mode boundaries
- ▶ DCM area has fractal like appearance



(a) Buck converter



(b) Boost converter



(c) Buck-boost converter

CONCLUSION

- ▶ peak limiting current mode control
- ▶ artificial ramp influence
- ▶ large signal behavior
- ▶ discontinuous conduction modes
- ▶ map of modes derived
- ▶ map of modes depends on m_A
- ▶ orbits could enter chaotic continuous conduction mode or large period number discontinuous conduction mode
- ▶ artificial ramp influence in discontinuous conduction mode should not be neglected